

Simulation using LTSPICE

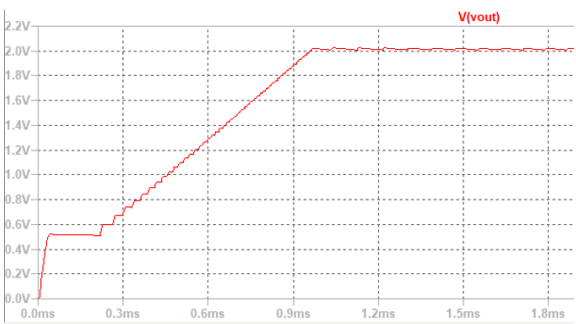
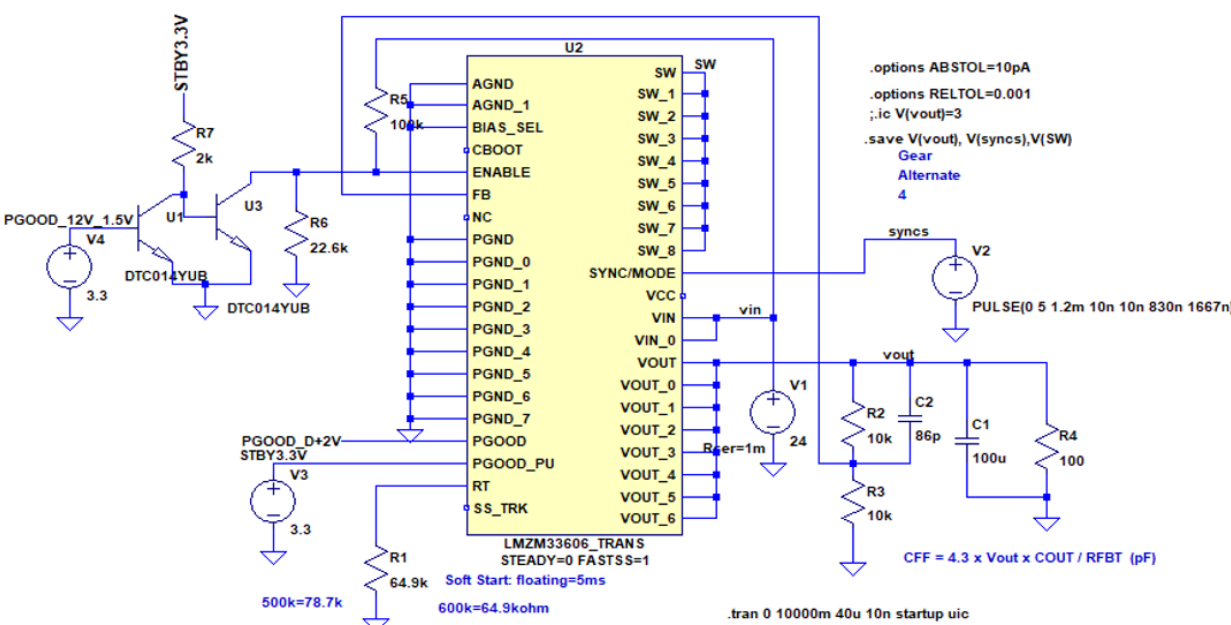
LMZM33606 Unencrypted PSpice Transient Model PSpice Model ZIP 2018年 6月 21日

Vin=24V, Vout=2V, Fsw=600kHz

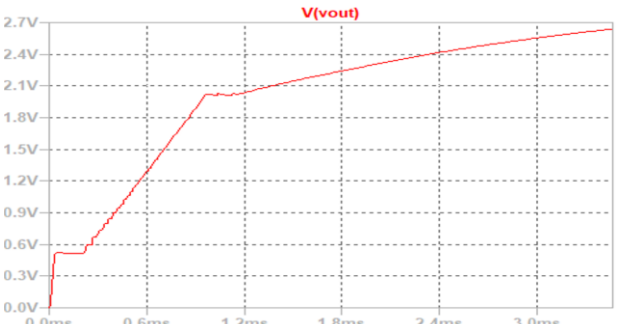
①

Customer corrected the blue frame at his own convenience.
As a result, some error messages occurred.
Is this a problem?

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* b. Current foldback
* c. Current limit, Hiccup
* d. PGOOD functionality
* e. MODE Pin functionality for FPWM
* 2. a. The operating quiescent current and thermal shutdown char
* b. Ground pins have been tied to 0V internally, therefore thi
*****
.SUBCKT LMZM33606_TRANS AGND AGND_1 BIAS_SEL CBOOT ENABLE FB NC PGN
+ PGND_1 PGND_2 PGND_3 PGND_4 PGND_5 PGND_6 PGND_7 PGOOD PGOOD_PU
+ SW_1 SW_2 SW_3 SW_4 SW_5 SW_6 SW_7 SW_8 SYNC/MODE VCC VIN VIN_0
+ VOUT_1 VOUT_2 VOUT_3 VOUT_4 VOUT_5 VOUT_6 PARAMS: STEADY=0 FAST
R_R20 PGND PGND_1 1m
R_R25 SW SW_6 0.001m
R_R21 SW SW_3 0.001m
* V V1 AGND PGND 0Vdc
R_R28 PGND PGND_7 1m
R_R27 SW SW_8 0.001m
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Frequency setting operation with resistor



External synchronization operation

②

Customer performed the simulation in state ①.
As a result, in the case of the external synchronization
operation, the output voltage has risen.
Please tell me the cause.