Fast response / 150mA Regulator IC

Monolithic IC MM3416

Outline

This IC is a 150 mA regulator capable of a fast transient response. It makes it possible to have a fast transient response together with an unloaded current consumption of 42µ A typ.

Moreover, through use of an ultra-small package of 1 mm/ , the device makes its contribution to the downsized specification required in mobile devices.

Features

- 1. Fast response
- 2. Built in soft-start
- 3. Ultra-small package PLP-4A

Drop of 120 mV on current rise of 0→150mA High ripple rejection ration of 75dB typ. Reduced rush current at startup Contributes to downsized specification

Package

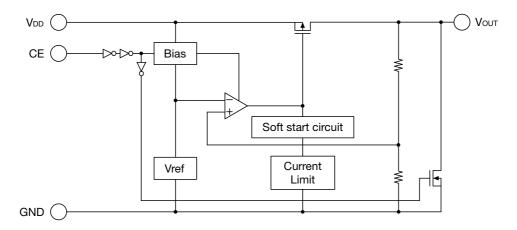
SOT-25A SC-82ABB PLP-4A

Applications

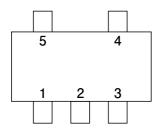
- 1. Mobile phones
- 2. Digital still cameras
- Camcorders etc.

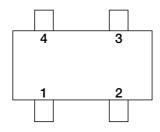
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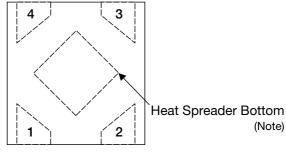
Block Diagram



Pin Assignment







SOT-25A (TOP VIEW)

2

3

4

5

 V_{DD}

GND

CE

NC

 V_{OUT}

	_

SC-82ABB (TOP VIEW)

1	CE
2	GND
3	$ m V_{OUT}$
4	$ m V_{DD}$

PLP-4A (TOP VIEW)

1	Vout
2	GND
3	CE
4	$ m V_{DD}$

Note: Heat Spreader Bottom with GND

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Pin Description

SOT-25A

Pin No.	Pin name	Functions				
1	V_{DD}	Voltage-sup	ply pin			
2	GND	GND pin				
3	CE	ON/OFF-C CE Low High Connect CE when it is no	OUTPUT OFF ON pin with V _{DD} pin,			
4	NC	No connection				
5	Vout	Output pin				

SC-82ABB

Pin No.	Pin name	Functions					
1	CE	ON/OFF-C CE Low High Connect CE when it is no	OUTPUT OFF ON pin with VDD pin,				
2	GND	GND pin					
3	Vout	Output pin					
4	$V_{ m DD}$	Voltage-supply pin					

PLP-4A

Pin No.	Pin name	Functions				
1	Vout	Output pin				
2	GND	GND pin				
3	CE	ON/OFF-C CE Low High Connect CE when it is no	OUTPUT OFF ON pin with VDD pin,			
4	V_{DD}	Voltage-sup	oply pin			

Absolute Maximum Ratings (Except where noted otherwise Ta=25°C)

Item	Symbol	Ratings		Units				
Storage Temperature	Tstg	-55~+150		°C				
Supply Voltage	$ m V_{DD}$	6.5		6.5		6.5		V
CE Input Voltage	Vce	6.5		6.5		V		
Output Current	Iout	200		mA				
		350(Note1) SOT-25A						
Power Dissipation	Pd	330(Note2)	SC-82ABB	mW				
		400(Note3) PLP-4A						

Note1: With the double sided PC Board of glass epoxy

 $(60 \times 40 \times 1.6^{t} \text{mm})$

Note2: With the double sided PC Board of glass epoxy

 $(100 \times 40 \times 0.8^{t} \text{mm})$

Note3: With the double sided PC Board of glass epoxy

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Recommended Operating Conditions (Except where noted otherwise Ta=25°C)

Item	Symbol	Ratings	Units
Operating Ambient Temperature	Tjop	-40~+85	°C
Operating Voltage	$V_{ m DDOP}$	1.7~5.5	V
Output Current	Iout	0~150	mA

Electrical Characteristics 1 (Except where noted otherwise Ta=25°C, VDD=VOUT (typ.)+1V, VCE=VDD)

Item	Symbol	Measurement conditions	Min.	Тур.	Max.	Units
Input Current (OFF)	IDDoff	V _{CE} =0V		0.1	1	μA
No-load Input Current	Idd	Iout=0mA		42	64	μA
Output Voltage (Note4)	Vout	Iour=10mA	×0.99 (-20mV)		×1.01 (20mV)	V
Line Regulation	VLINE	Iout=1mA Vout+0.5V≦Vdd≤5V		0.05	0.10	%/V
Load Regulation	VLOAD	1mA≤Iour≤150mA			40	mV
Dropout Voltage (Note4)	Vio	I _{OUT} =150mA				V
Output Short-Circuit Current (Note5)	Ilim	Vout=0V		50		mA
Vouт Temperature Coefficient (Note5)	△VOUT/△TOP	Iout=10mA -40°C≦Top≤85°C		±100		ppm/°C
Ripple Rejection (Note5)	RR	Vripple=0.5V, Iout=30mA f=1kHz		75		dB
Output Noise Voltage (Note5)	Vn	$I_{OUT}=30mA$ fbw=10 \sim 100kHz		60		μVrms
CE Pin Current (Note5)	Ice			0.5		μA
CE High Threshold Voltage	VCEH		1.5		V_{DD}	V
CE Low Threshold Voltage	VCEL		0		0.3	V
CE pin Transient Response (Note5)	tce	I _{OUT} =50mA		30		μs
Output NMOS ON Resistance (Note5)	Rdon	$V_{CE}=0V, V_{DD}=4V$		20		Ω

Note4: Please refer to another page.

Note5: The parameter is guaranteed by design.

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Electrical Characteristics 2 (Except where noted otherwise Ta=25°C, VDD=VOUT (typ.)+1V, VCE=VDD)

				lte	em			
NAI -I NI -	Output Voltage				Dropout Voltage (Note6)			
Model No.		Vout (V)				Vio (V)		
	Measurement Conditions	Min.	Тур.	Max.	Measurement Conditions	Min.	Тур.	Max.
MM3416A10		0.980	1.000	1.020				
MM3416Z10		1.030	1.050	1.070	I 150 A			
MM3416A11		1.080	1.100	1.120	Iout=150mA 1.0V≦Vout<1.3V		0.62	0.70
MM3416Z11		1.130	1.150	1.170			0.63	0.70
MM3416A12		1.180	1.200	1.220	(Note6)			
MM3416Z12		1.230	1.250	1.270				
MM3416A13		1.280	1.300	1.320				
MM3416Z13		1.330	1.350	1.370	Iout=150mA			
MM3416A14		1.380	1.400	1.420	1.3V≦Vout<1.6V		0.58	0.65
MM3416Z14		1.430	1.450	1.470	$\begin{array}{c} 1.3 \checkmark \leq \checkmark 0.01 < 1.0 \checkmark \\ \text{(Note6)} \end{array}$		0.36	0.03
MM3416A15	[1.485	1.500	1.515	(110160)			
MM3416Z15] [1.535	1.550	1.566				
MM3416A16	[1.584	1.600	1.616				
MM3416Z16		1.634	1.650	1.667	Iout=150mA			
MM3416A17		1.683	1.700	1.717	1.6V≤V _{OUT} <1.9V		0.53	0.60
MM3416Z17		1.733	1.750	1.768	$V_{DD}=V_{OUT}(typ)-0.2V$		0.00	0.00
MM3416A18	_	1.782	1.800	1.818	VDD-VO01(typ)-0.2V			
MM3416Z18		1.832	1.850	1.869				
MM3416A19		1.881	1.900	1.919	Iout=150mA - 1.9V≦Vout<2.1V - VDD=Vout(typ)-0.2V		0.41	0.48
MM3416Z19		1.931	1.950	1.970				
MM3416A20	I _{OUT} =10mA	1.980	2.000	2.020				0.40
MM3416Z20		2.030	2.050	2.071				
MM3416A21		2.079	2.100	2.121				
MM3416Z21	_	2.129	2.150	2.172	-			
MM3416A22		2.178	2.200	2.222				
MM3416Z22		2.228	2.250	2.273	I _{OUT} =150mA			
MM3416A23	-	2.277	2.300	2.323	2.1V≦Vout<2.6V		0.28	0.36
MM3416Z23	_	2.327	2.350	2.374	VDD=VOUT(typ)-0.2V		0.20	0.00
MM3416A24	_	2.376	2.400	2.424	- VDD-VOOT(typ) 0.2V			
MM3416Z24		2.426	2.450	2.475	-			
MM3416A25	-	2.475	2.500	2.525				
MM3416Z25	-	2.525	2.550	2.576				
MM3416A26	-	2.574	2.600	2.626	_			
MM3416Z26		2.624	2.650	2.677	_			
MM3416A27		2.673	2.700	2.727	_			
MM3416Z27	-	2.723	2.750	2.778	I _{OUT} =150mA			
MM3416A28		2.772	2.800	2.828	2.6V≦V _{OUT} <3.1V		0.22	0.30
MM3416Z28		2.822	2.850	2.879	V _{DD} =V _{OUT} (typ)-0.2V			
MM3416A29		2.871	2.900	2.929				
MM3416Z29		2.921	2.950	2.980	_			
MM3416A30	-	2.970	3.000	3.030	-			
MM3416Z30		3.020	3.050	3.081				

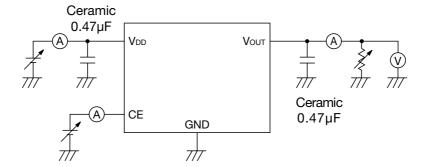
Note6: Dropout voltage MAX value in the input and it is confirmed that there is no output abnormal voltage impression the load 150mA in the model $V_{OUT} < 1.6V$.

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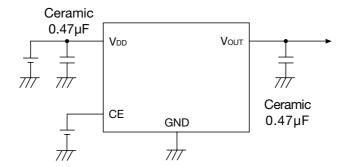
	Item								
Model No.	Out	put Volta	.ge		Dropout Voltage				
Model No.		V оит (V)			Vio (V)				
	Measurement Conditions	Min.	Тур.	Max.	Measurement Conditions	Min.	Тур.	Max.	
MM3416A31		3.069	3.100	3.131					
MM3416Z31		3.119	3.150	3.182					
MM3416A32		3.168	3.200	3.232					
MM3416Z32		3.218	3.250	3.283					
MM3416A33		3.267	3.300	3.333					
MM3416Z33		3.317	3.350	3.384					
MM3416A34		3.366	3.400	3.434					
MM3416Z34		3.416	3.450	3.485					
MM3416A35		3.465	3.500	3.535	Iout=150mA				
MM3416Z35	Iout=10mA	3.515	3.550	3.586	3.1V≦Vout		0.21	0.27	
MM3416A36		3.564	3.600	3.636	VDD=VOUT(typ)-0.2V				
MM3416Z36		3.614	3.650	3.687					
MM3416A37		3.663	3.700	3.737					
MM3416Z37		3.713	3.750	3.788					
MM3416A38		3.762	3.800	3.838					
MM3416Z38		3.812	3.850	3.889					
MM3416A39		3.861	3.900	3.939					
MM3416Z39		3.911	3.950	3.990					
MM3416A40		3.960	4.000	4.040					

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Measuring Circuit



Application Circuit



★ Temperature Characteristics : B

(reference example of external parts)

· Output capacitor Ceramic capacitor 0.47µF · Input Capacitor Ceramic capacitor 0.47µF

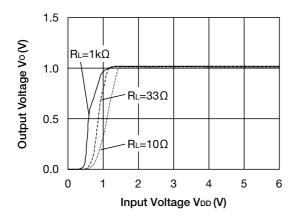
- · Note
- 1. The output capacitor is required between output and GND to prevent oscillation.
- 2. The ESR of capacitor must be defined in ESR stability area. It is possible to use a ceramic capacitor without ESR resistance for output. The ceramic capacitor must be used more than 0.47µF and B temperature characteristics.
- 3. The wire of VDD and GND is required to print full ground plane for noise and stability.
- 4. The input capacitor must be connected a distance of less than 1cm from input pin.
- 5. In case the output voltage is above the input voltage, the overcurrent flow by internal parastic diode from output to input.

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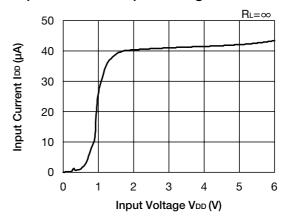
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Characteristics (Vo=1.0V) (Except where noted otherwise Ta=25°C, Vdd=Vout (typ.) +1V, VcE=Vdd)

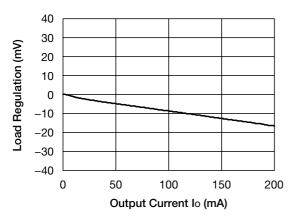
Output Voltage - Input Voltage



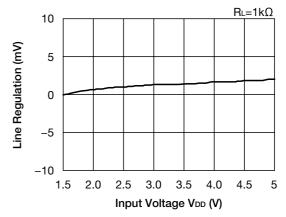
Input Current - Input Voltage



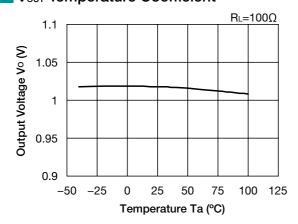
Load Regulation



Line Regulation

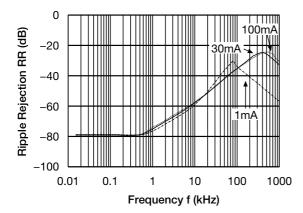


■ V_{OUT} Temperature Coefficient

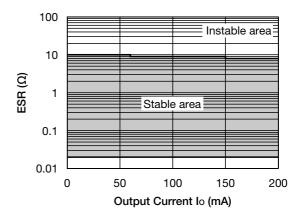


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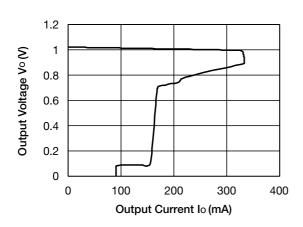
Ripple Rejection



ESR stable area



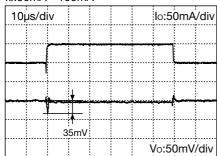
Current Limit



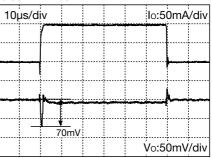
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■ Load Transient Response (VDD=Vo+1V, VCE=VDD, Cin=Co=0.47µF)

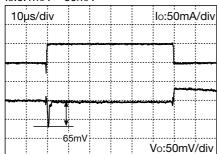
lo:50mA⇔100mA



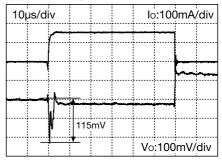
lo:50mA⇔150mA



lo:0.1mA ⇔50mA

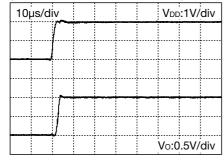


lo:0.1mA ⇔ 150mA



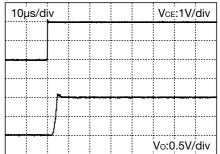
Input rise characteristics

 $(V_{DD}=0V \rightarrow 2.0V, V_{CE}=V_{DD}, I_{O}=50mA)$



CE rise characteristics

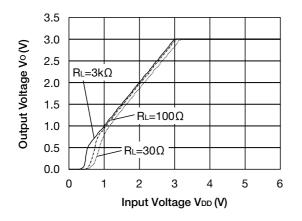
 $(V_{DD}=2.0V, V_{CE}=0V \rightarrow V_{DD}, I_{O}=50mA)$



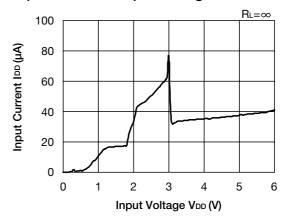
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Characteristics (Vo=3.0V) (Except where noted otherwise Ta=25°C, Vdd=Vdut (typ.) +1V, VcE=Vdd)

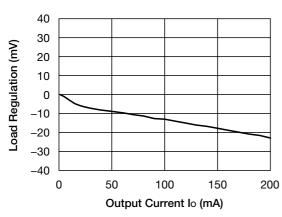
Output Voltage - Input Voltage



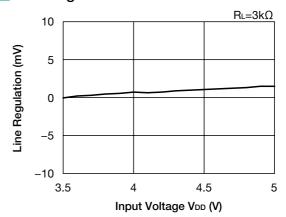
Input Current - Input Voltage



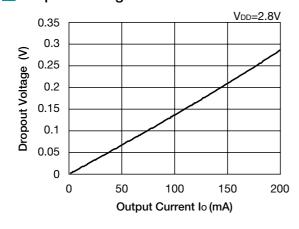
Load Regulation



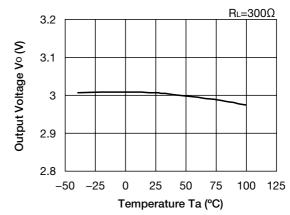
Line Regulation



Dropout Voltage

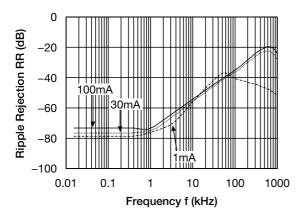


VOUT Temperature Coefficient

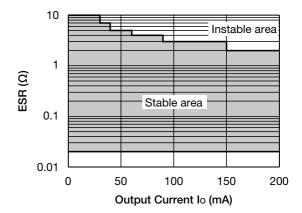


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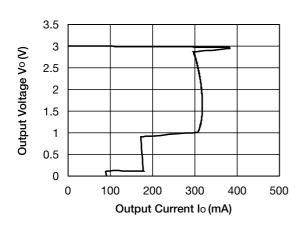
Ripple Rejection



ESR stable area



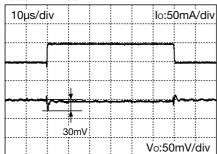
Current Limit



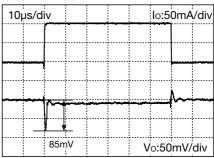
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■ Load Transient Response (VDD=Vo+1V, VCE=VDD, Cin=Co=0.47µF)

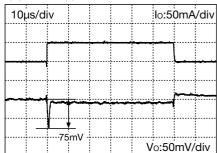
lo:50mA ⇔100mA



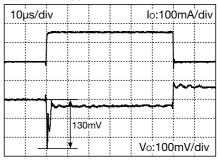
lo:50mA ⇔150mA



lo:0.1mA ⇔50mA

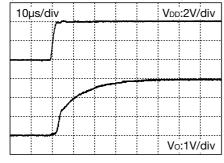


lo:0.1mA ⇔150mA



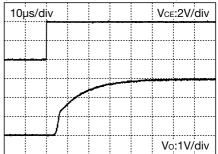
Input rise characteristics

 $(V_{DD}=0V \rightarrow 4.0V, V_{CE}=V_{DD}, I_{O}=50mA)$



CE rise characteristics

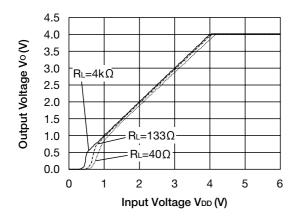
 $(V_{DD}=4.0V,V_{CE}=0V \rightarrow V_{DD},I_{O}=50mA)$



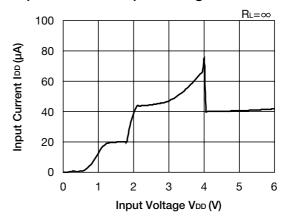
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Characteristics (Vo=4.0V) (Except where noted otherwise Ta=25°C, Vdd=Vdu (typ.) +1V, VcE=Vdd)

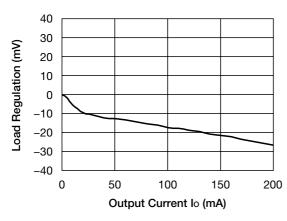
Output Voltage - Input Voltage



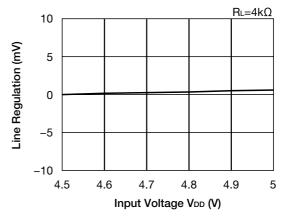
Input Current - Input Voltage



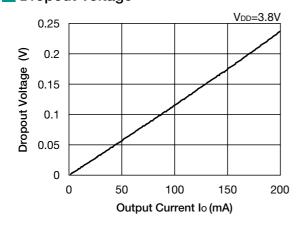
Load Regulation



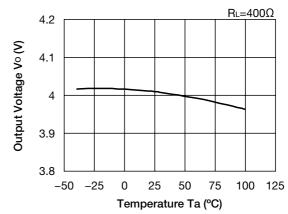
Line Regulation



Dropout Voltage

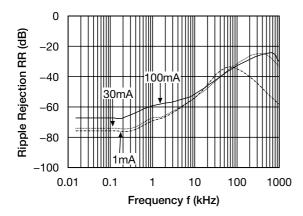


VOUT Temperature Coefficient

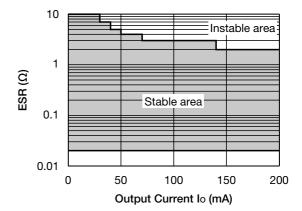


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 The details listed here are not a guarantee of the individual products at the time of ordering. When using the products, you will be asked to check their specifications.

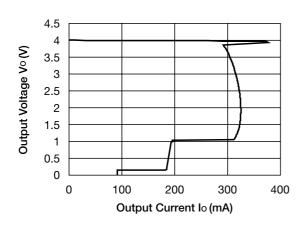
Ripple Rejection



ESR stable area



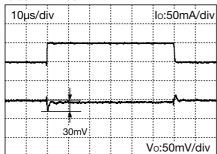
Current Limit



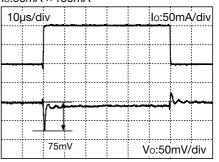
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Load Transient Response (VDD=Vo+1V, VCE=VDD, Cin=Co=0.47µF)

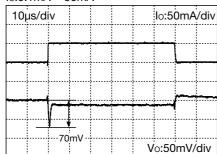
lo:50mA ⇔100mA



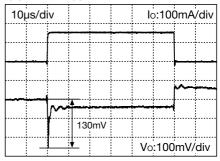
lo:50mA⇔150mA



lo:0.1mA ⇔50mA

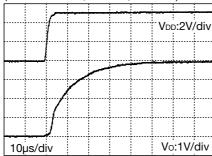


lo:0.1mA ⇔ 150mA



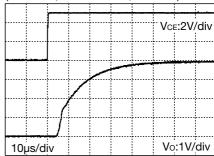
Input rise characteristics

 $(V_{DD}=0V \rightarrow 5.0V, V_{CE}=V_{DD}, I_{O}=50mA)$



CE rise characteristics

 $(V_{DD}=5.0V, V_{CE}=0V \rightarrow V_{DD}, I_{O}=50mA)$



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