



UCC39002 Load Share  
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Power Supply Control Products

## ENTER VARIABLES IN THE YELLOW AREAS; CALCULATED RESULTS ARE IN PINK AREAS

In the following example, two 3.3V, 10A modules are to be paralleled. The modules' remote sense has an adjustment range of 5% of the output voltage. The modules have an internal resistor between +Vout and Sense,  $R_{\text{sense}}$ , measured to be 200 $\Omega$ . The modules have a measured band width of 2.4kHz,  $f_{\text{comod}}$ .

$N_{\text{units}}$  is the number of paralleled units

$$N_{\text{units}} := 2$$

Useful identities

$$m\Omega := 10^{-3} \cdot \Omega$$

$$mW := 10^{-3} \cdot W$$

$$S := \frac{1}{\Omega}$$

### STEP 1: Characterize the power module to be paralleled.

measured crossover frequency of the module. **Measure the BODE PLOT of the module as described in the UCC29002 data sheet**

$$f_{\text{comod}} := 2.4 \cdot \text{kHz}$$

maximum output current of the module

$$I_{\text{outmax}} := 14.8 \cdot A$$

output voltage of the module

$$V_{\text{out}} := 54 \cdot V$$

maximum voltage adjustment of the module by way of the remote sense, usually given as a percentage in the module data sheet

$$\Delta V_{\text{outadjmax}} := 0.05 \cdot V_{\text{out}}$$

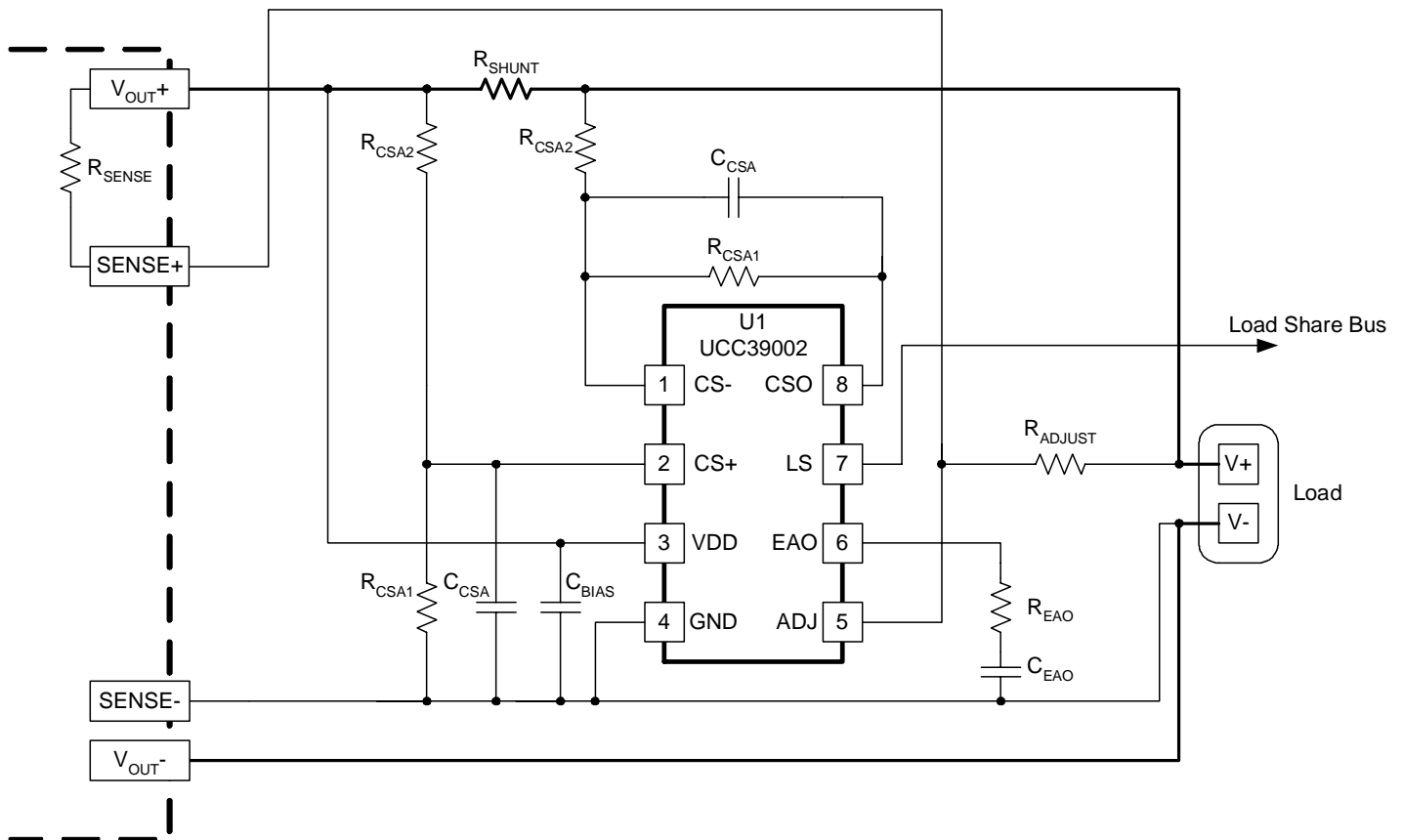
$$\Delta V_{\text{outadjmax}} = 2.7 \cdot V$$

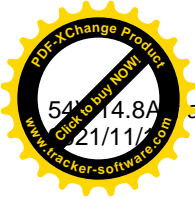
VDD input of the UCC39002

$$V_{\text{DD}} := 12 \cdot V$$

Measured internal resistance between VOUT+ and SENSE+,  $R_{sense}$

$$R_{sense} := 200 \cdot \Omega$$





## STEP 2: Determine the Current Sense Resistor

Desired maximum power dissipation of the current sense resistor

$$P_{Rshuntmax} := 0.5 \cdot W$$

calculated maximum value of the current sense resistor,  $R_{shunt}$ , for the given allowable power dissipation

$$R_{shuntmax} := \frac{P_{Rshuntmax}}{I_{outmax}^2}$$

$$R_{shuntmax} = 2.283 \cdot m\Omega$$

standard resistor value used for current sensing

$$R_{shunt} := 0.16 \cdot m\Omega$$

calculated power dissipation of given sense resistor

$$P_{Rshunt} := R_{shunt} \cdot I_{outmax}^2$$

$$P_{Rshunt} = 0.035 \text{ W}$$

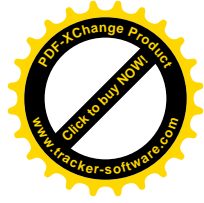
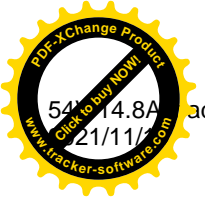
voltage drop across sense resistor (make sure it is less than the voltage adjustment range of the module and is much greater than the input voltage offset,  $100\mu V$ , of the current sense amplifier)

$$V_{Rshunt} := I_{outmax} \cdot R_{shunt}$$

$$V_{Rshunt} = 2.368 \cdot mV$$

$$\Delta V_{outadjmax} = 2.7 \cdot V$$

although the drop across the current sense resistor is less than the usable voltage adjustment range, it is a significant portion of it.



### STEP 3: Set up the Current Sense Amplifier

Internal resistor at the LS pin

$$R_{LS} := 100 \cdot k\Omega \quad \text{内部阻抗 } 100K$$

absolute max current sense amplifier output voltage before saturation, the master's  $V_{CSA}$  determines the voltage of the load share bus

$$V_{CSAo\_max} := V_{DD} - 2 \cdot V$$

$$V_{CSAo\_max} = 10 \text{ V}$$

The output current of the LS driver is limited to 1 mA, this relates to the maximum number of modules,  $N_{units}$ , to be paralleled to the LS voltage,  $N_{units\_max}$ :

$$I_{LSmax} := 1 \cdot \text{mA}$$

$$V_{LSmax} := V_{DD} - 1.7 \cdot V$$

$$N_{units\_max} := \text{floor} \left( \frac{R_{LS} \cdot I_{LSmax}}{V_{LSmax}} \right)$$

$$N_{units\_max} = 9$$

because  $V_{LS}$  is approximately equal to  $V_{CSA}$  this just determines whether the number of modules in parallel exceeds the current capability of the LS driver; so long as  $V_{CSO}$  is less than  $V_{LSmax}$  than everything is cool. Each slave module represents a load to the master based on the LS voltage and the 100kΩ internal resistor

$$I_{masterincreasemax} := N_{units} \cdot \left( \frac{V_{LSmax}}{R_{LS}} \right)$$

$$I_{masterincreasemax} = 0.206 \cdot \text{mA}$$

$$P_{masterincrease} := V_{DD} \cdot I_{masterincreasemax}$$

$$P_{masterincrease} = 2.472 \cdot \text{mW}$$

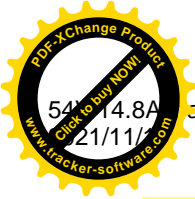
just keep this in mind for master IC bias current

the absolute allowable current sense gain before saturation,  $A_{CSAmax}$

$$A_{CSAmax} := \frac{V_{CSAo\_max}}{R_{shunt} \cdot I_{outmax}}$$

$$A_{CSAmax} = 4.223 \times 10^3$$

chosen current sense gain for this design, keeping well below the theoretical maximum calculated above:



$$A_{CSA} := 100$$

$$V_{CSAo} := A_{CSA} \cdot R_{shunt} \cdot I_{outmax}$$

$$V_{CSAo} = 0.237 \text{ V}$$

Gain set by Rcsa1 (parallel) and Rcsa2 (series)

$$R_{csa2} := 0.15 \cdot k\Omega$$

$$R_{csa1} := 15 \cdot k\Omega$$

$$A_{CSA\_actual} := \frac{R_{csa1}}{R_{csa2}}$$

resultant current sense amplifier output voltage and approximate LS bus voltage

$$A_{CSA\_actual} = 100$$

$$V_{CSAo\_actual} := A_{CSA\_actual} \cdot R_{shunt} \cdot I_{outmax}$$

$$V_{CSAo\_actual} = 0.237 \text{ V}$$

add a "high frequency" pole for noise roll off:

$$f_{pole} := 10 \cdot kHz$$

$$C_{CSA} := \frac{1}{2 \cdot \pi \cdot R_{csa1} \cdot f_{pole}}$$

$$C_{CSA} = 1.061 \cdot nF$$

Actual value of Ccsa:

$$C_{CSA\_actual} := 1000 \cdot pF$$

Resultant pole

$$f_{pole\_actual} := \frac{1}{2 \cdot \pi \cdot R_{csa1} \cdot C_{CSA\_actual}}$$

$$f_{pole\_actual} = 10.61 \cdot kHz$$

these compensation components must be on both input terminals of the differential amplifier

#### STEP 4: Determine the adjust resistor:

First measure the internal resistance between  $+V_{out}$  and  $+Sense$ .

$$R_{sense} = 200 \Omega$$

If we consider the voltage differential between  $+V_{out}$  and  $+Sense$  to be equal to the voltage adjustment range of the converter:

$$\Delta V_{outadjmax} = I_{sense} \cdot R_{sense}$$

and the adjustment range has been limited to:

$$\Delta V_{outadjmax} = 2.7 V$$

Solve for  $I_{sense}$

$$I_{sense} := \frac{\Delta V_{outadjmax}}{R_{sense}}$$

$$I_{sense} = 13.5 \text{ mA}$$

Because this internal resistor is essentially in parallel with the ADJ resistor, the ADJ amplifier current will be the sum of this  $I_{sense}$  and  $I_{Radj}$

Internally the adjust pin is clamped to 3V and has a 500 Ohm emitter resistor which results in a maximum sink current at the adjust pin (data sheet functional block diagram shows 3V and 500Ω)

$$I_{adjmax} := \frac{3 \cdot V}{500 \cdot \Omega}$$

$$I_{adjmax} = 6 \text{ mA}$$

Because  $V_{adj}$  must be greater than or equal to  $V_{eao} + 1V$ , in order to keep the BJT on the adj from saturating,  $R_{adj}$  must be carefully selected. We know the master  $V_{eao}$  is 0V, therefore, since Adj is connected to the load bus through a resistor,  $V_{adj}$  is approximately equal to  $V_{BUS}$  and if  $V_{adj}$  must be  $V_{eao} + 1V$  or greater, then the output bus voltage is limited to 1V minimum. Account for forward drop of oring diode if used.

$$V_{BUS} = V_{out} - I_{out} \cdot R_{shunt}$$

$$\Delta V_{outadjmax} = I_{sense} \cdot R_{sense}$$

$$I_{adj} = I_{sense} + I_{Radj}$$

$$I_{Radj} = I_{adj} - I_{sense}$$

$$I_{sense} \cdot R_{sense} = I_{out} \cdot R_{shunt} + I_{Radj} \cdot R_{adj}$$

$$I_{sense} \cdot R_{sense} = I_{out} \cdot R_{shunt} + (I_{adj} - I_{sense}) \cdot R_{adj}$$



Solve for  $I_{adj}$ :

$$I_{adj} = \frac{I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt} + I_{sense} \cdot R_{adj}}{R_{adj}}$$

We know that  $V_{eao}$  will be approximately equal to:

$$V_{eao} = I_{adj} \cdot 500 \cdot \Omega$$

And the voltage on  $V_{adj}$  is equal to:

$$V_{adj} = V_{BUS} - R_{adj} \cdot I_{Radj}$$

$$V_{adj} = V_{out} - I_{out} \cdot R_{shunt} - R_{adj} \cdot I_{Radj}$$

And to keep the Adj BJT from saturating:

$$V_{adj} \geq V_{eao} + 1 \cdot V$$

Substituting leads to:

$$V_{out} - I_{out} \cdot R_{shunt} - R_{adj} \cdot I_{Radj} \geq I_{adj} \cdot 500 \cdot \Omega + 1 \cdot V$$

$$V_{out} - I_{out} \cdot R_{shunt} - R_{adj} \cdot (I_{adj} - I_{sense}) \geq I_{adj} \cdot 500 \cdot \Omega + 1 \cdot V$$

$$V_{out} - I_{out} \cdot R_{shunt} - R_{adj} \cdot (I_{adj} - I_{sense}) \geq I_{adj} \cdot 500 \cdot \Omega + 1 \cdot V$$

$$V_{out} - I_{out} \cdot R_{shunt} - R_{adj} \cdot I_{adj} + R_{adj} \cdot I_{sense} \geq I_{adj} \cdot 500 \cdot \Omega + 1 \cdot V$$

which leads to:

$$V_{out} - I_{out} \cdot R_{shunt} - R_{adj} \cdot \frac{I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt} + I_{sense} \cdot R_{adj}}{R_{adj}} + R_{adj} \cdot I_{sense} \geq \frac{I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt} + I_{sense} \cdot R_{adj}}{R_{adj}} \cdot 500 \cdot \Omega + 1 \cdot V$$

$$V_{out} - I_{out} \cdot R_{shunt} - (I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt} + I_{sense} \cdot R_{adj}) + R_{adj} \cdot I_{sense} \geq \frac{I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt} + I_{sense} \cdot R_{adj}}{R_{adj}} \cdot 500 \cdot \Omega + 1 \cdot V$$

$$V_{out} - I_{out} \cdot R_{shunt} - I_{sense} \cdot R_{sense} + I_{out} \cdot R_{shunt} - I_{sense} \cdot R_{adj} + R_{adj} \cdot I_{sense} \geq \frac{I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt} + I_{sense} \cdot R_{adj}}{R_{adj}} \cdot 500 \cdot \Omega + 1 \cdot V$$

$$V_{out} - I_{sense} \cdot R_{sense} \geq \frac{I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt} + I_{sense} \cdot R_{adj}}{R_{adj}} \cdot 500 \cdot \Omega + 1 \cdot V$$

multiply both sides by Radj:

$$R_{adj} \cdot (V_{out} - I_{sense} \cdot R_{sense}) \geq (I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt} + I_{sense} \cdot R_{adj}) \cdot 500 \cdot \Omega + 1 \cdot V \cdot R_{adj}$$

$$R_{adj} \cdot (V_{out} - I_{sense} \cdot R_{sense} - 1 \cdot V) \geq I_{sense} \cdot R_{sense} \cdot (500 \cdot \Omega) - I_{out} \cdot R_{shunt} \cdot (500 \cdot \Omega) + I_{sense} \cdot R_{adj} \cdot (500 \cdot \Omega)$$

$$R_{adj} \cdot (V_{out} - I_{sense} \cdot R_{sense} - 1 \cdot V - I_{sense} \cdot 500 \cdot \Omega) \geq I_{sense} \cdot R_{sense} \cdot (500 \cdot \Omega) - I_{out} \cdot R_{shunt} \cdot (500 \cdot \Omega)$$

$$R_{adj} \geq \frac{(I_{sense} \cdot R_{sense} - I_{out} \cdot R_{shunt}) \cdot 500 \cdot \Omega}{V_{out} - I_{sense} \cdot R_{sense} - 1 \cdot V - I_{sense} \cdot 500 \cdot \Omega}$$

In order to keep the internal bipolar from saturating, Radj must be greater than or equal to:

$$R_{adj\_veao} := \frac{(I_{sense} \cdot R_{sense} - I_{outmax} \cdot R_{shunt}) \cdot 500 \cdot \Omega}{V_{out} - I_{sense} \cdot R_{sense} - 1 \cdot V - I_{sense} \cdot 500 \cdot \Omega}$$

$$R_{adj\_veao} := \frac{(\Delta V_{outadjmax} - I_{outmax} \cdot R_{shunt}) \cdot 500 \cdot \Omega}{V_{out} - \Delta V_{outadjmax} - 1 \cdot V - \frac{\Delta V_{outadjmax}}{R_{sense}} \cdot 500 \cdot \Omega}$$

$$R_{adj\_veao} = 30.972 \Omega$$

So  $R_{adj}$  must be **greater than or equal to this** to fulfill the  $V_{adj} - V_{eao} \geq 1V$  requirement. This requirement will automatically be met in higher voltage applications as shown in Figure 4 on the UCC29002 data sheet because  $V_{adj}$  will be within  $1V_{BE}$  of VDD and VDD must be at least 4.75V.



the adjust resistor is also sized such that it operates over the available voltage adjustment current range of the module

$$I_{\text{sense}} \cdot R_{\text{sense}} = I_{\text{out}} \cdot R_{\text{shunt}} + (I_{\text{adj}} - I_{\text{sense}}) \cdot R_{\text{adj}}$$

$$R_{\text{adj}} = \frac{I_{\text{sense}} \cdot R_{\text{sense}} - I_{\text{out}} \cdot R_{\text{shunt}}}{I_{\text{adjmax}} - I_{\text{sense}}}$$

$$R_{\text{adj\_Iadj}} := \frac{\Delta V_{\text{outadjmax}} - I_{\text{outmax}} \cdot R_{\text{shunt}}}{I_{\text{adjmax}} - \frac{\Delta V_{\text{outadjmax}}}{R_{\text{sense}}}}$$

$$R_{\text{adj\_Iadj}} = -359.684 \, \Omega$$

select  $R_{\text{adj}}$  such that it is large enough to fulfill the  $V_{\text{adj}} - V_{\text{eao}}$  is greater than or equal to 1V requirement AND results in an adjust current not greater than the 6mA limitation of the IC. Then calculate  $I_{\text{adj}}$ ,  $V_{\text{adj}}$ , and  $V_{\text{eao}}$  to confirm that  $I_{\text{adj}}$  is not greater than 6mA,  $V_{\text{adj}}$  is greater than or equal to  $V_{\text{eao}} + 1$ , and  $V_{\text{eao}}$  is not greater than 3V

**selected  $R_{\text{adj}}$  value to meet both requirements**

$$R_{\text{adj}} := 300 \cdot \Omega$$

$$I_{\text{adj}} := \text{if} \left[ (I_{\text{adjmax}} - I_{\text{sense}}) \cdot R_{\text{adj}} + I_{\text{outmax}} \cdot R_{\text{shunt}} > \Delta V_{\text{outadjmax}}, \frac{I_{\text{sense}} \cdot R_{\text{sense}} - I_{\text{outmax}} \cdot R_{\text{shunt}} + I_{\text{sense}} \cdot R_{\text{adj}}}{R_{\text{adj}}}, I_{\text{adjmax}} \right]$$

$$I_{\text{adj}} = 6 \cdot \text{mA}$$

In other words, there are two factors that are dependent: the  $I_{\text{adj}}$  and the  $\Delta V_{\text{outadj}}$ . The  $I_{\text{adj}}$  maximum is determined by the 3.5V clamp and 500 $\Omega$  resistor in the IC,  $\Delta V_{\text{outadj}}$  maximum is limited by the module, neither of these can be exceeded but both can be lower than maximum. If it is determined that the max 7mA  $I_{\text{adj}}$  would result in a  $\Delta V_{\text{outadj}}$  greater than the module is capable of, then  $I_{\text{adj}}$  is limited. Make sure  $V_{\text{adj}} > V_{\text{eao}} + 1$ , and  $V_{\text{eao}} < 3\text{V}$

$$\Delta V_{\text{outadj}} := I_{\text{outmax}} \cdot R_{\text{shunt}} + (I_{\text{adj}} - I_{\text{sense}}) \cdot R_{\text{adj}}$$

$$\Delta V_{\text{outadj}} = -2.248 \, \text{V}$$

$$V_{\text{BE}} := 0.7 \cdot \text{V}$$



$$V_{\text{adj}} := \begin{cases} V_{\text{out}} - I_{\text{outmax}} \cdot R_{\text{shunt}} - R_{\text{adj}} \cdot (I_{\text{adj}} - I_{\text{sense}}) & \text{if } V_{\text{DD}} < 15 \cdot \text{V} \\ V_{\text{DD}} - V_{\text{BE}} & \text{if } V_{\text{DD}} \geq 15 \cdot \text{V} \end{cases}$$

$$V_{\text{adj}} = 56.248 \text{ V}$$

$$V_{\text{eao}} := I_{\text{adj}} \cdot 500 \cdot \Omega$$

$$V_{\text{eao}} = 3 \text{ V}$$

$$V_{\text{adj}} - V_{\text{eao}} = 53.248 \text{ V}$$

BJT is saturating if  $V_{\text{adj}} - V_{\text{eao}} < 1 \text{ V}$

$$I_{\text{adj\_amp}} := \frac{I_{\text{sense}} \cdot R_{\text{sense}} - I_{\text{outmax}} \cdot R_{\text{shunt}} + I_{\text{sense}} \cdot R_{\text{adj}}}{R_{\text{adj}}}$$

$$I_{\text{adj\_amp}} = 22.492 \cdot \text{mA}$$

Now for the bode plots

$$R_{\text{load}} := \frac{V_{\text{out}}}{I_{\text{outmax}}}$$

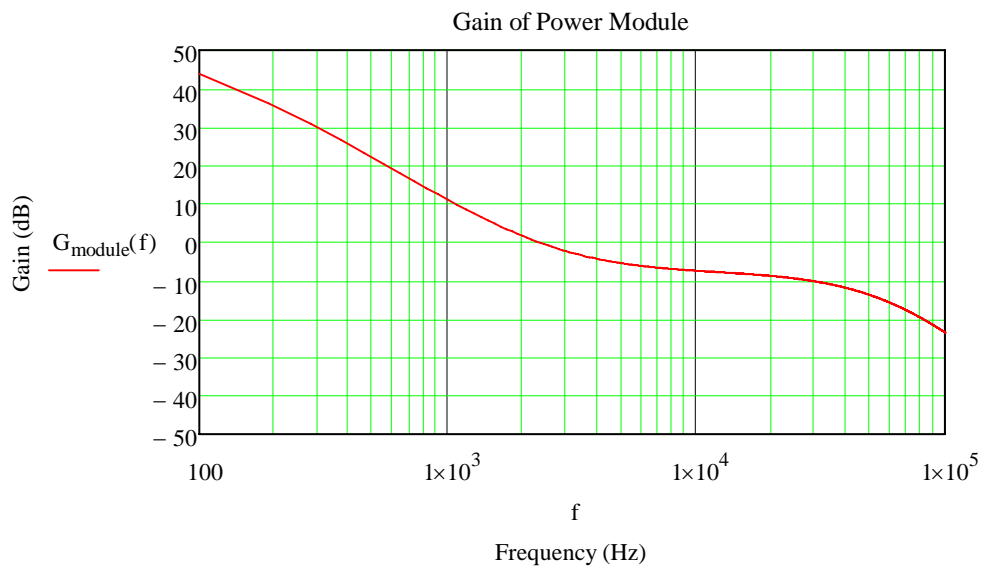
$$f := 10 \cdot \text{Hz}, 100 \cdot \text{Hz}.. 100 \cdot \text{kHz}$$

$$s(f) := j \cdot 2 \cdot \pi \cdot f$$

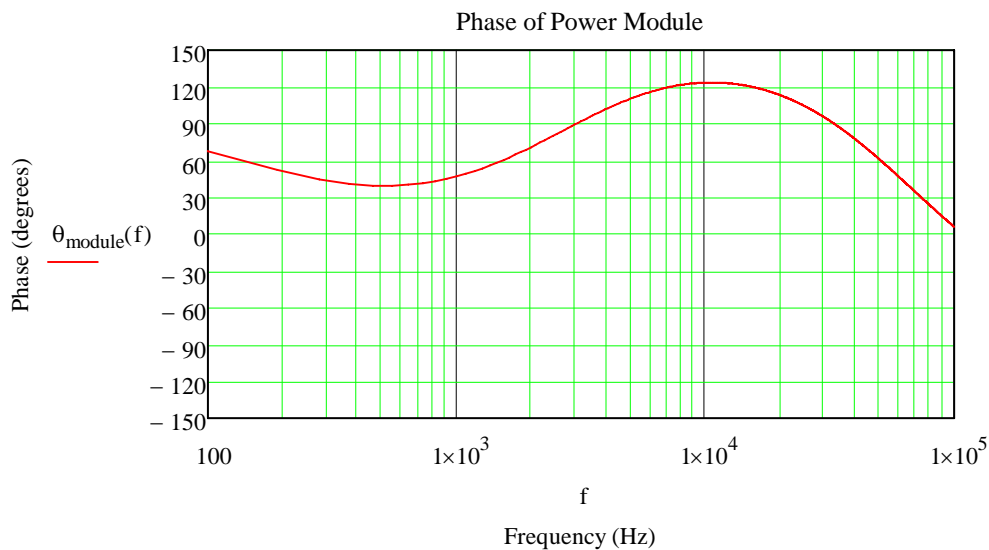
Based on the given Bode, the loop equation is derived and plotted to match measured plot...

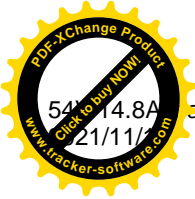
$$G_{\text{mod}}(f) := 10^{-\frac{71}{20}} \cdot \frac{\left[1 + s(f) \cdot \left(\frac{1}{2 \cdot \pi \cdot 2.8 \cdot \text{kHz}}\right)\right]^2}{\left[1 + s(f) \cdot \left(\frac{1}{2 \cdot \pi \cdot 5 \cdot \text{Hz}}\right)\right] \cdot \left[1 + s(f) \cdot \left(\frac{1}{2 \cdot \pi \cdot 180 \cdot \text{Hz}}\right)\right] \cdot \left[1 + s(f) \cdot \left(\frac{1}{2 \cdot \pi \cdot 65 \cdot \text{kHz}}\right)\right]^3}$$

$$G_{\text{module}}(f) := 20 \cdot \log(|G_{\text{mod}}(f)|)$$



$$\theta_{\text{module}}(f) := \arg(G_{\text{mod}}(f)) \cdot \frac{180}{\pi} + 180$$





Want the total loop to cross at a decade or more before the module's crossover. To do this, determine what the gain of the module is at the desired system crossover frequency, determine the contribution from the open loop load share components, then select the error amplifier components to add the desired attenuation

$$f_{\text{comod}} = 2.4 \cdot \text{kHz}$$

$$f_{\text{CO}} := 240 \cdot \text{Hz}$$

SELECT THIS VALUE,  $f_{\text{CO}}$ , TO BE AT LEAST A DECADE BEFORE THE MEASURED  $f_{\text{comod}}$

The gain of the module at the desired crossover frequency, in V/V:

$$|G_{\text{mod}}(f_{\text{CO}})| = 44.667$$

or calculate from dB as taken from the Bode plot

$$\frac{G_{\text{module}}(f_{\text{CO}})}{10^{\frac{20}{20}}} = 44.667$$

The gain of the module at the desired crossover frequency, in dB, which can be read directly from the Bode plot

$$G_{\text{module}}(f_{\text{CO}}) := 20 \cdot \log(|G_{\text{mod}}(f_{\text{CO}})|)$$

$$G_{\text{module}}(f_{\text{CO}}) = 33$$

taking the Current sense amp into consideration:

$$G_{\text{CSA}}(f) := A_{\text{CSA\_actual}} \cdot \left[ \frac{1}{1 + s(f) \cdot R_{\text{csa1}} \cdot (C_{\text{CSA}})} \right]$$

$$|G_{\text{CSA}}(f_{\text{CO}})| = 99.971$$

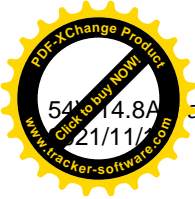
this is the actual gain over frequency but the pole is usually way beyond the module bandwidth and won't be a factor

$$A_{\text{CSA\_actual}} = 100$$

voltage gain

$$A_V := \frac{R_{\text{shunt}}}{R_{\text{load}}}$$

$$A_V = 4.385 \times 10^{-5}$$



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Adjust amplifier gain

$$A_{ADJ} := \frac{R_{adj} \cdot R_{sense}}{(R_{adj} + R_{sense}) \cdot (500 \cdot \Omega)}$$

$$A_{ADJ} = 0.24$$

Internal Error amplifier transconductance

$$G_m := 0.014 \cdot S$$

open loop gain, converted to dB:

$$G_{openloop}(f) := (A_V \cdot A_{CSA\_actual} \cdot A_{ADJ})$$

$$G_{openloop}(f_{CO}) = 1.052 \times 10^{-3}$$

Combining all of the known gains to determine the error amp compensation:

$$|G_{mod}(f_{CO})| \cdot G_{openloop}(f_{CO}) \cdot G_{EA}(f_{CO}) = 1$$

$$G_{EA}(f_{CO}) := \frac{1}{|G_{mod}(f_{CO})| \cdot G_{openloop}(f_{CO})}$$

$$G_{EA}(f_{CO}) = 21.272$$

THIS IS THE VALUE OF GAIN, in dB, NEEDED FROM THE ERROR AMP COMPENSATION TO GET THE DESIRED CROSSOVER FREQUENCY

We know that the gain of a transconductance error amplifier is equal to:

$$G_{ErrorAmp}(f) = G_m \cdot \left( \frac{1}{s(f) \cdot C_{EAO}} + R_{EAO} \right)$$

Substituting for s:

$$G_{ErrorAmp}(f) = \left| G_m \cdot \left( \frac{1}{j \cdot 2\pi \cdot f_{CO} \cdot C_{EAO}} + R_{EAO} \right) \right|$$

Assumption for  $C_{EAO}$ , should be a relatively large value due to the low frequency requirement:

$$C_{EAO} \geq \frac{G_m}{2 \cdot \pi \cdot f_{CO}} \cdot (A_V \cdot A_{CSA\_actual} \cdot A_{ADJ} \cdot |G_{mod}(f_{CO})|)$$

to get a valid result for  $R_{EAO}$ ,  $C_{EAO}$  must be equal to or larger than this

$$C_{EAOmin} := \frac{G_m}{2 \cdot \pi \cdot f_{CO}} \cdot (A_V \cdot A_{CSA\_actual} \cdot A_{ADJ} \cdot |G_{mod}(f_{CO})|)$$

$$C_{EAOmin} = 0.436 \cdot \mu F$$

ASSIGN A VALUE FOR  $C_{EAO}$  BASED UPON TYPICAL AVAILABLE VALUES

$$C_{EAO} := 10 \cdot \mu F$$

Solving for the magnitude of the resistance:

$$R_{EAO} := \sqrt{\left( \frac{1}{j \cdot 2 \pi \cdot f_{CO} \cdot C_{EAO}} \right)^2 + \left( \frac{G_{EA}(f_{CO})}{G_m} \right)^2}$$

$$R_{EAO} := \sqrt{\left( \frac{1}{j \cdot 2 \pi \cdot f_{CO} \cdot C_{EAO}} \right)^2 + \left[ \frac{1}{G_m \cdot (|G_{mod}(f_{CO})| \cdot G_{openloop}(f_{CO}))} \right]^2}$$

$$R_{EAO} = 1.518 \times 10^3 \Omega$$

OR

$$R_{EAO} := \sqrt{\left[ \frac{1}{G_m \cdot (|G_{mod}(f_{CO})| \cdot (A_V \cdot A_{CSA\_actual} \cdot A_{ADJ}))} \right]^2 - \left( \frac{1}{2 \cdot \pi \cdot f_{CO} \cdot C_{EAO}} \right)^2}$$

$$R_{EAO} = 1.518 \times 10^3 \Omega$$

IS THIS A REAL OR IMAGINARY VALUE? IF IMAGINARY (CONTAINS j), SELECT ANOTHER VALUE FOR  $C_{EAO}$

resultant actual zero frequency

THE CHOSEN  $C_{EAO}$  AND  $R_{EAO}$  COMPONENTS WILL PLACE A ZERO HERE

$$f_{ZERO} := \frac{1}{2 \cdot \pi \cdot R_{EAO} \cdot C_{EAO}}$$

$$f_{ZERO} = 0.01 \cdot \text{kHz}$$

Now Bode the whole thing:

$$G_{module}(f) := 20 \cdot \log(|G_{mod}(f)|)$$

$$\text{Gain}_{openloop}(f) := A_{CSA\_actual} \cdot A_V \cdot A_{ADJ} \cdot G_{mod}(f)$$

$$G_{openloop}(f) := 20 \cdot \log(|G_{CSA}(f)|) + 20 \cdot \log(|A_V|) + 20 \cdot \log(|A_{ADJ}|)$$

$$\text{Gain}_{ErrorAmp}(f) := G_m \cdot \left( \frac{1}{s(f) \cdot C_{EAO}} + R_{EAO} \right)$$

$$G_{EA}(f) := 20 \cdot \log(|\text{Gain}_{ErrorAmp}(f)|)$$

$$G_{total}(f) := G_{module}(f) + G_{openloop}(f) + G_{EA}(f)$$

THE FINAL RESULT SHOULD HAVE THE UNITY GAIN FOR THE TOTAL SYSTEM (BLACK) BEFORE THE ORIGINAL MODULE UNITY GAIN (RED), AND BE LINEAR. THE INDIVIDUAL CONTRIBUTIONS FROM THE OPEN LOOP COMPONENTS (PINK) AND ERROR AMP (BLUE) ARE ALSO SHOWN

