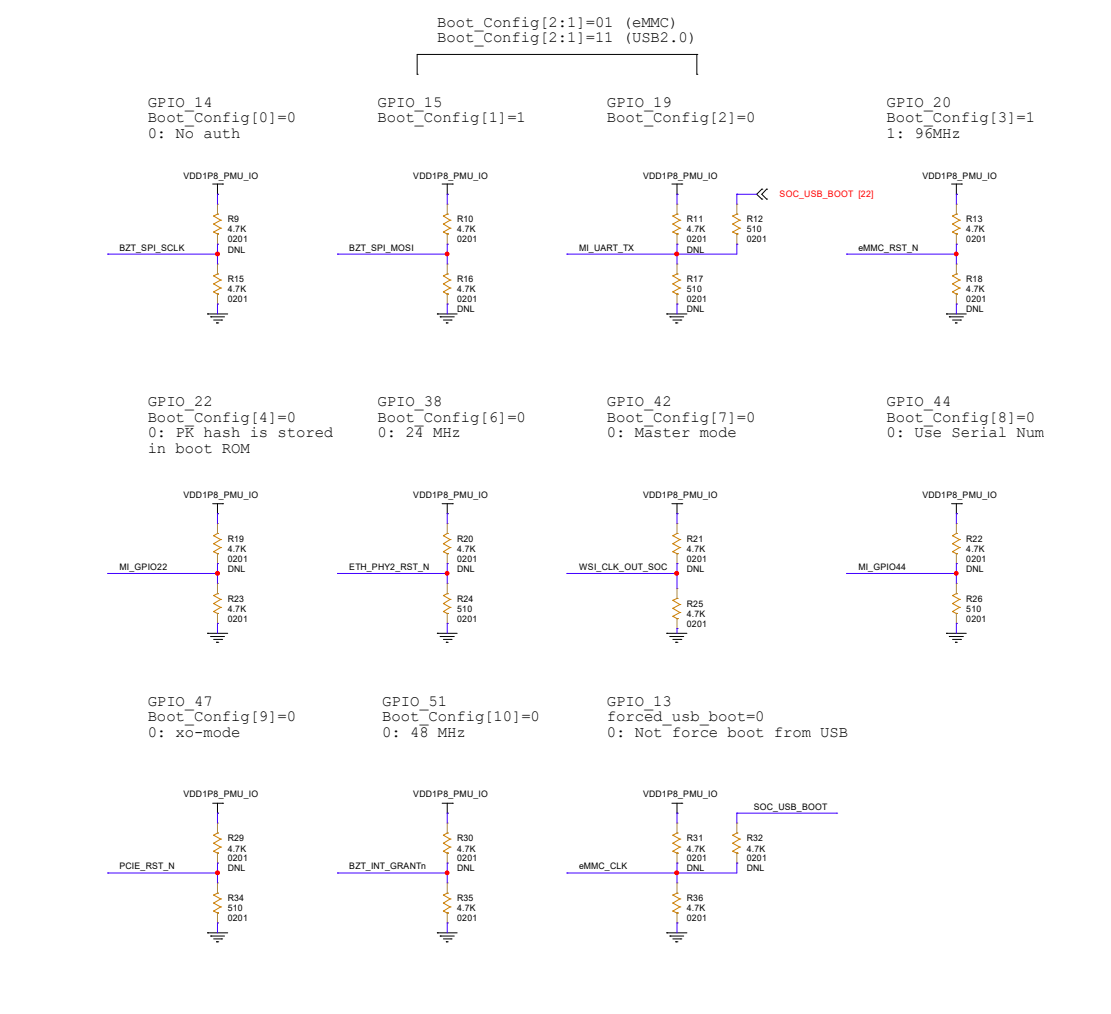
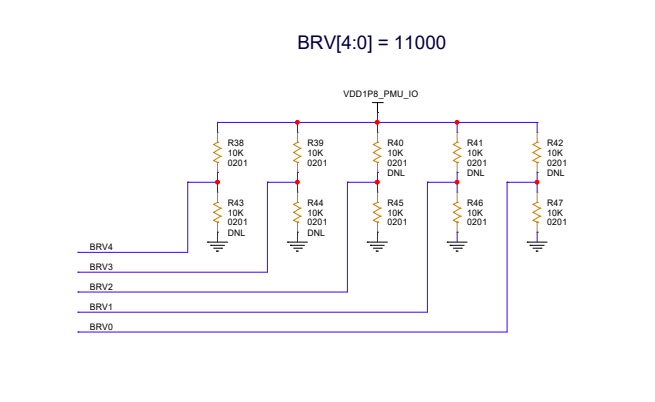
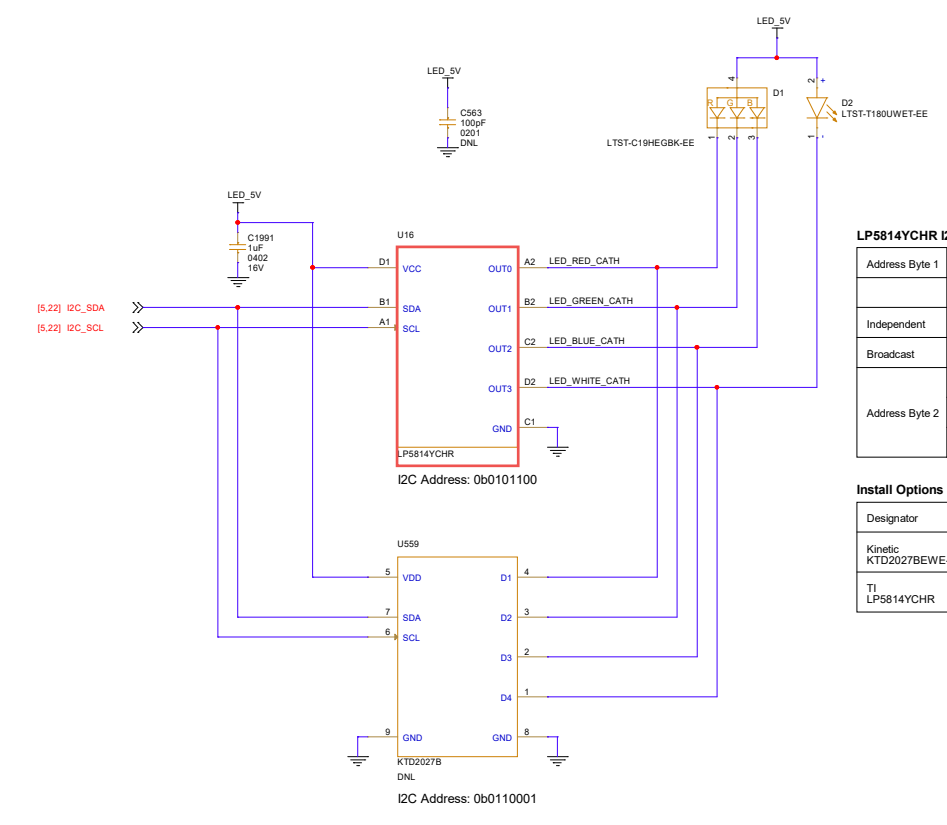
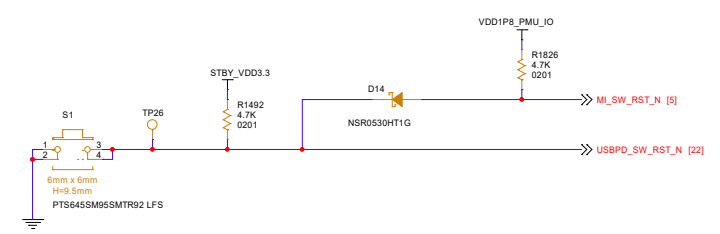
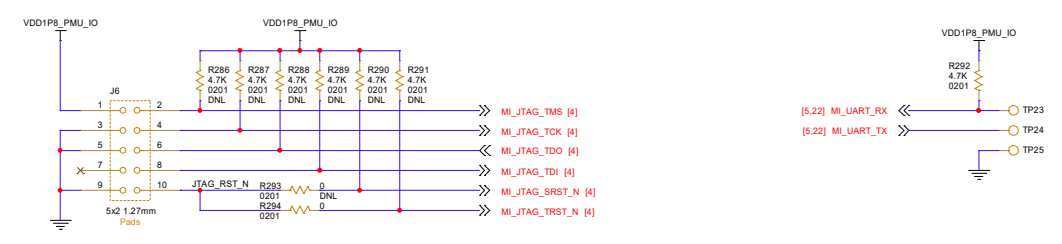


Miami Boot Configuration



Board Register Value



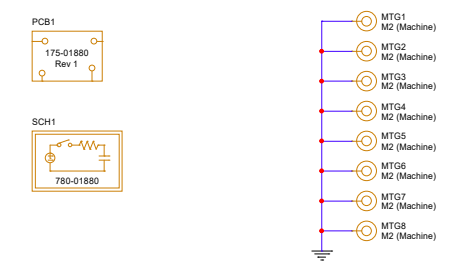


LP5814YCHR I2C Data Format

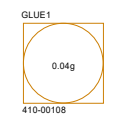
| Address Byte 1 | Chip Address | | | | | | | R/W | |
|----------------|------------------|---------|---------|---------|---------|---------|---------|---------|-----------|
| | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | | Bit 0 |
| Independent | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | R-1 / W-0 |
| Broadcast | 0 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | |
| Address Byte 2 | Register Address | | | | | | | | |
| | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | |
| | 7th bit | 6th bit | 5th bit | 4th bit | 3th bit | 2th bit | 1th bit | 0th bit | |

Install Options For LED Driver Solutions

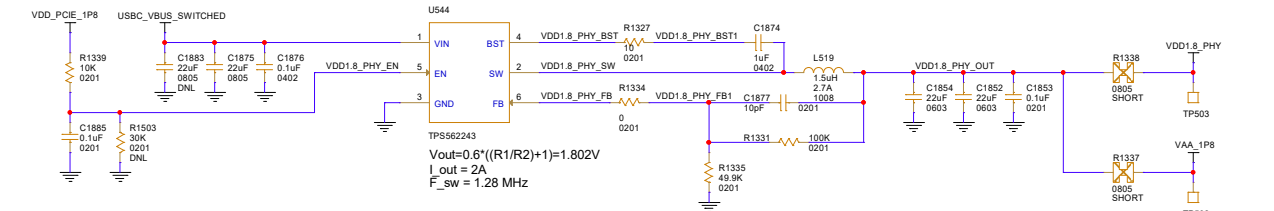
| Designator | U16 | U599 |
|---------------------------|---------|---------|
| Kinetic KTD2027BEWE-TR | DNL | Install |
| TI LP5814YCHR | Install | DNL |



- FD1 ○-X
- FD2 ○-X
- FD3 ○-X
- FD4 ○-X
- FD5 ○-X
- FD6 ○-X



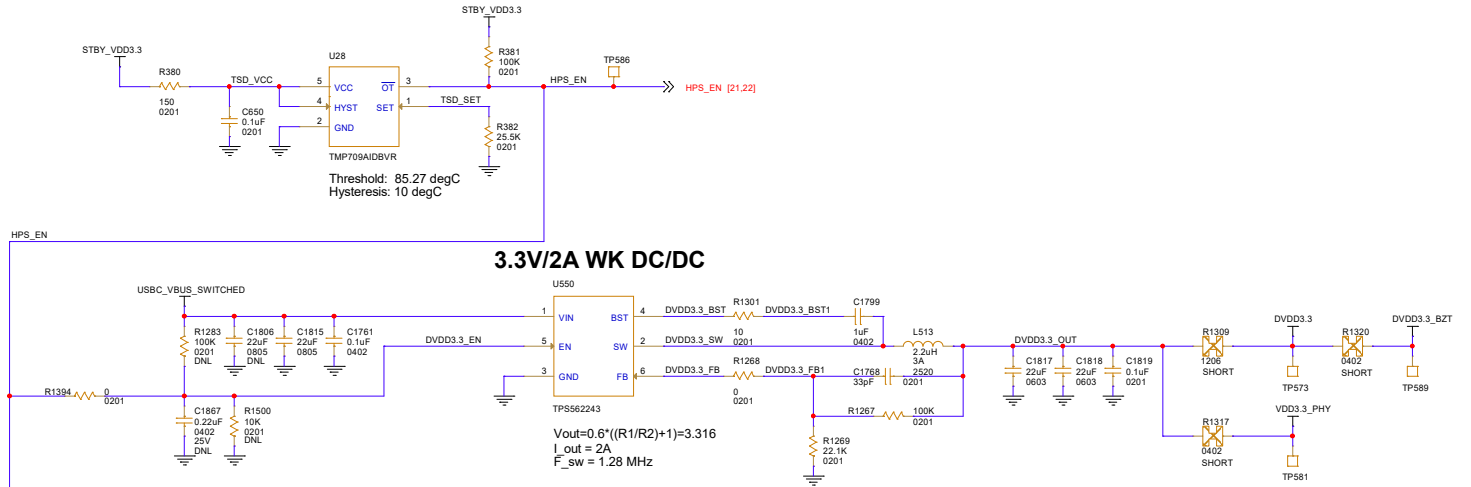
1.8V/2A for Ethernet PHY and WK VAA_1P8



BOM Option for VDD1.8_PHY_OUT

| Designator | R1327 | C1874 | R1335 | R1331 | C1877 |
|----------------------|--------|--------|-----------|-----------|--------|
| MP1682C (1.1 MHz) | 0 ohm | 0.1 uF | 20 Kohm | 40.2 Kohm | 470 pF |
| TPS562243 (1.28 MHz) | 10 ohm | 1 uF | 49.9 Kohm | 49.9 Kohm | 10 pF |

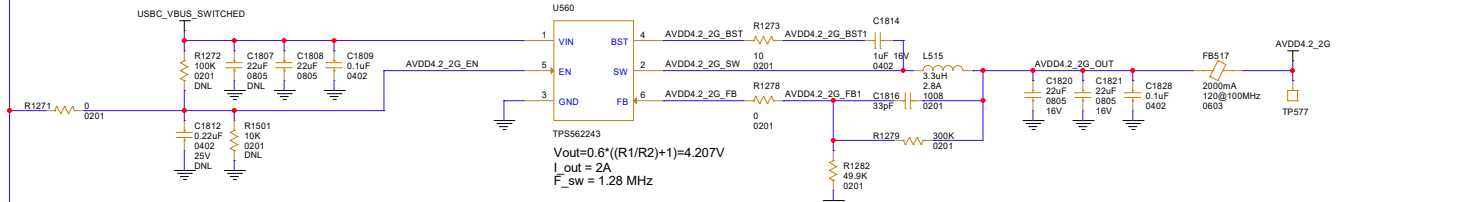
3.3V/2A WK DC/DC



BOM Option for DVDD3.3_OUT

| Designator | R1301 | C1799 | R1269 | R1267 | C1768 |
|----------------------|--------|--------|-----------|-----------|--------|
| MP1682C (1.1 MHz) | 0 ohm | 0.1 uF | 8.87 Kohm | 40.2 Kohm | 470 pF |
| TPS562243 (1.28 MHz) | 10 ohm | 1 uF | 22.1 Kohm | 100 Kohm | 33 pF |

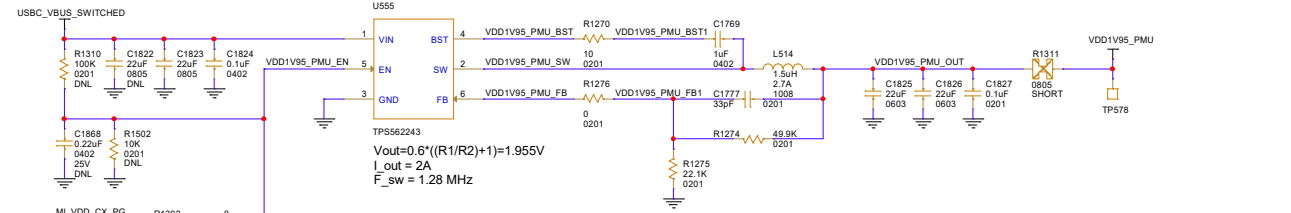
4.2V/2A for 2G



BOM Option for AVDD4.2_2G_OUT

| Designator | R1273 | C1814 | R1282 | R1279 | C1816 |
|----------------------|--------|--------|-----------|-----------|--------|
| MP1682C (1.1 MHz) | 0 ohm | 0.1 uF | 6.65 Kohm | 40.2 Kohm | 470 pF |
| TPS562243 (1.28 MHz) | 10 ohm | 1 uF | 49.9 Kohm | 300 Kohm | 33 pF |

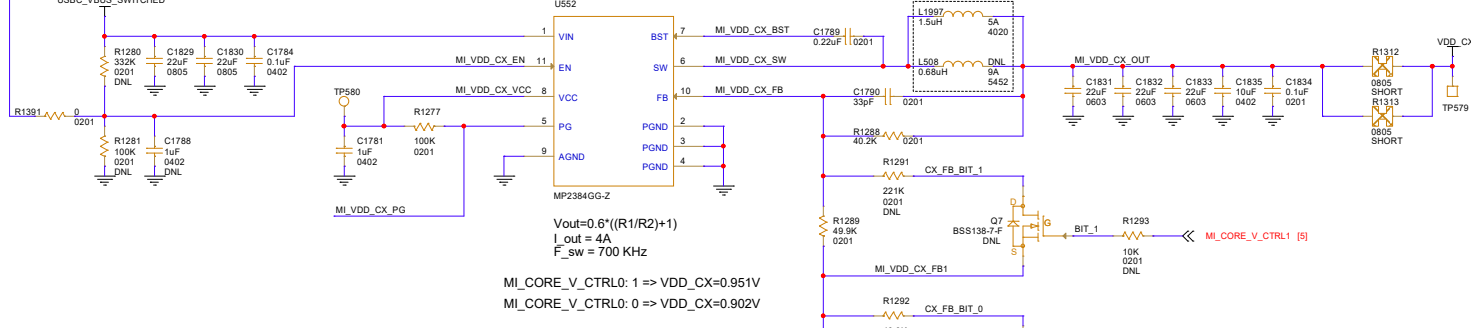
1.96V/2A for MI VDD1V95_PMU



BOM Option for VDD1V95_PMU_OUT

| Designator | R1270 | C1769 | R1275 | R1274 | C1777 |
|----------------------|--------|--------|-----------|-----------|--------|
| MP1682C (1.1 MHz) | 0 ohm | 0.1 uF | 17.5 Kohm | 40.2 Kohm | 470 pF |
| TPS562243 (1.28 MHz) | 10 ohm | 1 uF | 22.1 Kohm | 49.9 Kohm | 33 pF |

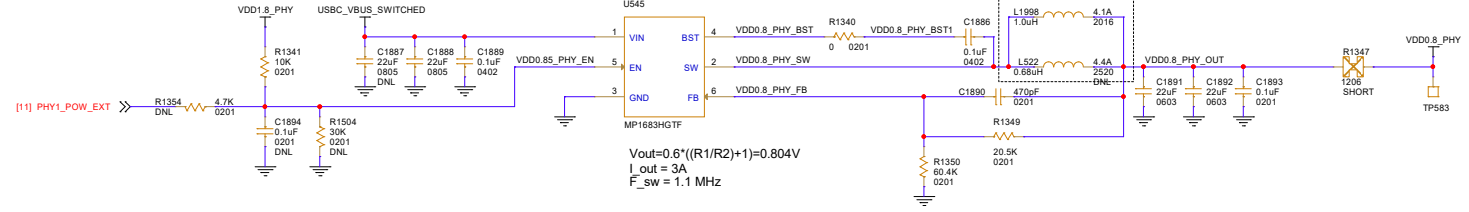
0.9V/4A for MI VDD_CX



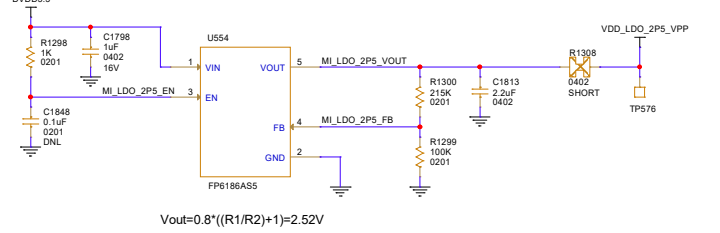
BOM Options For 4A or 8A DC/DC

| Designator | U552 | L508 | L1997 |
|-------------------|------------|-----------|----------|
| 4A for MP2384GG-Z | MP2384GG-Z | DNL | 1.5uH/5A |
| 8A for MP2386GG-Z | MP2386GG-Z | 0.68uH/9A | DNL |

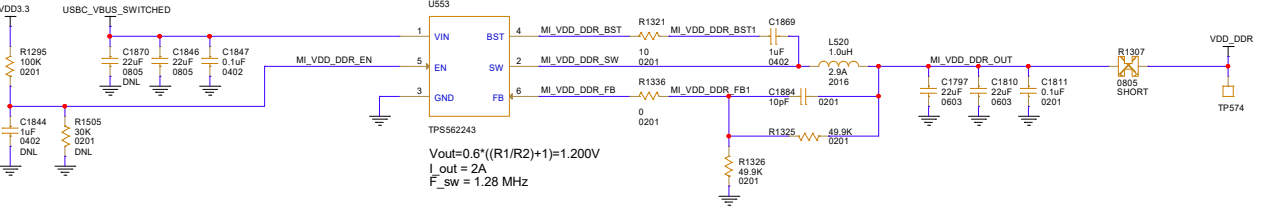
0.8V/3A for Ethernet PHY



2.5V/0.6A for MI VDD_LDO_2P5_VPP



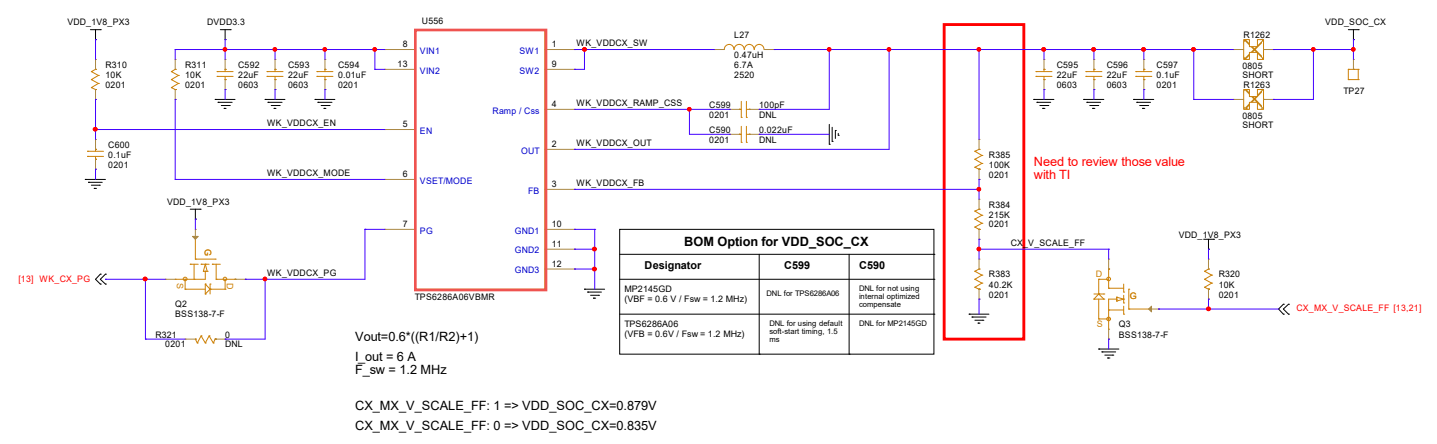
1.2V/2A for MI VDD_DDR



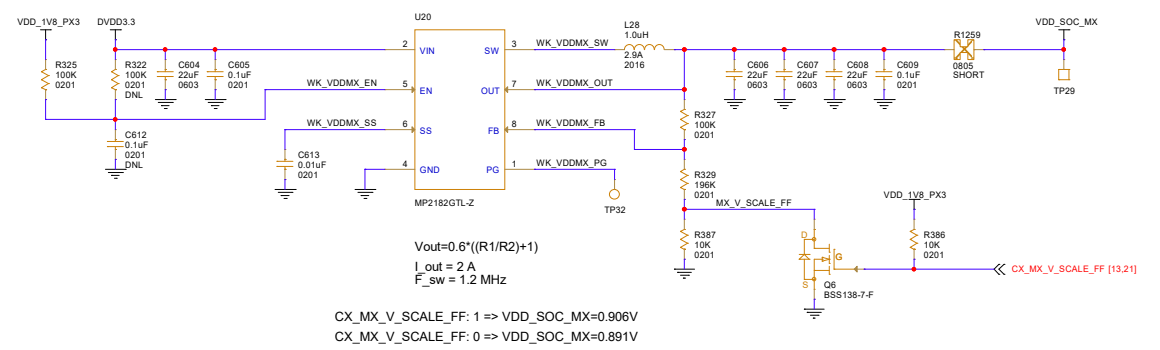
BOM Option for MI_VDD_DDR_OUT

| Designator | R1321 | C1869 | R1326 | R1325 | C1884 |
|----------------------|--------|--------|-----------|-----------|--------|
| MP1682C (1.1 MHz) | 0 ohm | 0.1 uF | 40.2 Kohm | 40.2 Kohm | 470 pF |
| TPS562243 (1.28 MHz) | 10 ohm | 1 uF | 49.9 Kohm | 49.9 Kohm | 10 pF |

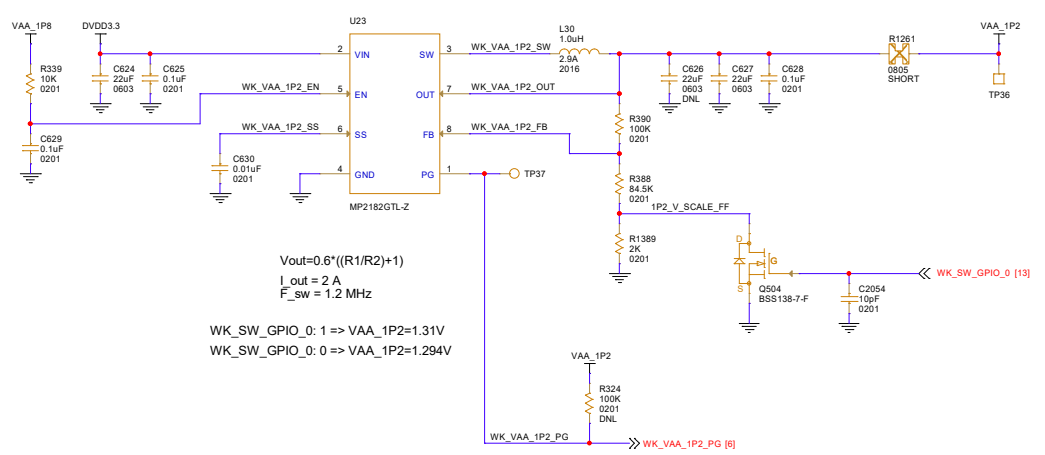
0.85V/6A for WK VDD_SOC_CX



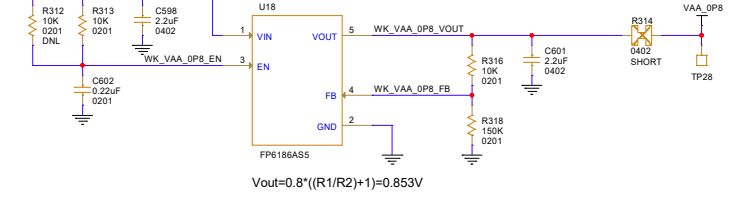
0.865V/2A for WK VDD_SOC_MX



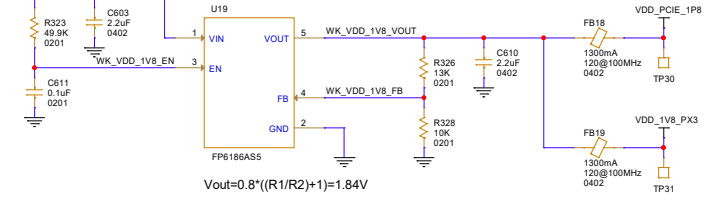
1.3V/2A for WK VAA_1P2



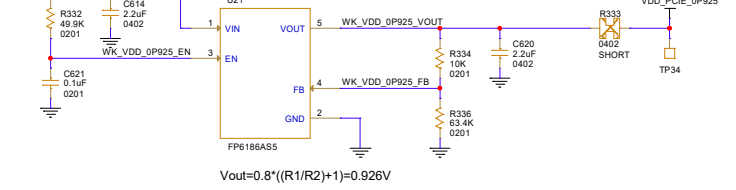
0.85V/0.6A for WK VAA_0P8



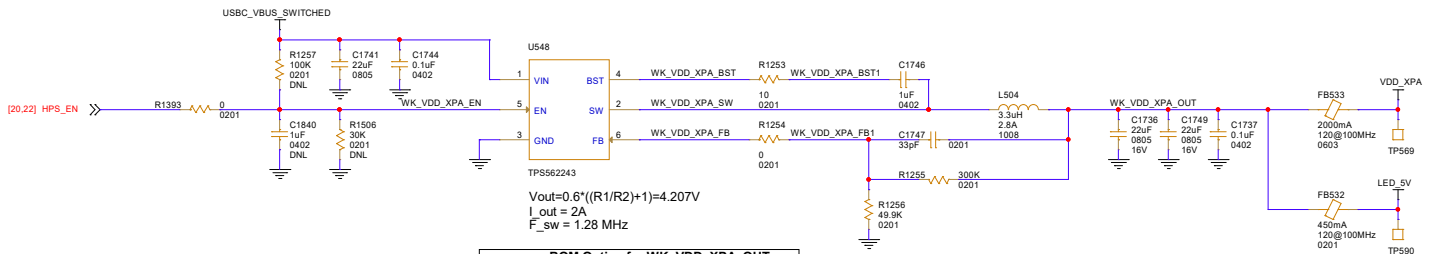
1.8V/0.6A for WK VDD_PCIE_1P8 and VDD_1V8_PX3



0.93V/0.6A for WK VDD_PCIE_0P925



4.2V/2A for 5G / 6G PA and LED

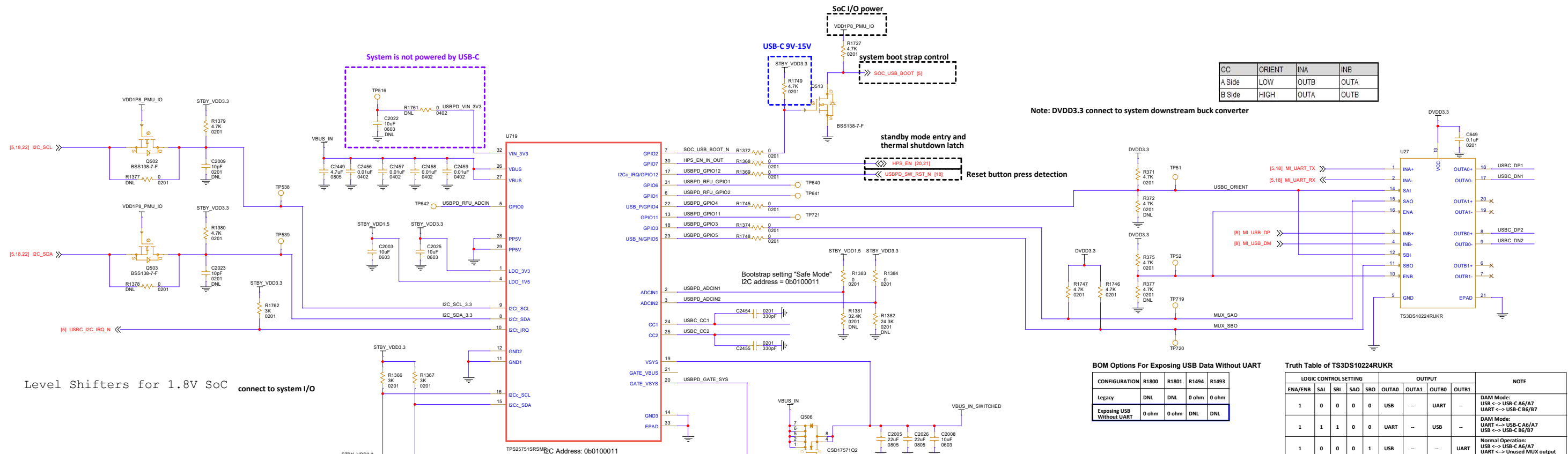


BOM Option for WK_VDD_XPA_OUT

| Designator | R1253 | C1746 | R1256 | R1255 | C1747 |
|----------------------|--------|--------|------------|------------|--------|
| MP1682C (1.1 MHz) | 0 ohm | 0.1 uF | 6.65 Kohms | 40.2 Kohms | 470 pF |
| TPS562243 (1.28 MHz) | 10 ohm | 1 uF | 49.9 Kohms | 300 Kohms | 33 pF |

Install Options For 5/6 GHz FEMs

| Designator | R1256 (MP1682C) | R1255 (TPS562243) |
|---------------------------------|-------------------------|-------------------------|
| VDD_XPA = 5V for Qorvo FEM | 180-00153 5.49 Kohms | 180-00075 40.2 Kohms |
| VDD_XPA = 4.2V for Skyworks FEM | 180-00193 6.65 Kohms | 180-00230 49.9 Kohms |



| CC | ORIENT | INA | INB |
|--------|--------|------|------|
| A Side | LOW | OUTB | OUTA |
| B Side | HIGH | OUTA | OUTB |

Note: DVDD3.3 connect to system downstream buck converter

Level Shifters for 1.8V SoC connect to system I/O

TPS25751SRSM I2C Address: 0b0100011
USB-PD Controller

Truth Table of TS3DS10224RUKR

| LOGIC CONTROL SETTING | | | | | OUTPUT | | | NOTE | |
|-----------------------|-----|-----|-----|-----|--------|-------|-------|------|--|
| ENA/ENB | SAI | SBI | SAO | SBO | OUTA0 | OUTA1 | OUTB0 | | OUTB1 |
| 1 | 0 | 0 | 0 | 0 | USB | -- | UART | -- | DAM Mode: USB <-> USB-C A5/A7 UART <-> USB-C B6/B7 |
| 1 | 1 | 1 | 0 | 0 | UART | -- | USB | -- | DAM Mode: UART <-> USB-C A6/A7 USB <-> USB-C B6/B7 |
| 1 | 0 | 0 | 0 | 1 | USB | -- | -- | UART | Normal Operation: USB <-> USB-C A6/A7 UART <-> Unused MUX output |
| 1 | 1 | 1 | 1 | 0 | -- | UART | USB | -- | Normal Operation: UART <-> Unused MUX output USB <-> Unused MUX output |
| 1 | 0 | 0 | 1 | 1 | -- | USB | -- | UART | Termination Mode: UART <-> Unused MUX output USB <-> Unused MUX output |
| 1 | 1 | 1 | 1 | 1 | -- | UART | -- | USB | Termination Mode: UART <-> Unused MUX output USB <-> Unused MUX output |

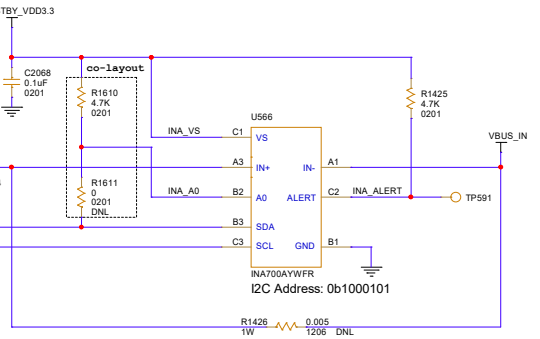
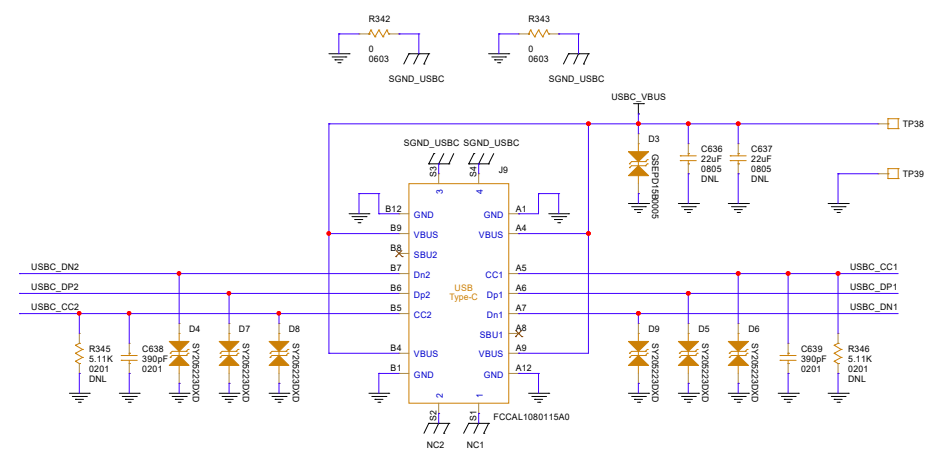
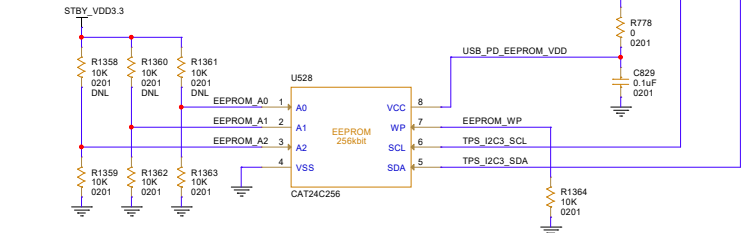


Table 7-2. Address Pins and Secondary Device Addresses

| A0 | DEVICE ADDRESS |
|-----|----------------|
| GND | 1000100 |
| VS | 1000101 |
| SDA | 1000110 |
| SCL | 1000111 |