

NCEP0140AG

NCE N-Channel Super Trench Power MOSFET

Description

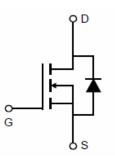
The NCEP0140AG uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

- V_{DS} =100V, I_{D} =40A $R_{DS(ON)}$ =18m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =22m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

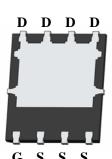
Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Schematic Diagram





Top View

Bottom View

100% UIS TESTED!

100% AVds TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP0140AG	NCEP0140AG	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage	V _G S	±20	V	
Drain Current-Continuous (Silicon Limited)	I _D	40	Α	
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	28.3	А	
Pulsed Drain Current (Package Limited)	I _{DM}	160	А	
Maximum Power Dissipation	P _D	50	W	
Derating factor		0.4	W/℃	
Single pulse avalanche energy (Note 5)	E _{AS}	200	mJ	
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 150	$^{\circ}\!\mathbb{C}$	



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Thermal Characteristic

Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	Voltage BV_{DSS} V_{GS} =0V I_D =250 μ A		100		-	V
Zero Gate Voltage Drain Current	I _{DSS}			-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•			•
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.2	2.0	2.8	V
Danie Course On Otata Desistance	-	V _{GS} =10V, I _D =20A	-	18	23	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A	-	22	27	mΩ
Forward Transconductance	g FS	V_{DS} =5 V , I_{D} =20 A	-	35	-	S
Dynamic Characteristics (Note4)			•			•
Input Capacitance	C _{Iss}	\/ 5 0\/\/ 0\/	-	1600	-	PF
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V,	-	139	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	11	-	PF
Switching Characteristics (Note 4)	<u> </u>		•			
Turn-on Delay Time	t _{d(on)}		-	6	-	nS
Turn-on Rise Time	t _r	V_{DD} =50 V , I_D =20 A	-	2	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =1.6 Ω	-	18	-	nS
Turn-Off Fall Time	t _f		-	2	-	nS
Total Gate Charge	Qg	V 50V/1 00A	-	26	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =50V,I _D =20A,	-	7.4		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	3.8		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =40A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	40	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = 20A	-		26	nS
Reverse Recovery Charge	Qrr	$di/dt = 500A/\mu s^{(Note3)}$	-		98	nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V_DD=20V,V_G=10V,L=0.5mH,Rg=25 Ω





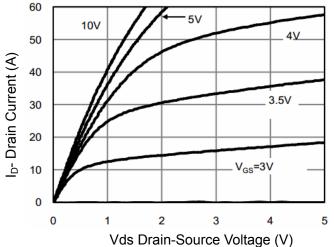


Figure 1 Output Characteristics

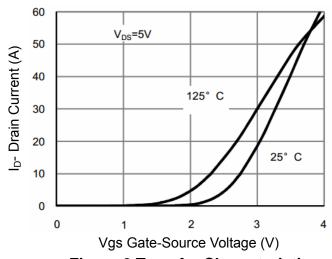


Figure 2 Transfer Characteristics

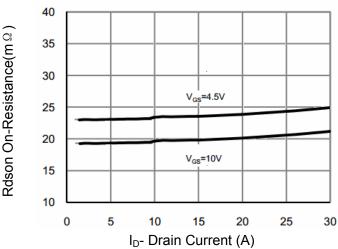


Figure 3 Rdson- Drain Current

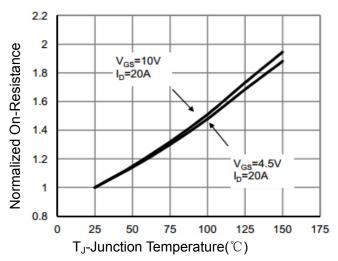


Figure 4 Rdson-Junction Temperature

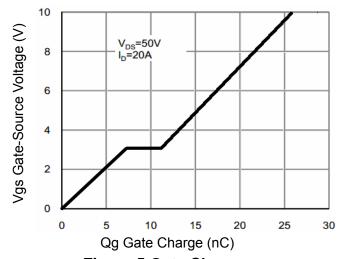


Figure 5 Gate Charge

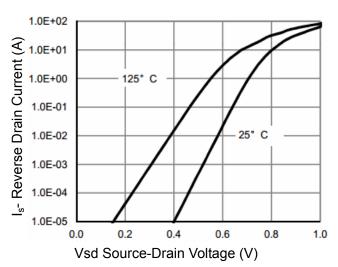


Figure 6 Source- Drain Diode Forward



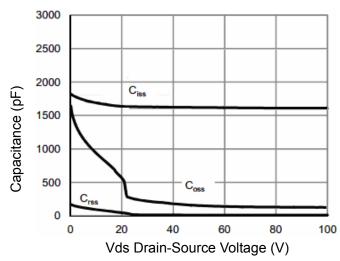


Figure 7 Capacitance vs Vds

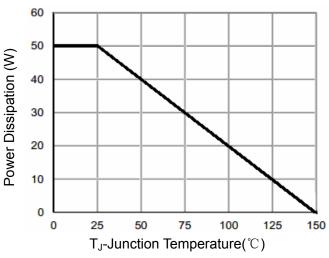


Figure 9 Power De-rating

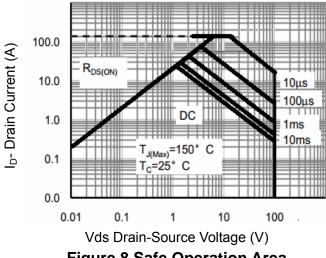


Figure 8 Safe Operation Area

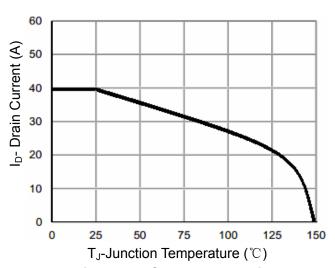


Figure 10 Current De-rating

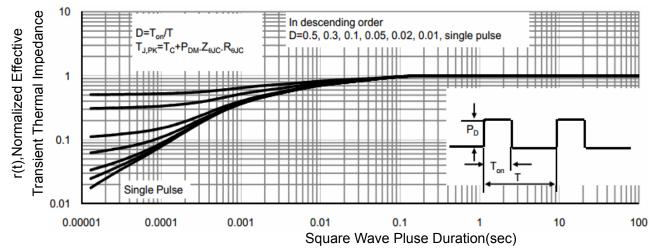
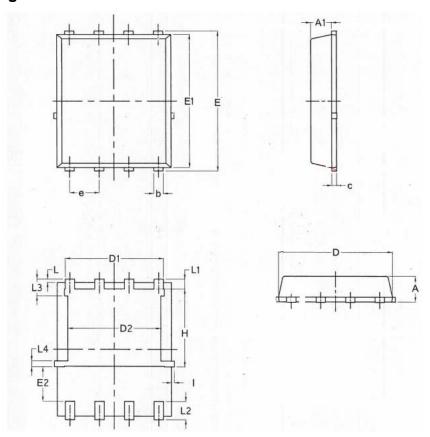


Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
Α	0.90	1.10	1.17	0.0354	0.0433	0.0461
A1	0.824	0.897	0.97	0.0324	0.0353	0.0382
b	0.33	0.41	0.50	0.0130	0.0161	0.0197
С	0.150	0.20	0.250	0.0059	0.0079	0.0098
D	4.80	4.90	5.00	0.1890	0.1929	0.1969
D1	3.91	4.22	4.36	0.1539	0.1661	0.1717
D2	3.85	4.00	4.15	0.1516	0.1575	0.1634
E	5.90	60.5	6.15	0.2323	0.2382	0.2421
E1	5.65	5.76	5.85	0.2224	0.2268	0.2303
E2	1.10	1	1	0.0433	1	1
е	1.27 BSC			0.050 BSC		
L	0.05	0.15	0.25	0.0020	0.0059	0.0098
L1	0.38	0.425	0.50	0.0150	0.0167	0.0197
L2	0.51	0.785	0.86	0.0201	0.0309	0.0339
L3	0.55	0.70	0.85	0.0217	0.0276	0.0335
L4	0.10	0.25	0.40	0.0039	0.0098	0.0157
Н	3.25	3.35	3.58	0.1280	0.1319	0.1409
I	0	1	0.18	0	1	0.0071



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