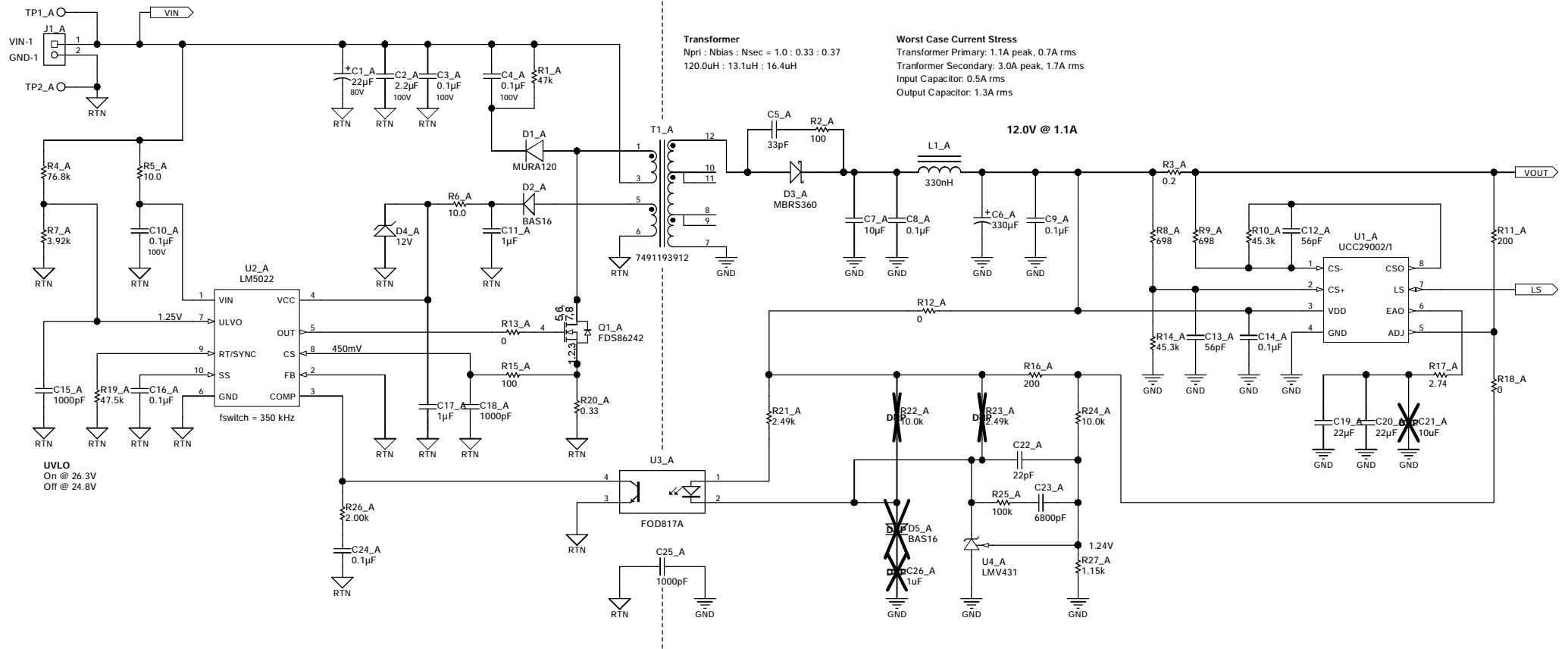


Revision History	
Revision	Notes
A	* First release
B	* Built & Tested

**Design Notes**  
 \* R12, R18 for test purpose only  
 \* C21, C26, D5, R22, R23 not populated  
 \* U1 (SO-8) does not meet footprint (8-VSSOP)

Up to 8 Inputs  
 27 .. 60V DC



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Designed for: Public Release	Mod. Date: 1/30/2015
Project Title: 8x Revers Power	
Number: PMP10099	Rev: B
SVN Rev: Not in version control	
Drawn By:	Sheet: 2 of 2
Engineer: Matthias Ulmann	File: PMP10099RevB - Circuit.SchDoc
	Size: A3
	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>

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