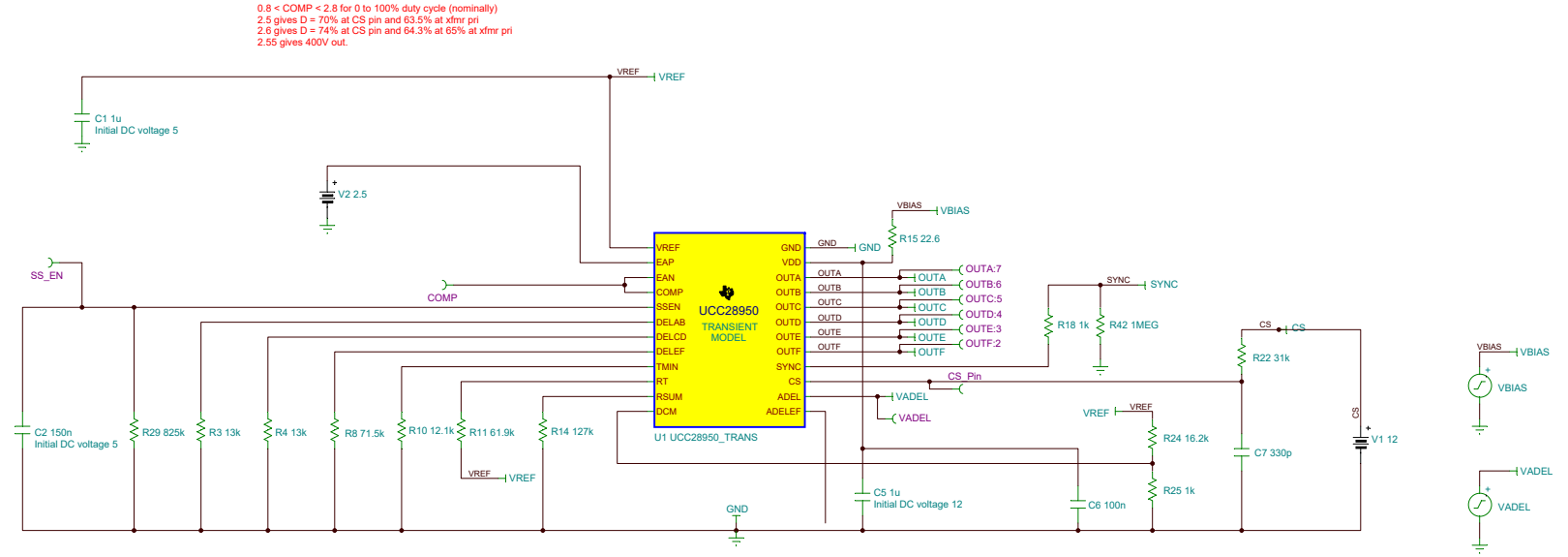
UCC28950 STEADY STATE SIMULATION

Title	**UCC28950 Green Phase-Shifted Full-Bridge Controller with Synchronous Rectification**		
Size	Document No. **Datasheet: SLUSA16D EVM: SLUU421A**		Rev **1.0**
Date	**JULY 25, 2019**	Sheet **1** of **1**	



TEST BENCH DESCRIPTION:
1. The UCC28950 transient model is encrypted and will only run in PSPICE Versions 15.7 and up.
2. The test bench has been configured for VIN= 410V, VOUT= 12V and IOUT= 50A.
3. This test bench is corner tested for VIN from 370V to 410V and Iload from 1mA to 50A.
4. All transients can be applied after the output voltage reaches steady-state.
5. Thermal behavior is not modelled.

SIMULATION INFO:
1. Go to Analysis >> Options and ensure the following:
   a) Under 'Trace Mode' section, select 'percentage bar' option
   b) Under 'Transient' section, ensure the 'Integration method' is 'Gear' and the 'Integration order' is '2'
   c) Check 'Enable run time statistics'
   d) Check 'Enable instant diagram drawing'
   e) Check 'Save all analysis results'
   f) Check 'Disable warning for large size analysis results'
   g) Check 'Remember diagram settings'
2. Analysis parameters may have been modified from Default settings to ensure convergence and/or reduce simulation time.
3. To run simulation, go to Analysis >> Transient and ensure the following:
   a) 'Draw excitation' is unchecked
   b) Click 'OK'
4. The simulation runs for 600us and takes approximately 6 mins 25 secs on a 4 core 2.8 GHz machine.