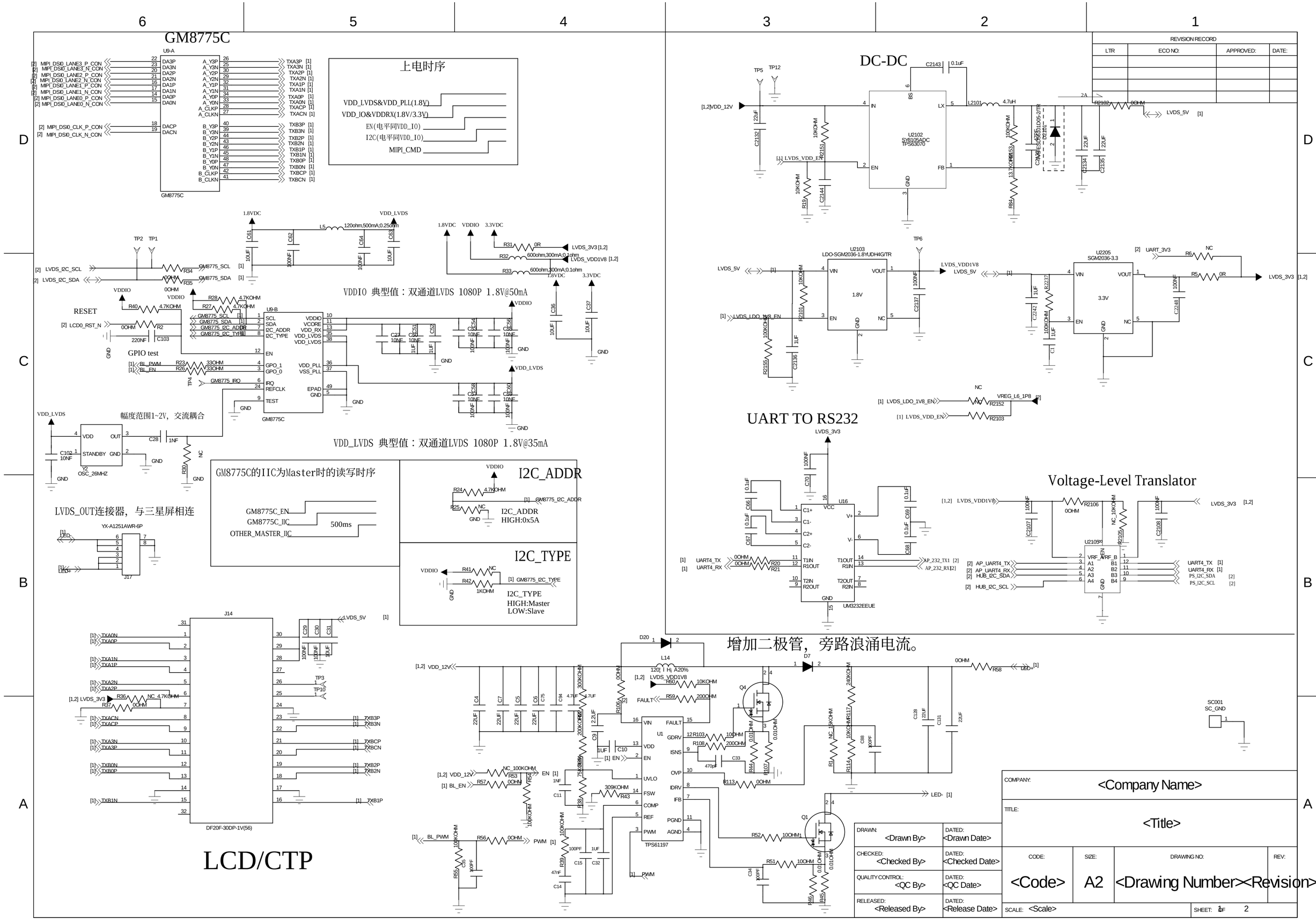
GM8775C
U9-A
上电时序
VDD_LVDS&VDD_PLL(1.8V)
VDD_IO&VDDRX(1.8V/3.3V)
EN(电平同VDD_IO)
I2C(电平同VDD_IO)
MIPI_CMD
VDDIO 典型值：双通道LVDS 1080P 1.8V@50mA
RESET
GPIO test
VDD_LVDS 典型值：双通道LVDS 1080P 1.8V@35mA
幅度范围1~2V，交流耦合
LVDS_OUT连接器，与三星屏相连
GM8775C的IIC为Master时的读写时序
500ms
I2C_ADDR
I2C_ADDR
HIGH:0x5A
I2C_TYPE
I2C_TYPE
HIGH:Master
LOW:Slave
LCD/CTP
DC-DC
UART TO RS232
Voltage-Level Translator
增加二极管，旁路浪涌电流。
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
COMPANY:
<Company Name>
TITLE:
<Title>
DRAWN:
<Drawn By>
DATED:
<Drawn Date>
CHECKED:
<Checked By>
DATED:
<Checked Date>
QUALITY CONTROL:
<QC By>
DATED:
<QC Date>
RELEASED:
<Released By>
DATED:
<Release Date>
CODE:
<Code>
SIZE:
A2
DRAWING NO:
<Drawing Number><Revision>
REV:
SCALE: <Scale>
SHEET: 1 OF 2