

**Selected Certification - All\_Cert**

SI No	Applicable MOI
1	Power Delivery 3.1 Tests
2	PD2 Communication Engine Tests
3	PD2 Deterministic Tests
4	USB-C Functional Tests
5	Source Power Tests
6	Power Delivery 3.0 Tests
7	Quick Charger 3.0 Tests
8	Quick Charge 4 Tests
9	Thunderbolt Power Tests
10	DisplayPort Alternate Mode Tests
11	BC1.2 DCP Sink Tests
12	Custom OEM Tests
13	QC3+ Tests
14	IEC Functional Safety Tests
15	Controller Self Test
16	Get Capabilities

**Test Summary - Overall**

MOI Name	Total	PASS	FAIL	INCOMPLETE	NOT_SELECTED	OTHERS
PD_Merged	151	64	8	0	79	0

**PD\_Merged Result**

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
1	TEST.PD.PROT.ALL3.8	TEST.PD.PROT.ALL3.8 Get Revision Response	FAIL
		COMMON.PROC.BU.1	PASS
		COMMON.PROC.BU.1 - REVISION_3_0 Rev3ChkdSrc	PASS
		First source capability timer - COMMON.PROC.BU.1#1 >>Vbus up time: 0.513s and Sourcecap time: 0.63s [PASS] Max = 250ms. Obtained time difference is 116.655ms	PASS
		DUT responded with accept message - COMMON.PROC.BU.1#2 >>Packet#22	PASS
		tSrcTransReq timer check - COMMON.PROC.BU.1#3 >>[PASS] Max = 325ms. Obtained time difference is 35.918ms Packet#24	PASS
		Rev3ChkdSrc	FAIL
		Get_Revision response check - TEST.PD.PROT.ALL3.8#1 >>UUT responded with Not_Supported message for Tester's Get_Revision message	FAIL
		Revision message details check - TEST.PD.PROT.ALL3.8#2	PASS
2	TEST.PD.PROT.SRC.7	TEST.PD.PROT.SRC.7 DR_Swap	FAIL
		COMMON.PROC.BU.1	PASS
		COMMON.PROC.BU.1 - REVISION_2_0 Rev2Src	PASS
		First source capability timer - COMMON.PROC.BU.1#1 >>Vbus up time: 0.512s and Sourcecap time: 0.626s [PASS] Max = 250ms. Obtained time difference is 114.155ms	PASS
		DUT responded with accept message - COMMON.PROC.BU.1#2 >>Packet#22	PASS
		tSrcTransReq timer check - COMMON.PROC.BU.1#3 >>[PASS] Max = 325ms. Obtained time difference is 35.935ms Packet#24	PASS
		COMMON.PROC.BU.1 - REVISION_3_0 Rev3ChkdSrc	PASS
		First source capability timer - COMMON.PROC.BU.1#1 >>Vbus up time: 7.022s and Sourcecap time: 7.138s [PASS] Max = 250ms. Obtained time difference is 115.822ms	PASS
		DUT responded with accept message - COMMON.PROC.BU.1#2 >>Packet#63	PASS

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		tSrcTransReq timer check - COMMON.PROC.BU.1#3 >>[PASS] Max = 325ms. Obtained time difference is 35.898ms Packet#65	PASS
		Rev2Src	FAIL
		DR_Swap Response Check - TEST.PD.PROT.SRC.7#1 >>UUT respond Reject to DR_Swap message and DR_Swap_To_UFP_Supported field is Yes	FAIL
		DR_Swap Response Check - TEST.PD.PROT.SRC.7#2 >>Check the DRSwap AMS and confirm the test results	FAIL
		Rev3ChkdSrc	FAIL
		DR_Swap Response Check - TEST.PD.PROT.SRC.7#1 >>UUT respond Reject to DR_Swap message and DR_Swap_To_UFP_Supported field is Yes	FAIL
		DR_Swap Response Check - TEST.PD.PROT.SRC.7#2 >>Check the DRSwap AMS and confirm the test results	FAIL
3	TEST.PD.USB4.DRST.5	TEST.PD.USB4.DRST.5 –Data_Reset command response of DFP UUT Sourcing Vconn	FAIL
		COMMON.PROC.BU.1	PASS
		COMMON.PROC.BU.1 - REVISION_3_0 Rev3ChkdSrc	PASS
		First source capability timer - COMMON.PROC.BU.1#1 >>Vbus up time: 0.516s and Sourcecap time: 0.632s [PASS] Max = 250ms. Obtained time difference is 115.822ms	PASS
		DUT responded with accept message - COMMON.PROC.BU.1#2 >>Packet#22	PASS
		tSrcTransReq timer check - COMMON.PROC.BU.1#3 >>[PASS] Max = 325ms. Obtained time difference is 35.89ms Packet#24	PASS
		Rev3ChkdSrc	FAIL
		Data_Reset command response check - TEST.PD.USB4.DRST.5#1 >>UUT respond Accept to Data_Reset command at protocol index 37	PASS
		Accept VCONN voltage check - TEST.PD.USB4.DRST.5#2 >>VCONN voltage at PORTA_CC2_VOLTAGE is 0.0102973236153327v	PASS

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		VCONN Reapplied check - TEST.PD.USB4.DRST.5#3 >>UUT keeps the Vconn below vRaReconnect upto 10.8322500000002 mS	PASS
		Vconn voltage rise check - TEST.PD.USB4.DRST.5#4 >>VCONN voltage at PORTA_CC2_VOLTAGE is 3.62371146979846v	PASS
		Vconn voltage rise time check - TEST.PD.USB4.DRST.5#5 >>VCONN voltage at PORTA_CC2_VOLTAGE is 5.24361491929535v Vconn voltage is reaching to valid range in 0.833249999999897 mS	PASS
		Data_Reset_Complete command check - TEST.PD.USB4.DRST.5#6 >>UUT not initiated any other message between Accept and Data_Reset_Complete	PASS
		Data_Reset_Complete VBUS voltage check - TEST.PD.USB4.DRST.5#7 >>VBUS is not changed and it is in valid range throughout the Data_Reset process. Obtained voltage is 5.24789178411299V	PASS
		Data_Reset_Complete command response time check - TEST.PD.USB4.DRST.5#8 >>Data_Reset_Complete message received in 0.22936915 S at protocol index 39	PASS
		Data_Reset_Complete command error recovery check - TEST.PD.USB4.DRST.5#9	PASS
		Device discovery process check - TEST.PD.USB4.DRST.5#10 >>UUT initiated VendorDefined command at protocol index 42	PASS
		Cable discovery process check - TEST.PD.USB4.DRST.5#11 >>UUT failed to respond VendorDefined command	FAIL
4	TEST.PD.USB4.DRST.6	TEST.PD.USB4.DRST.6 –Data_Reset command response of DFP UUT, UFP Sourcing Vconn	FAIL
		COMMON.PROC.BU.1	PASS
		COMMON.PROC.BU.1 - REVISION_3_0 Rev3ChkdSrc	PASS
		First source capability timer - COMMON.PROC.BU.1#1 >>Vbus up time: 0.548s and Sourcecap time: 0.663s [PASS] Max = 250ms. Obtained time difference is 114.989ms	PASS
		DUT responded with accept message - COMMON.PROC.BU.1#2 >>Packet#22	PASS

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		tSrcTransReq timer check - COMMON.PROC.BU.1#3 >>[PASS] Max = 325ms. Obtained time difference is 35.921ms Packet#24	PASS
		Rev3ChkdSrc	FAIL
		VCONN_Swap command response check - TEST.PD.USB4.DRST.6#1 >>DUT sent Accept message for VCONN_Swap message at protocol index 37 Tester sent PS_RDY message upon Accept message at protocol index 39	PASS
		Data_Reset command response check - TEST.PD.USB4.DRST.6#2 >>UUT respond Accept to Data_Reset command at protocol index 44	PASS
		VCONN Reapplied check - TEST.PD.USB4.DRST.6#3 >>PS_RDY message initiated after 0.12787077s from Accept GoodCRC recieved at protocol index 46 VconnReapplied voltage at PORTA_CC2_VOLTAGE is 0.139526631060226v VconnReapplied measured from time 3.9973749 to 4.0073749	PASS
		VCONN voltage rise check - TEST.PD.USB4.DRST.6#4 >>VCONN voltage at PORTA_CC2_VOLTAGE is 5.24549234264806v Vconn voltage measured at 4.01820815 S	PASS
		Vconn voltage rise time check - TEST.PD.USB4.DRST.6#5 >>Vconn voltage is reaching to valid range in 0.833250000000341 mS	PASS
		Data_Reset_Complete command check - TEST.PD.USB4.DRST.6#6 >>UUT not initiated any other message between Accept and Data_Reset_Complete	PASS
		Data_Reset_Complete VBUS voltage check - TEST.PD.USB4.DRST.6#7 >>VBUS is not changed and it is in valid range throughout the Data_Reset process. Obtained voltage is 5.24711998847333V	PASS
		Data_Reset_Complete command response time check - TEST.PD.USB4.DRST.6#8 >>Data_Reset_Complete message received in 0.22831414 S at protocol index 48	PASS
		Device discovery process check - TEST.PD.USB4.DRST.6#9 >>UUT initiated VendorDefined command at protocol index 51	PASS
		Cable discovery process check - TEST.PD.USB4.DRST.6#10 >>UUT failed to respond VendorDefined command	FAIL

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
5	TEST.PD.USB4.EUSB.1	TEST.PD.USB4.EUSB.1 – Enter_USB Message response of UFP UUT-Valid Mode	FAIL
		COMMON.PROC.BU.1	PASS
		COMMON.PROC.BU.1 - REVISION_3_0 Rev3ChkdSrc	PASS
		First source capability timer - COMMON.PROC.BU.1#1 >>Vbus up time: 0.516s and Sourcecap time: 0.632s [PASS] Max = 250ms. Obtained time difference is 115.822ms	PASS
		DUT responded with accept message - COMMON.PROC.BU.1#2 >>Packet#22	PASS
		tSrcTransReq timer check - COMMON.PROC.BU.1#3 >>[PASS] Max = 325ms. Obtained time difference is 35.888ms Packet#24	PASS
		Rev3ChkdSrc	FAIL
		DR_Swap PD Message response check - TEST.PD.USB4.EUSB.1#1 >>UUT respond Reject to DR_Swap PD Message	FAIL
		VendorDefined PD Message response check - TEST.PD.USB4.EUSB.1#3	FAIL
		Enter_USB PD Message response check - TEST.PD.USB4.EUSB.1#4	FAIL
		Enter_USB PD Message response check - TEST.PD.USB4.EUSB.1#5	FAIL
6	TEST.PD.USB4.EUSB.2	TEST.PD.USB4.EUSB.2 – Enter_USB Message response of UFP UUT-Invalid Mode	FAIL
		COMMON.PROC.BU.1	PASS
		COMMON.PROC.BU.1 - REVISION_3_0 Rev3ChkdSrc	PASS
		First source capability timer - COMMON.PROC.BU.1#1 >>Vbus up time: 0.515s and Sourcecap time: 0.631s [PASS] Max = 250ms. Obtained time difference is 116.655ms	PASS
		DUT responded with accept message - COMMON.PROC.BU.1#2 >>Packet#22	PASS
		tSrcTransReq timer check - COMMON.PROC.BU.1#3 >>[PASS] Max = 325ms. Obtained time difference is 35.88ms Packet#24	PASS
		Rev3ChkdSrc	FAIL
		DR_Swap PD Message response check - TEST.PD.USB4.EUSB.2#1 >>UUT respond Reject to DR_Swap PD Message	FAIL

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		VendorDefined PD Message response check - TEST.PD.USB4.EUSB.2#3	FAIL
		Enter_USB PD Message response check - TEST.PD.USB4.EUSB.2#4	FAIL
7	TEST.PD.USB4.EUSB.4	TEST.PD.USB4.EUSB.4 – DR_Swap after Entering USB4 Mode entry	FAIL
		COMMON.PROC.BU.1	PASS
		COMMON.PROC.BU.1 - REVISION_3_0 Rev3ChkdSrc	PASS
		First source capability timer - COMMON.PROC.BU.1#1 >>Vbus up time: 0.537s and Sourcecap time: 0.652s [PASS] Max = 250ms. Obtained time difference is 114.989ms	PASS
		DUT responded with accept message - COMMON.PROC.BU.1#2 >>Packet#22	PASS
		tSrcTransReq timer check - COMMON.PROC.BU.1#3 >>[PASS] Max = 325ms. Obtained time difference is 35.883ms Packet#24	PASS
		Rev3ChkdSrc	FAIL
		DR_Swap PD Message response check - TEST.PD.USB4.EUSB.4#1 >>UUT respond Reject to DR_Swap PD Message	FAIL
		Tester Enter_USB PD Message response check - TEST.PD.USB4.EUSB.4#3	FAIL
		DR_Swap PD Message response check - TEST.PD.USB4.EUSB.4#4	FAIL
		SOP1 Enter_USB PD Message response check - TEST.PD.USB4.EUSB.4#5	FAIL
		SOP2 Enter_USB PD Message response check - TEST.PD.USB4.EUSB.4#6	FAIL
		SOP Enter_USB PD Message parameters of EUDO check - TEST.PD.USB4.EUSB.4#7	FAIL
		SOP Enter_USB PD Message response expiry time check - TEST.PD.USB4.EUSB.4#8	FAIL
		DR_Swap PD Message response check - TEST.PD.USB4.EUSB.4#9	FAIL
		Tester Enter_USB PD Message response check - TEST.PD.USB4.EUSB.4#10	FAIL
		Tester Request PD Message response check - TEST.PD.USB4.EUSB.4#11	FAIL
8	2.1	Common Checks	FAIL

# GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		Common_Check_PD_1_Check_Preamble - COMMON.CHECK.PD.1	PASS
		Check Preamble sequence and count - COMMON.CHECK.PD.1#1	PASS
		Common_Check_PD_2_Check_Message_Header - COMMON.CHECK.PD.2	PASS
		Check message header fields - COMMON.CHECK.PD.2#1	PASS
		Common_Check_PD_3_Check_GoodCRC - COMMON.CHECK.PD.3	PASS
		Check goodCRC response time - COMMON.CHECK.PD.3#1	PASS
		Check goodCRC message header fields - COMMON.CHECK.PD.3#2	PASS
		Common_Check_PD_4_Check_Atomic_Message_Sequence - COMMON.CHECK.PD.4	PASS
		Check Atomic message sequence - COMMON.CHECK.PD.4#1	PASS
		Common_Check_PD_5_Check_Unexpected_Messages_And_Signals - COMMON.CHECK.PD.5	PASS
		Unexpected Soft reset - COMMON.CHECK.PD.5#1	PASS
		Unexpected Hard Reset or a cable reset - COMMON.CHECK.PD.5#2	PASS
		Unexpected GotoMin - COMMON.CHECK.PD.5#3	PASS
		Unexpected messages - COMMON.CHECK.PD.5#4	PASS
		Common_Check_PD_6_Control_Message - COMMON.CHECK.PD.6	PASS
		Number of data objects in header should be zero - COMMON.CHECK.PD.6#1	PASS
		Common_Check_PD_7_Source_Capability_Message - COMMON.CHECK.PD.7	PASS
		Check Source Capability message - COMMON.CHECK.PD.7#1	PASS
		Check Source Capability message - COMMON.CHECK.PD.7#2	PASS
		Check Data Objects field - COMMON.CHECK.PD.7#3	PASS
		Check First PDO - COMMON.CHECK.PD.7#4	PASS
		Check Fixed PDO - COMMON.CHECK.PD.7#5	PASS
		Check PPS Validation - COMMON.CHECK.PD.7#6	PASS
		Check Power Rules - COMMON.CHECK.PD.7#7	PASS
		Check PDO Consistency - COMMON.CHECK.PD.7#8	PASS
		Check PDO Sequence - COMMON.CHECK.PD.7#9	PASS
		Check Fixed PDO Voltage - COMMON.CHECK.PD.7#10	PASS
		Check Variable PDO Voltage - COMMON.CHECK.PD.7#11	PASS
		Check Battery PDO Voltage - COMMON.CHECK.PD.7#12	PASS
		Common_Check_PD_8_Request_Message - COMMON.CHECK.PD.8	PASS



## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		Request messages fields check - COMMON.CHECK.PD.8#1	PASS
		Common_Check_PD_9_Structured_VDM - COMMON.CHECK.PD.9	FAIL
		Structured VDM header field check - COMMON.CHECK.PD.9#1 >>TEST.PD.PROT.ALL3.8: CommandVersion [b[12:11]] Obt:0 and Exp: 1. Packet#14, 27, TEST.PD.PROT.SRC.7: CommandVersion [b[12:11]] Obt:0 and Exp: 1. Packet#14, 55, 68, TEST.PD.USB4.DRST.5: CommandVersion [b[12:11]] Obt:0 and Exp: 1. Packet#14, 27, 42, TEST.PD.USB4.DRST.6: CommandVersion [b[12:11]] Obt:0 and Exp: 1. Packet#14, 27, 51, TEST.PD.USB4.EUSB.1: CommandVersion [b[12:11]] Obt:0 and Exp: 1. Packet#14, 27, TEST.PD.USB4.EUSB.2: CommandVersion [b[12:11]] Obt:0 and Exp: 1. Packet#14, 27, TEST.PD.USB4.EUSB.4: CommandVersion [b[12:11]] Obt:0 and Exp: 1. Packet#14, 27, 31,	FAIL
		Common_Check_PD_10_Extended_Message_Header - COMMON.CHECK.PD.10	PASS
		Check Extended Message Header - COMMON.CHECK.PD.10#1	PASS
		Common_Check_PD_11_Source_Capability_Extended_Message - COMMON.CHECK.PD.11	PASS
		Source capabilities extended message fields check - COMMON.CHECK.PD.11#1	PASS
		Common_Check_PD_12_Check_Sink_Capabilities - COMMON.CHECK.PD.12	PASS
		Sink capabilities fields check - COMMON.CHECK.PD.12#1	PASS
		Common_Check_PD_13_Check_Correct_Use_of_Rp - COMMON.CHECK.PD.13	PASS
		Rp Level Validation - COMMON.CHECK.PD.13#1	PASS
		Common_Check_PD_14_Check_Hard_Reset - COMMON.CHECK.PD.14	PASS
		Check Hard_Reset basic timing - COMMON.CHECK.PD.14#1	PASS
		Common_Check_PD_15_Check_Sink_Capabilities_Extended_Message - COMMON.CHECK.PD.15	PASS

# GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		Sink capabilities extended message fields check - COMMON.CHECK.PD.15#1	PASS
		COMMON_CHECK_PD3_1_Check_EPR_Request_Message - COMMON.CHECK.PD3.1	PASS
		EPR_Request messages fields check - COMMON.CHECK.PD3.1#1	PASS
		COMMON_CHECK_PD3_2_Check_EPR_Mode_Message - COMMON.CHECK.PD3.2	PASS
		EPR_Mode messages fields check - COMMON.CHECK.PD3.2#1	PASS
		COMMON_CHECK_PD3_3_Check_EPR_Source_Capabilities_Message - COMMON.CHECK.PD3.3	PASS
		VIF field EPR_Supported_As_Src check - COMMON.CHECK.PD3.3#1	PASS
		First Fixed PDO consistency check - COMMON.CHECK.PD3.3#2	PASS
		Fixed PDO check - COMMON.CHECK.PD3.3#3	PASS
		Programmable Power Supply APDO check - COMMON.CHECK.PD3.3#4	PASS
		EPR PDOs power rules check - COMMON.CHECK.PD3.3#5	PASS
		EPR PDOs consistency check - COMMON.CHECK.PD3.3#6	PASS
		SPR PDOs check - COMMON.CHECK.PD3.3#7	PASS
		COMMON_CHECK_PD3_4_Check_EPR_Sink_Capabilities_Message - COMMON.CHECK.PD3.4	PASS
		EPR_Sink_Capabilities fields check - COMMON.CHECK.PD3.4#1	PASS
9	2.2	Common Procedures	PASS
		COMMON_PROC_PD_2_UUT_Sent_Get_Source_Cap - COMMON.PROC.PD.2	PASS
		Validate Get source capabilities message initiated by DUT - COMMON.PROC.PD.2#1	PASS
		DUT's Request message validation - COMMON.PROC.PD.2#2	PASS
		COMMON_PROC_PD_3_UUT_Sent_Get_Sink_Cap - COMMON.PROC.PD.3	PASS
		Validate Get sink cap message initiated by DUT - COMMON.PROC.PD.3#1	PASS
		COMMON_PROC_PD_4_UUT_Sent_Ping - COMMON.PROC.PD.4	PASS
		Ping message initiated by DUT - COMMON.PROC.PD.4#1	PASS
		COMMON_PROC_PD_5_UUT_Sent_PR_Swap - COMMON.PROC.PD.5	PASS
		PR_Swap valid condition check - COMMON.PROC.PD.5#1	PASS
		PR_Swap init and VIF field value comparison - COMMON.PROC.PD.5#2	PASS

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		PR_Swap init and VIF field value comparison - COMMON.PROC.PD.5#3	PASS
		COMMON_PROC_PD_6_UUT_Sent_VCONN_Swap - COMMON.PROC.PD.6	PASS
		Vconn_Swap valid condition check - Tester Vconn Source - COMMON.PROC.PD.6#1	PASS
		Vconn_Swap init and VIF field value comparison - COMMON.PROC.PD.6#2	PASS
		tVCONNSourceOn Timer Validation - COMMON.PROC.PD.6#3	PASS
		Vconn_Swap valid condition check - DUT Vconn Source - COMMON.PROC.PD.6#4	PASS
		COMMON_PROC_PD_7_UUT_Sent_Discover_Identity_Request - COMMON.PROC.PD.7	PASS
		Validate Discover ID request message initiated by DUT - COMMON.PROC.PD.7#1	PASS
		Structured VDM Message Header check - COMMON.PROC.PD.7#2	PASS
		Tester's VDM response check - COMMON.PROC.PD.7#3	PASS
		COMMON_PROC_PD_8_UUT_Sent_Discover_SVIDs_Request - COMMON.PROC.PD.8	PASS
		Validate Discover ID request message initiated by DUT - COMMON.PROC.PD.8#1	PASS
		Structured VDM Message Header check - COMMON.PROC.PD.8#2	PASS
		Tester's VDM response check - COMMON.PROC.PD.8#3	PASS
		COMMON_PROC_PD_9_UUT_Sent_Attention - COMMON.PROC.PD.9	PASS
		Validate attention request message initiated by DUT - COMMON.PROC.PD.9#1	PASS
		Structured VDM message header check - COMMON.PROC.PD.9#2	PASS
		COMMON_PROC_PD_10_UUT_Sent_Request - COMMON.PROC.PD.10	PASS
		Validate request message initiated by DUT - COMMON.PROC.PD.10#1	PASS
		COMMON_PROC_PD_11_UUT_Sent_Source_Capabilities - COMMON.PROC.PD.11	PASS
		Validate Source capabilities message initiated by DUT - COMMON.PROC.PD.11#1	PASS
		DUT should respond with Accept - COMMON.PROC.PD.11#2	PASS
		DUT should send PS_RDY - COMMON.PROC.PD.11#3	PASS

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		Wait Message - COMMON.PROC.PD.11#4	PASS
		COMMON_PROC_PD_12_UUT_Sent_DR_Swap - COMMON.PROC.PD.12	PASS
		Validate DR_Swap message initiated by DUT - COMMON.PROC.PD.12#1	PASS
		COMMON_PROC_PD_17_Tester_Sent_Vconn_swap_message - COMMON.PROC.PD.17	PASS
		VCONN present check - COMMON.PROC.PD.17#1	PASS
		PS_RDY is missing - COMMON.PROC.PD.17#2	PASS
		VCONN present check - COMMON.PROC.PD.17#3	PASS
		tVONNSourceOff timer check - COMMON.PROC.PD.17#4	PASS
		COMMON_PROC_PD3_1_Sink_Start_an_AMS - COMMON.PROC.PD3.1	PASS
		Sink Start AMS - COMMON.PROC.PD3.1#1	PASS
		COMMON_PROC_PD3_2_UUT_Sent_EPR_Source_Cap_message - COMMON.PROC.PD3.2	PASS
		Validate EPR Source capabilities message initiated by DUT - COMMON.PROC.PD3.2#1	PASS
		DUT should respond with Accept - COMMON.PROC.PD3.2#2	PASS
		DUT should send PS_RDY - COMMON.PROC.PD3.2#3	PASS
		COMMON_PROC_PD3_3_UUT_Sent_EPR_Get_Source_Cap - COMMON.PROC.PD3.3	PASS
		Validate EPR Get Source Capabilities message initiated by DUT - COMMON.PROC.PD3.3#1	PASS
		DUT's EPR Request message validation - COMMON.PROC.PD3.3#2	PASS
		COMMON_PROC_PD3_4_UUT_Sent_EPR_Request - COMMON.PROC.PD3.4	PASS
		Validate EPR Request message initiated by DUT - COMMON.PROC.PD.3.4#1	PASS
		COMMON_PROC_PD3_5_Tester_Sent_EPR_Mode_Enter - COMMON.PROC.PD3.5	PASS
		Validate EPR Enter Ack initiated by DUT - COMMON.PROC.PD3.5#1	PASS
		VIF Field Captive Cable is set to No and UUT is not a Vconn source - COMMON.PROC.PD3.5#2	PASS
		UUT sends a Vconn_Swap message - COMMON.PROC.PD3.5#3	PASS

## GRL-USB-PD Compliance Test Solution

SI No	Test ID	Test Name	Test Result
		UUT does not send a SOP1 Discover Identity message - COMMON.PROC.PD3.5#4	PASS
		UUT sends a SOP1 Discover Identity message - COMMON.PROC.PD3.5#5	PASS
		EPR Mode Enter Succeeded message check - COMMON.PROC.PD3.5#6	PASS
		EPR Source Cap message - COMMON.PROC.PD3.5#7	PASS
		UUT establishes EPR contract - COMMON.PROC.PD3.5#8	PASS
		COMMON_PROC_PD3_6_UUT_Sent_EPR_Mode_Enter - COMMON.PROC.PD3.6	PASS
		Validate EPR_Mode_Enter initiated by DUT - COMMON.PROC.PD3.6#1	PASS
		Validate EPR_Mode_Enter response - COMMON.PROC.PD3.6#2	PASS
		Tester sends a Vconn_Swap message - COMMON.PROC.PD3.6#3	PASS
		Validate EPR_Mode_Enter failed message - COMMON.PROC.PD3.6#4	PASS
		Validate SOP' Discover_Id and EPR Mode Enter Succeeded message - COMMON.PROC.PD3.6#5	PASS
		EPR_Source_Cap message - COMMON.PROC.PD3.6#6	PASS
		UUT establishes EPR contract - COMMON.PROC.PD3.6#7	PASS
		COMMON_PROC_PD3_7_Tester_Sends_EPR_KeepAlive_Message - COMMON.PROC.PD3.7	PASS
		Validate EPR_KeepAlive response message initiated by DUT - COMMON.PROC.PD3.7#1	PASS

