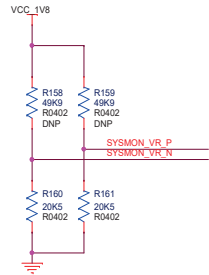


FPGA POWER



Internal Pull Up-Down resistor control PIN

