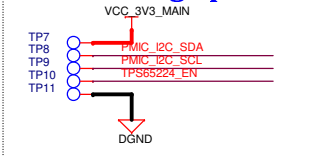


PMIC Config option



Silk: PMIC_PROG

SOC POWER SUPPLY PMIC

CAD Note :-
Follow Kelvin connection for Current Sensing when using 2 terminal resistors

Route as Pseudo differential pair trace from Load (Remote Sense)
(See "PCB Notes")

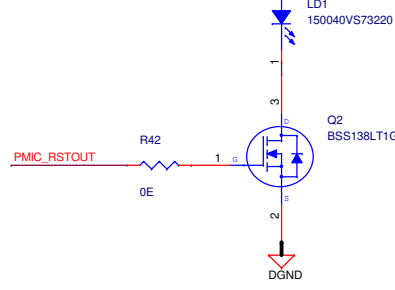
PMIC uses default I2C1 ADDR: 0x48,0x49, 0x4A, 0x4B

PMIC PUSH BUTTON LOGIC

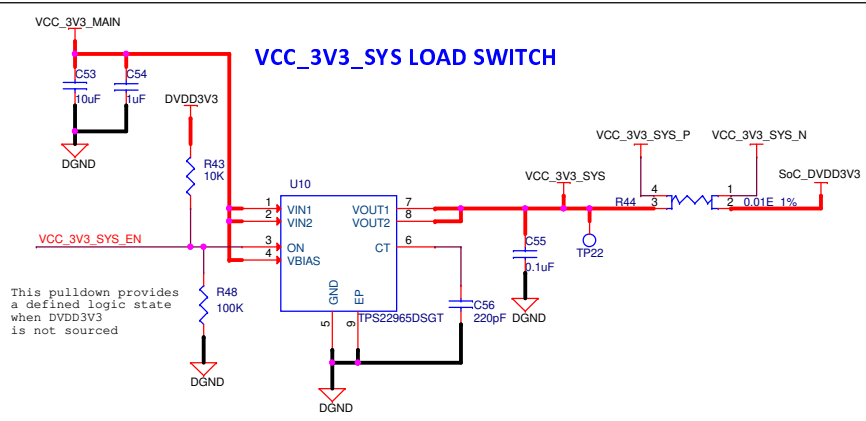
R41	VDD_CORE VOLTAGE
Mounted	0.85 V (DEFAULT)
Not Mounted	0.75 V

Note: When R157 is DNI, Push Button (SW2) becomes operational through internal pullup on PMIC GPIO3

PMIC POWER INDICATION LED



VCC_3V3_SYS LOAD SWITCH



This pulldown provides a defined logic state when DVDD3V3 is not sourced

Title			<Title>
Size	Document Number	Rev	<RevCode>
C	<Doc>		
Date:	Friday, July 12, 2024	Sheet	7 of 35