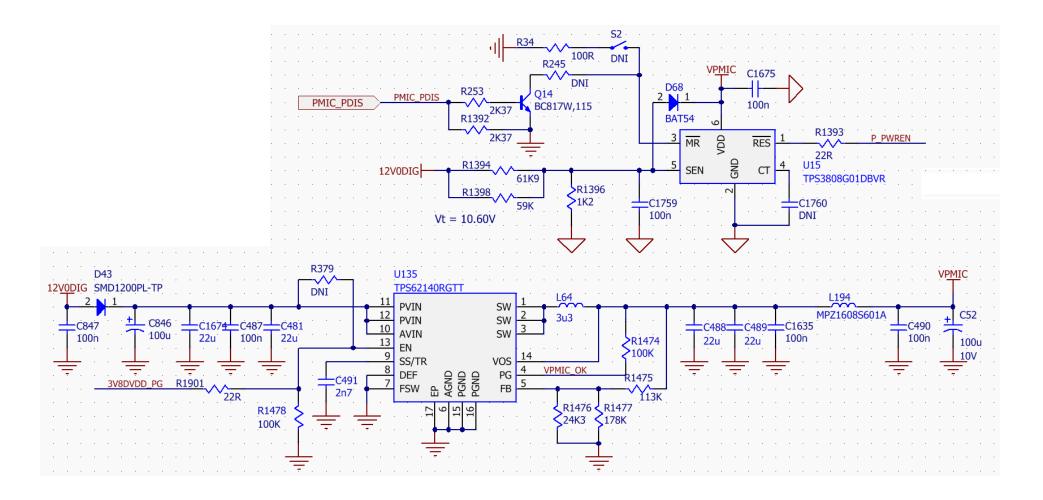


Here is the PMIC sch.



Here is the DCDC providing the Vin PMIC (VINDCDCx) ; VPMIC = +5.2V;

and the Voltage supervisor sch controlling the PWR_EN (U14 PMIC pin 9). Vsense is 10.6V. This is the going up and down voltage where the PWR_EN transitions from LOW to HIGH and HIGH to LOW

Note:

"DNI" marked components are "Not Installed"