



Build on PMP21993 Rev A PCB
 Connect T2-3 to PGND

PCB LOGO
 FCC disclaimer

PCB LOGO
 WEEE logo

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: No	Designed for: Public Release	Mod. Date: 1/17/2020
TID #: N/A	Project Title: Class 0 PoE Sync Flyback	
Number: PMP21993	Rev: B	Sheet Title: 5V/2.3A
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 1
Drawn By: D Strasser	File: PMP21993_RevB_SH1.SchDoc	Size: B
Engineer: D Strasser	Contact: N/A	

http://www.ti.com
 © Texas Instruments 2019