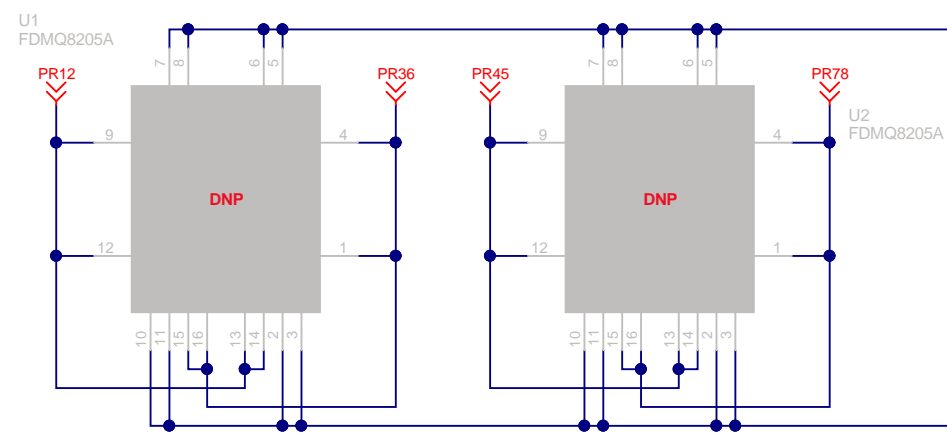
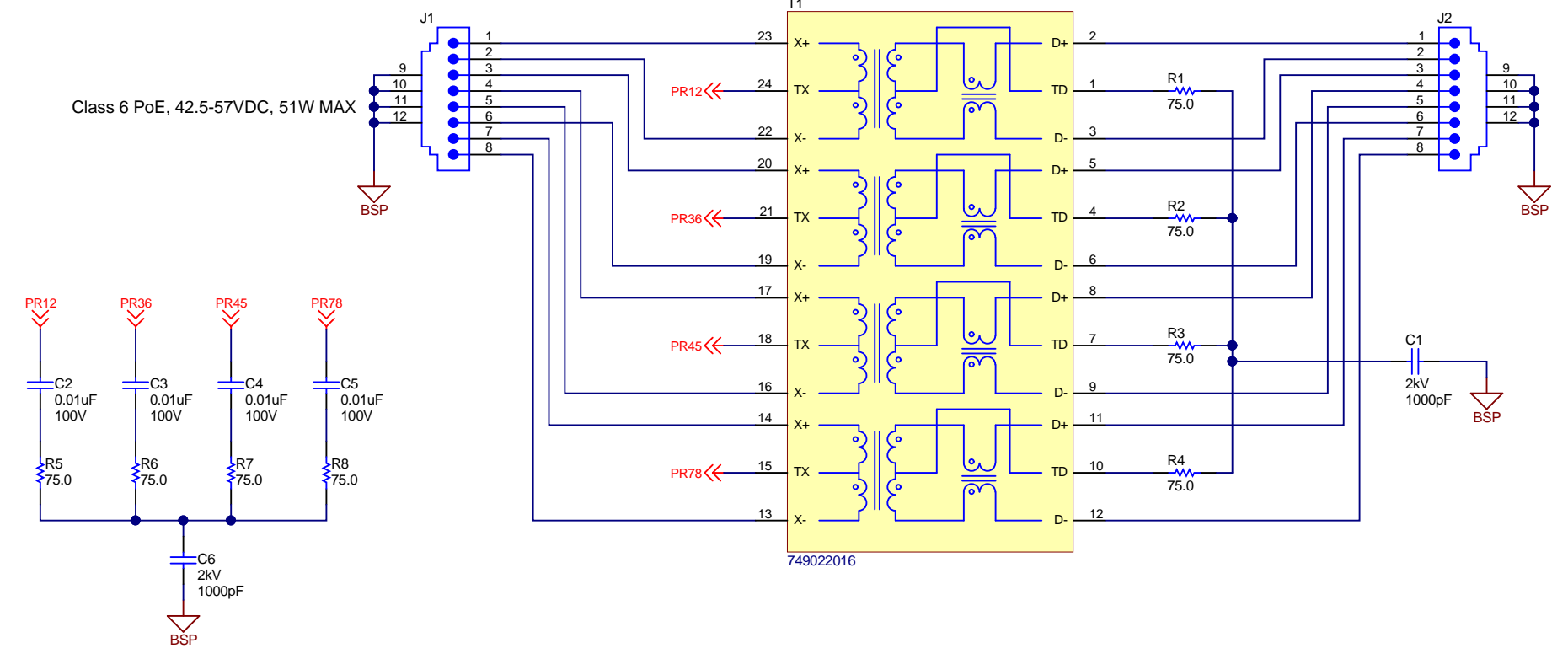
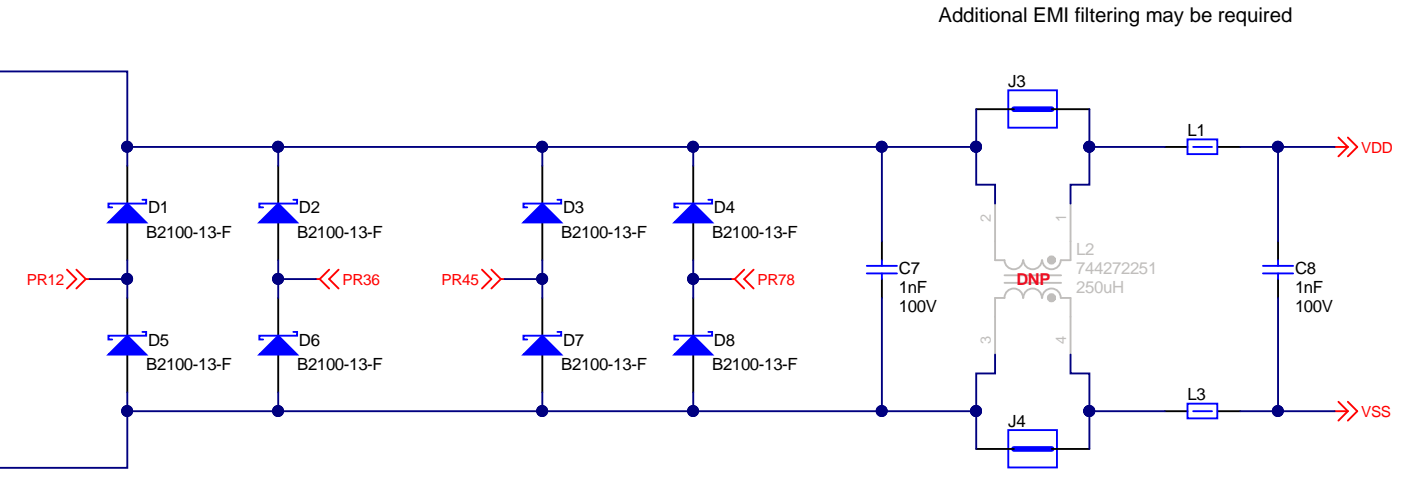


Class 6 PoE, 42.5-57VDC, 51W MAX



OPTIONAL INTEGRATED FET BRIDGE FOR HIGHER EFFICIENCY AND SMALLER SIZE



Additional EMI filtering may be required

PRELIMINARY

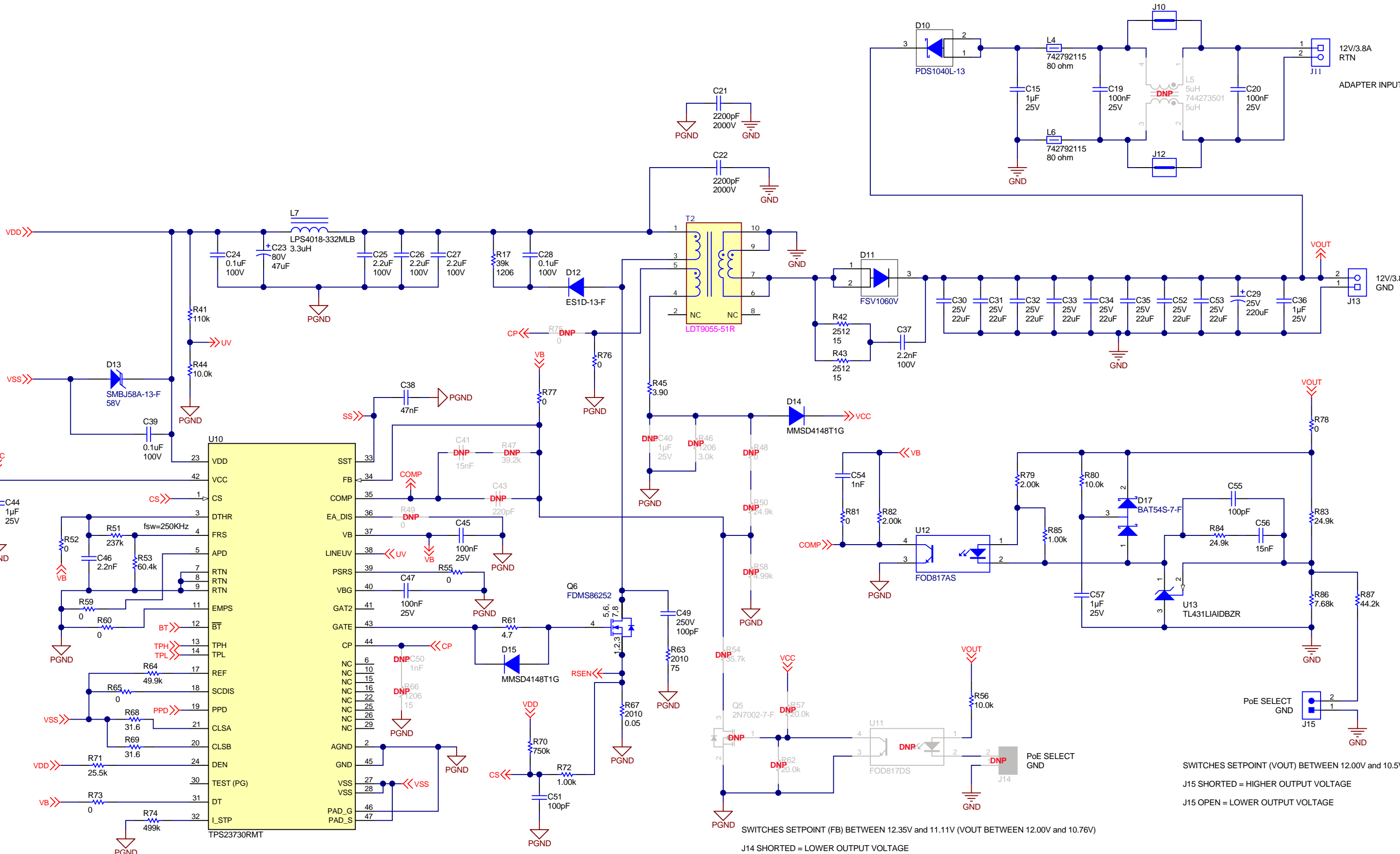
Logo1
PCB
LOGO
WEEE logo

Logo2
PCB
LOGO
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Orderable: No	Designed for: Public Release	Mod. Date: 5/21/2020
TID #: N/A	Project Title: Class 6 PoE PD (12V/3.8A) with Input Power Limit	
Number: PMP22494	Rev: A	Sheet Title: PoE Input
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 1 of 3
Drawn By: D Strasser	File: PMP22494_RevA_SH1.SchDoc	Size: B
Engineer: D Strasser	Contact: N/A	





SWITCHES SETPOINT (VOUT) BETWEEN 12.00V and 10.5V
 J15 SHORTED = HIGHER OUTPUT VOLTAGE
 J15 OPEN = LOWER OUTPUT VOLTAGE

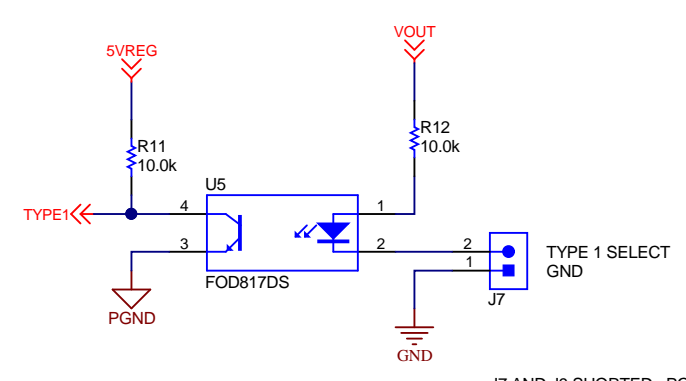
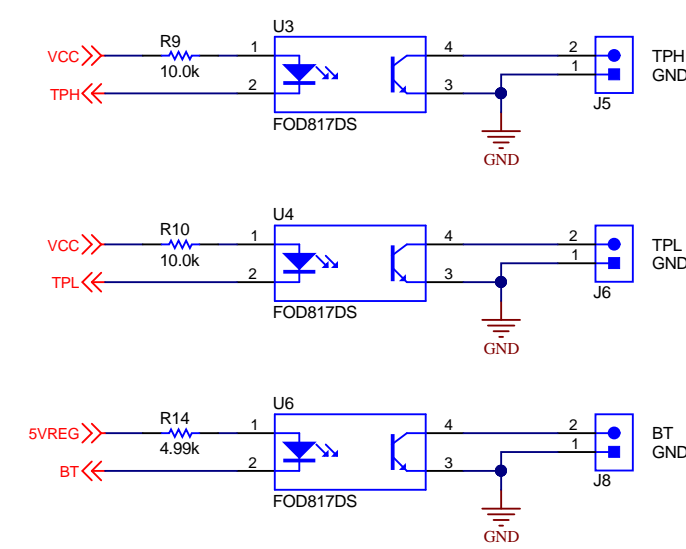
SWITCHES SETPOINT (FB) BETWEEN 12.35V and 11.11V (VOUT BETWEEN 12.00V and 10.76V)
 J14 SHORTED = LOWER OUTPUT VOLTAGE
 J14 OPEN = HIGHER OUTPUT VOLTAGE

PRELIMINARY

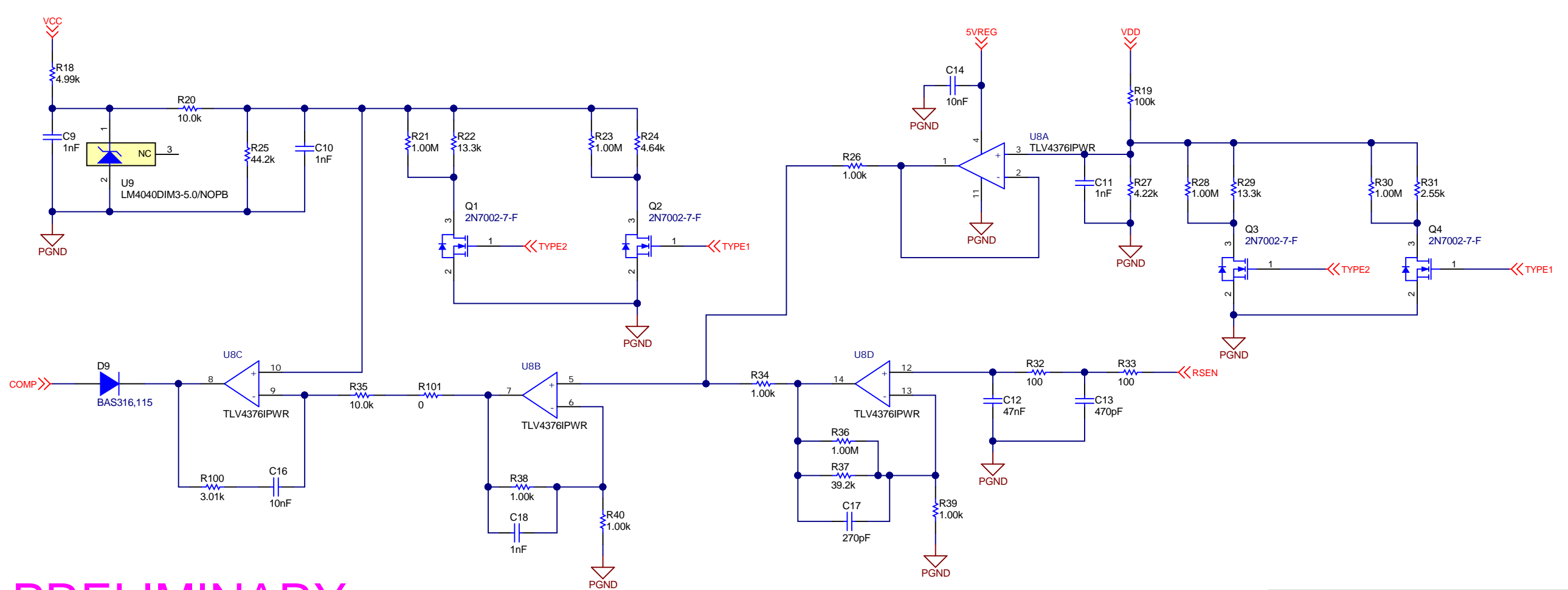
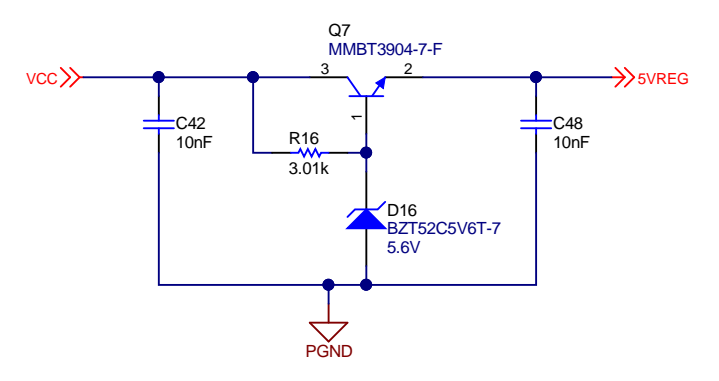
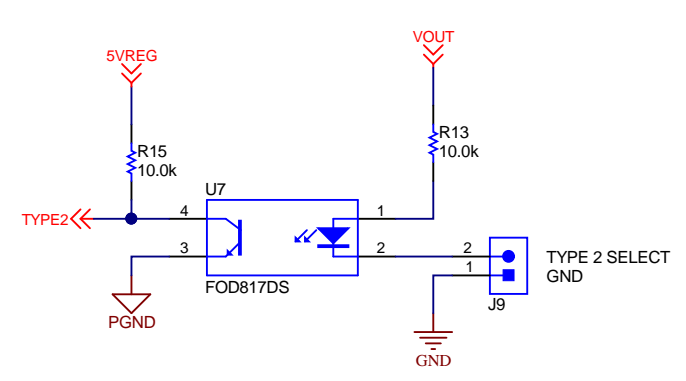
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Orderable: No	Designed for: Public Release	Mod. Date: 5/21/2020
TID #: N/A	Project Title: Class 6 PoE PD (12V/3.8A) with Input Power Limit	
Number: PMP22494	Rev: A	Sheet Title: Flyback Converter (12V/3.8A)
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 3
Drawn By: D Strasser	File: PMP22494_RevA_SH2.SchDoc	Size: B
Engineer: D Strasser	Contact: N/A	

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J7 AND J9 SHORTED: POWER LIMIT IS TYPE 3
 J7 SHORTED AND J9 OPEN: POWER LIMIT IS TYPE 2
 J7 AND J9 OPEN: POWER LIMIT IS TYPE 1



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Orderable: No	Designed for: Public Release	Mod. Date: 5/21/2020
TID #: N/A	Project Title: Class 6 PoE PD (12V/3.8A) with Input Power Limit	
Number: PMP22494	Rev: A	Sheet Title: Input Power Limit Circuit
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 3
Drawn By: D Strasser	File: PMP22494_RevA_SH3.SchDoc	Size: B
Engineer: D Strasser	Contact: N/A	

