

A

B

C

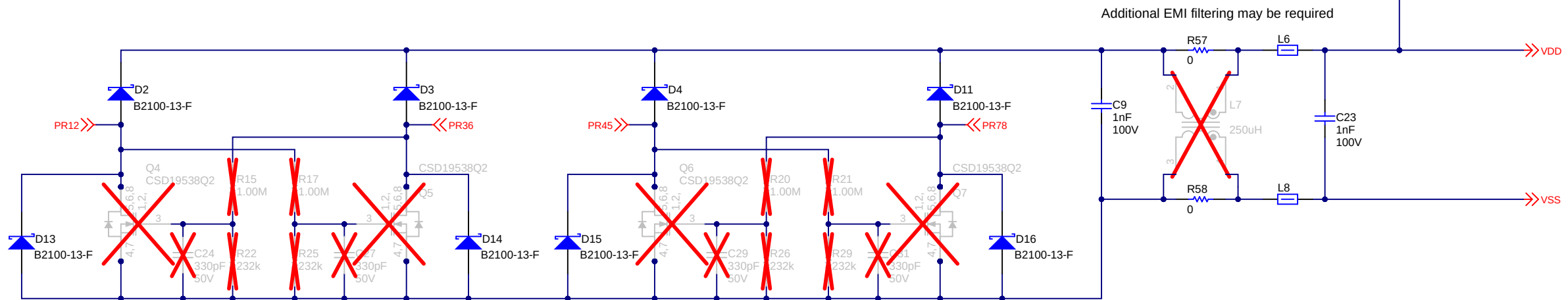
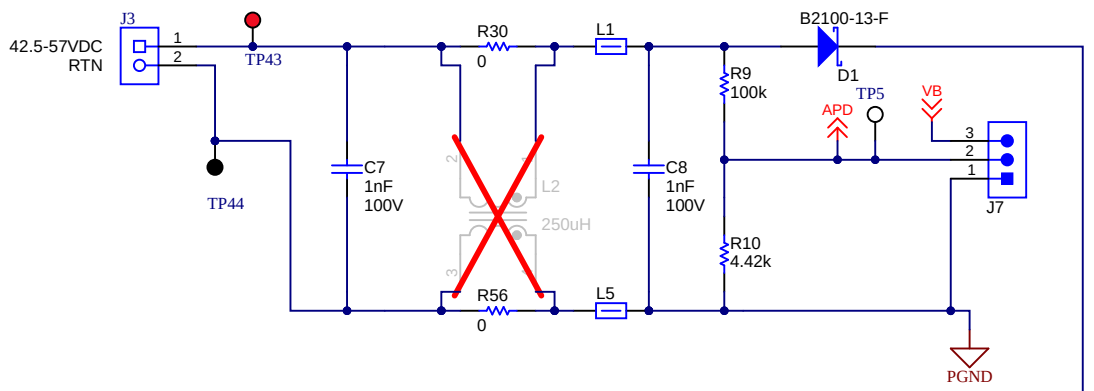
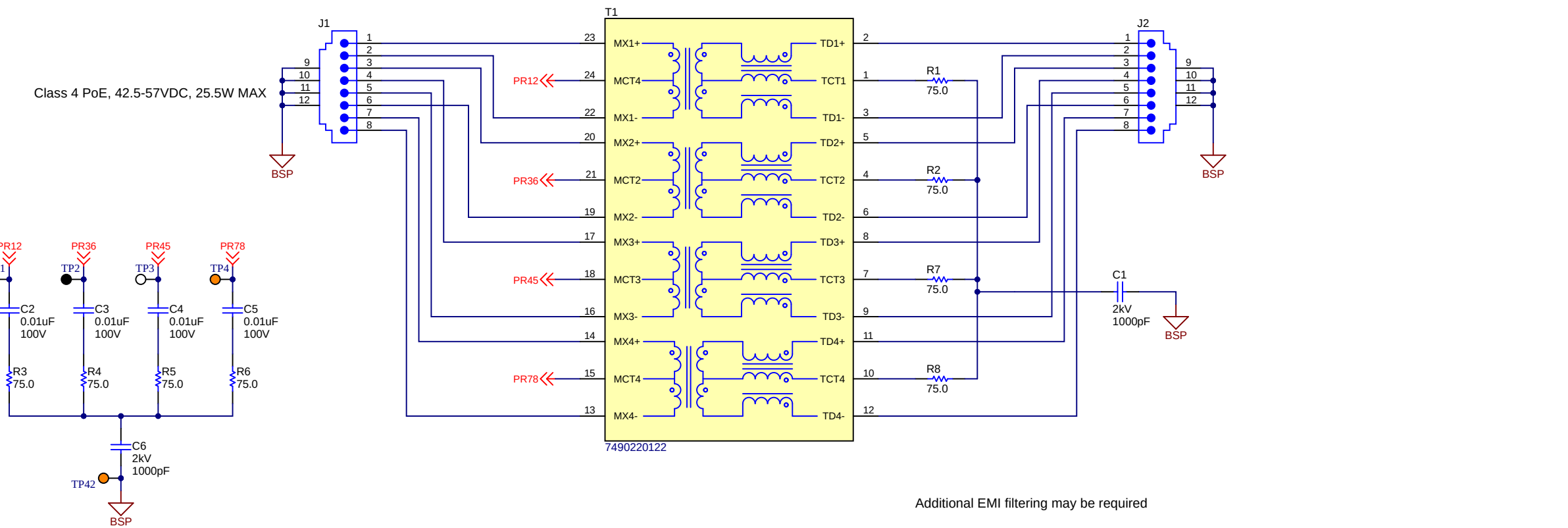
D

A

B

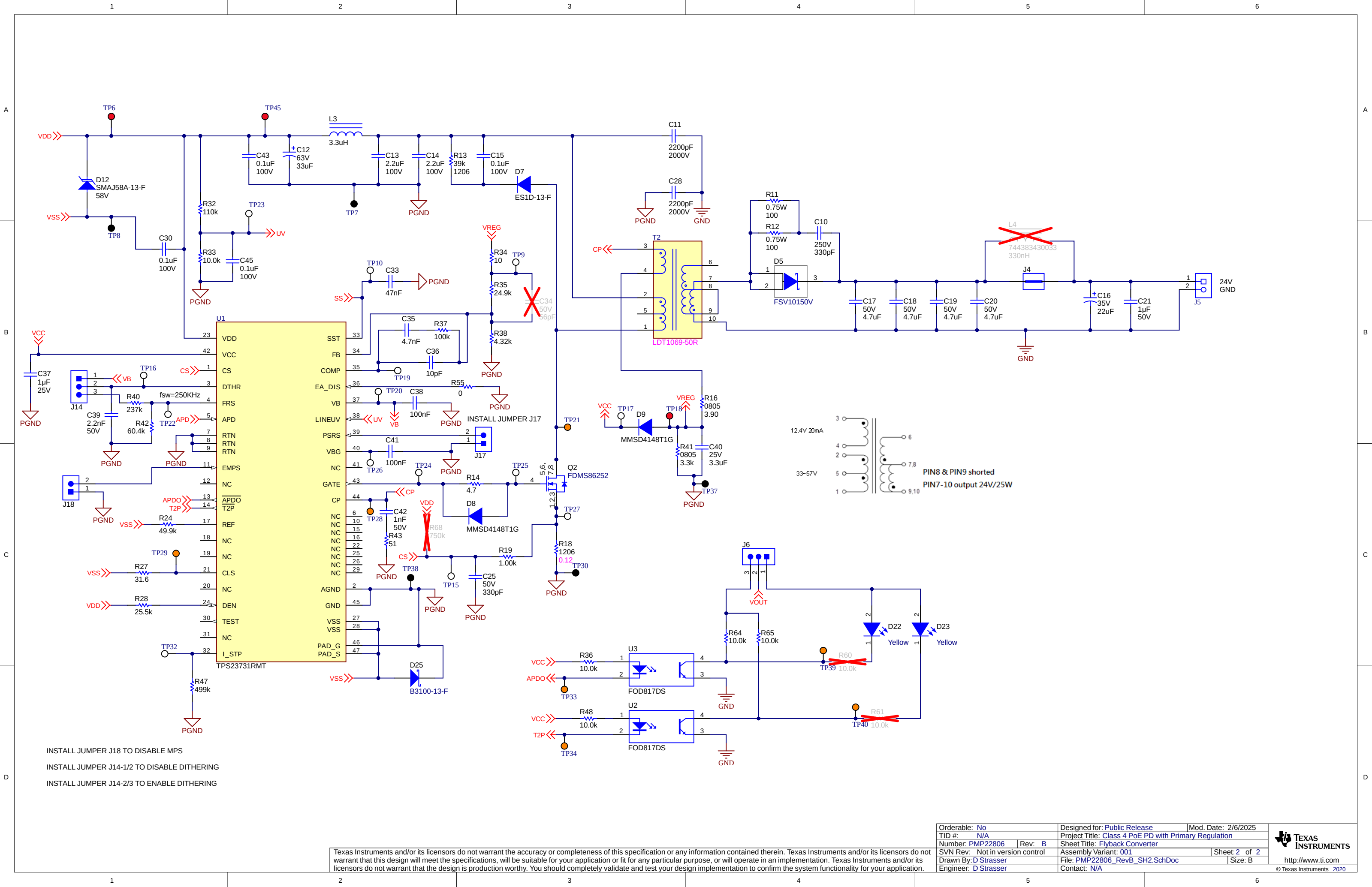
C

D



Logo1
PCB
LOGO
FCC disclaimer

Logo2
PCB
LOGO
WEEE logo



INSTALL JUMPER J18 TO DISABLE MPS
INSTALL JUMPER J14-1/2 TO DISABLE DITHERING
INSTALL JUMPER J14-2/3 TO ENABLE DITHERING

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: No	Designed for: Public Release	Mod. Date: 2/6/2025
TID #: N/A	Project Title: Class 4 PoE PD with Primary Regulation	
Number: PMP22806	Rev: B	Sheet Title: Flyback Converter
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 2
Drawn By: D Strasser	File: PMP22806_RevB_SH2.SchDoc	Size: B
Engineer: D Strasser	Contact: N/A	