

D

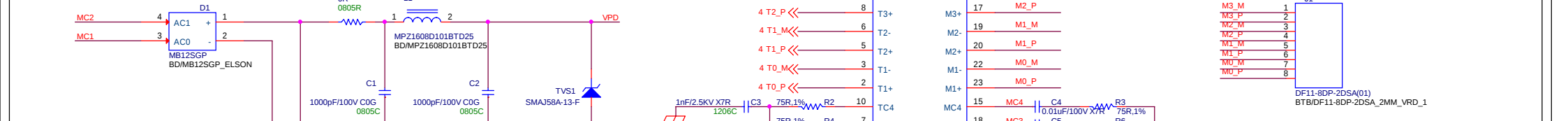
R1
B

11

4 T2_M<< 9 T3-
M3- 16 M2_M

11

D



	D2	R5		GND_Chassis	TC3	MC3	0.01uF/100V X7R	75R,1%
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The circuit diagram shows two operational amplifier (op-amp) stages. The first stage has an input labeled AC1, a feedback resistor R7 with a value of 75R and 1% tolerance, and its output is labeled TC2. The second stage has two inputs, MC2 and C6, a feedback resistor R8 with a value of 75R and 1% tolerance, and its output is also labeled TC2. A capacitor C6 is connected between the two stages.

