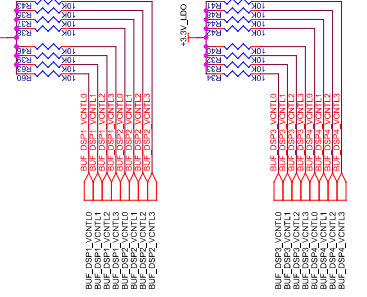
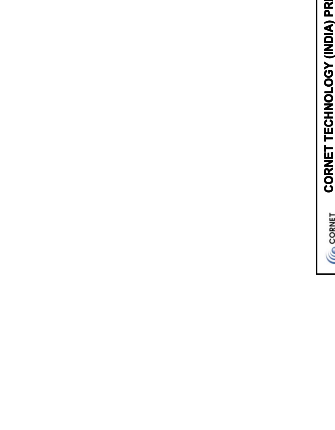


• 3.3V_LDO



+3 3V CPLD GENERATION



[illegible][illegible]