

| Revision History |       |               |             |       |
|------------------|-------|---------------|-------------|-------|
| Rev              | ECN # | Approved Date | Approved by | Notes |
| N/A              | N/A   | N/A           | N/A         | N/A   |

A

B

C

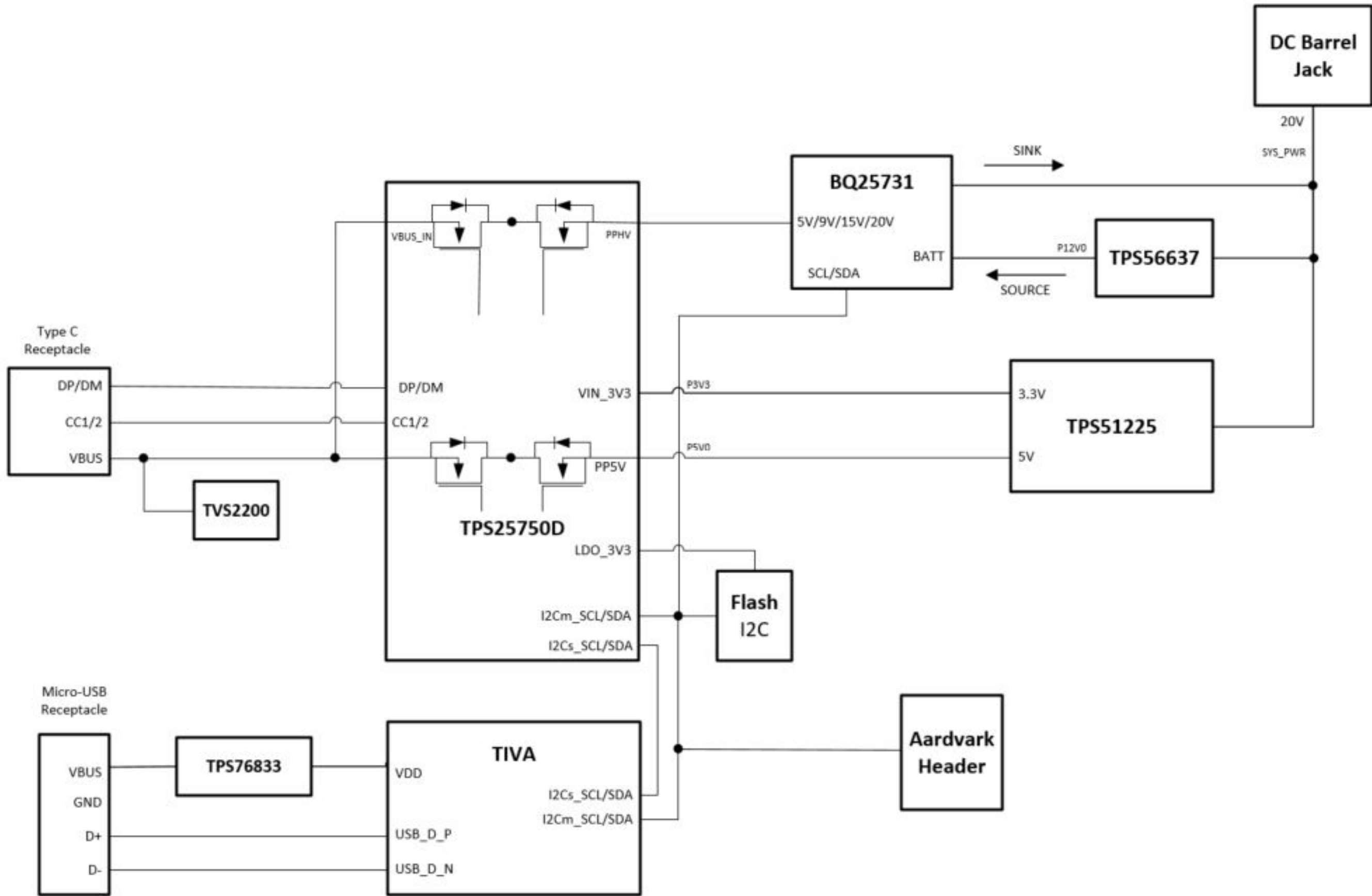
D

A

B

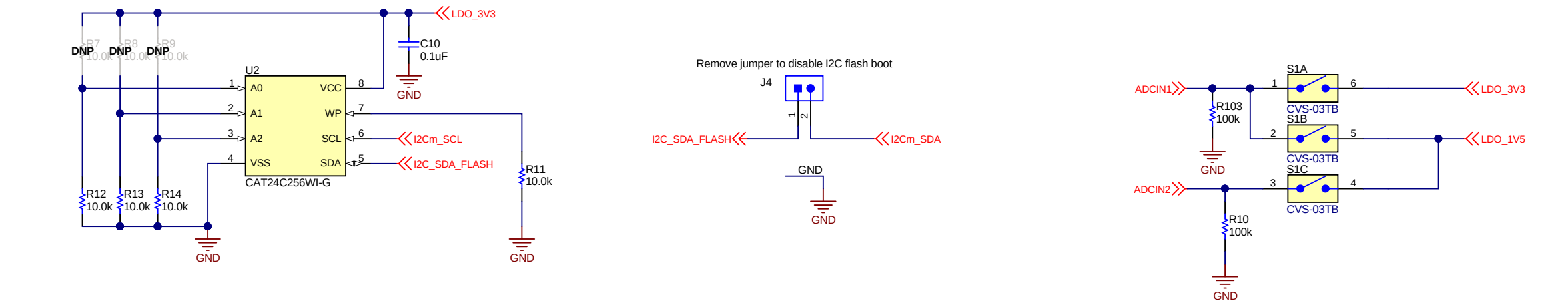
C

D

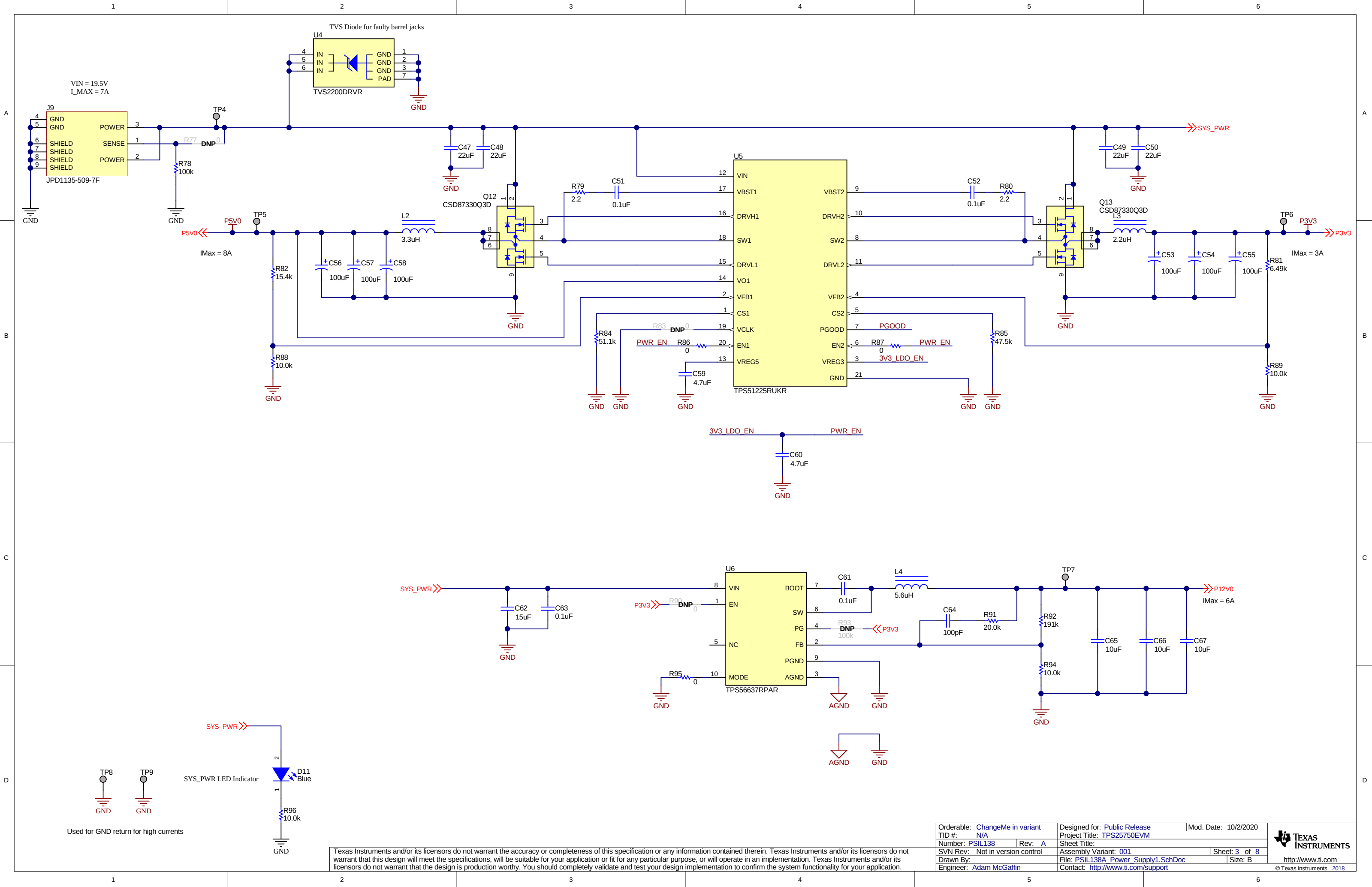


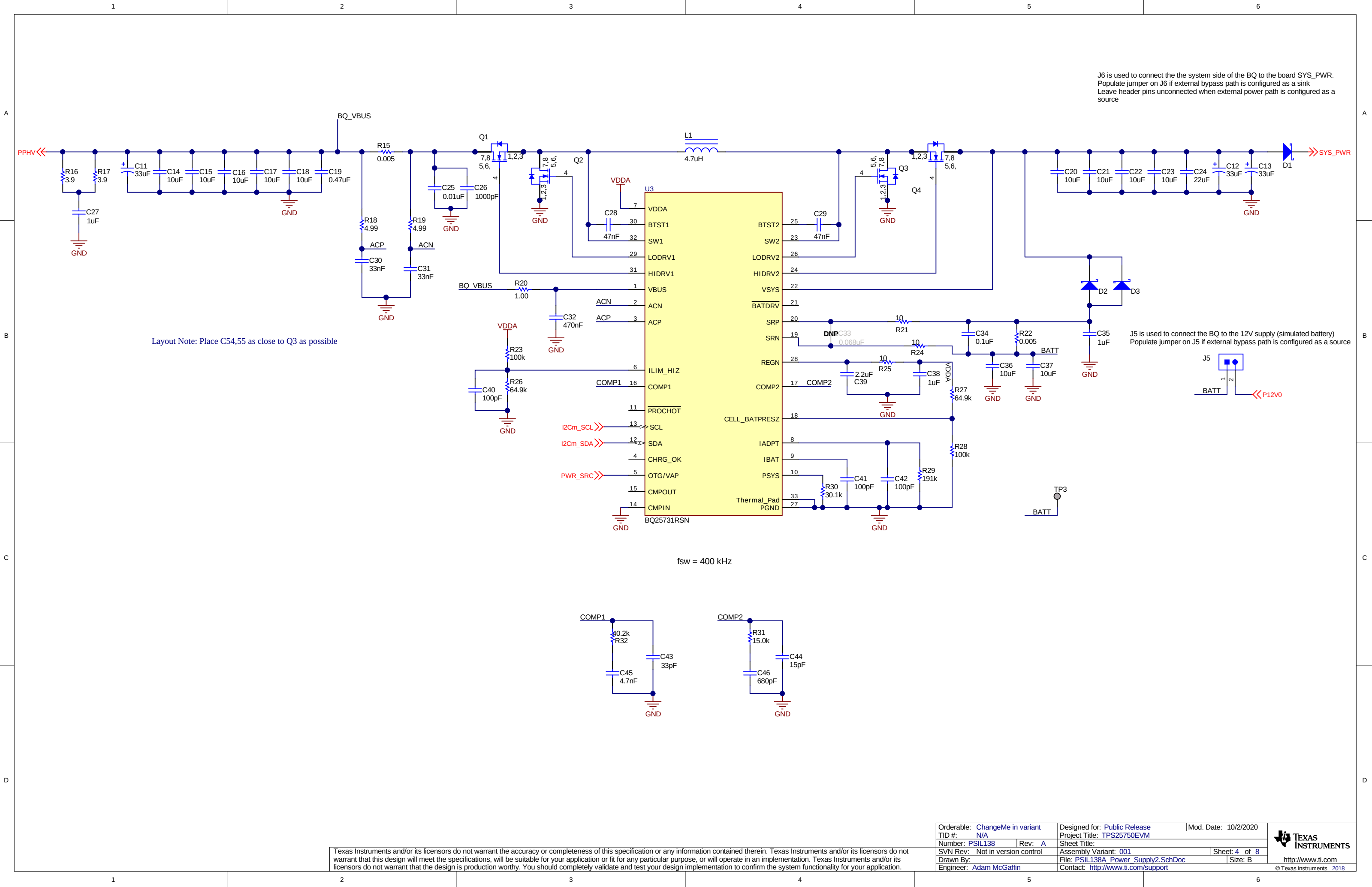
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|                                                 |                                                    |                                               |
|-------------------------------------------------|----------------------------------------------------|-----------------------------------------------|
| Orderable: <a href="#">ChangeMe in variant</a>  | Designed for: <a href="#">Public Release</a>       | Mod. Date: 9/29/2020                          |
| TID #: <a href="#">N/A</a>                      | Project Title: <a href="#">TPS25750EVM</a>         |                                               |
| Number: <a href="#">PSIL138</a>                 | Rev: <a href="#">A</a>                             | Sheet Title:                                  |
| SVN Rev: <a href="#">Not in version control</a> | Assembly Variant: <a href="#">001</a>              | Sheet: <a href="#">1</a> of <a href="#">8</a> |
| Drawn By:                                       | File: <a href="#">PSIL138A_CoverSheet.SchDoc</a>   | Size: <a href="#">B</a>                       |
| Engineer: <a href="#">Adam McGaffin</a>         | Contact: <a href="#">http://www.ti.com/support</a> |                                               |



| S1A | S1B | S1C | Dead Battery Condition | I2C Slave Address |
|-----|-----|-----|------------------------|-------------------|
| 0   | 0   | 0   | SafeMode               | #2                |
| 0   | 1   | 1   | AlwaysEnableSink       | #2                |
| 1   | 0   | 0   | SafeMode               | #1                |
| 1   | 0   | 1   | AlwaysEnableSink       | #1                |





J6 is used to connect the the system side of the BQ to the board SYS\_PWR.  
Populate jumper on J6 if external bypass path is configured as a sink  
Leave header pins unconnected when external power path is configured as a source

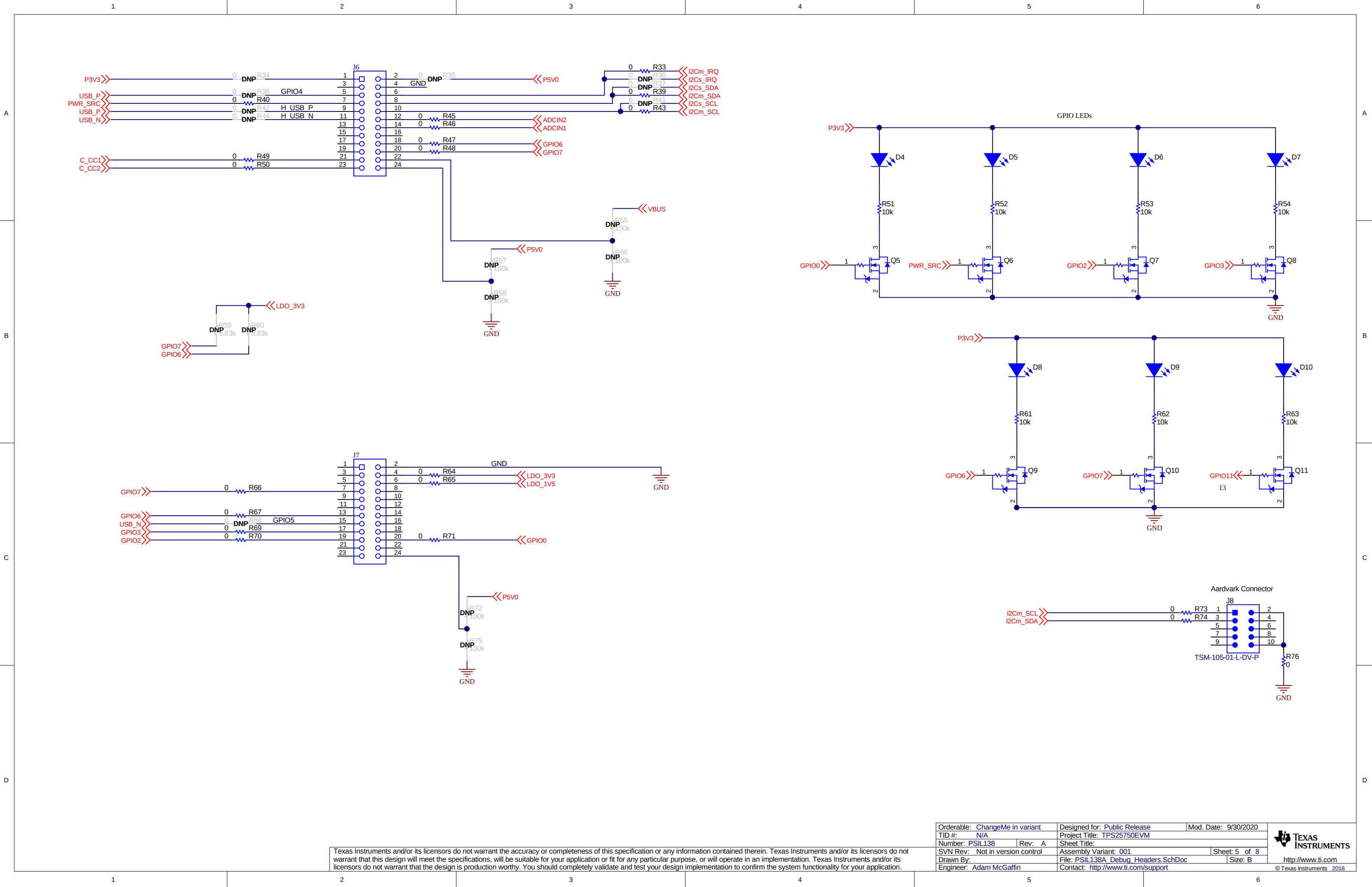
Layout Note: Place C54,55 as close to Q3 as possible

J5 is used to connect the BQ to the 12V supply (simulated battery)  
Populate jumper on J5 if external bypass path is configured as a source

fsw = 400 kHz

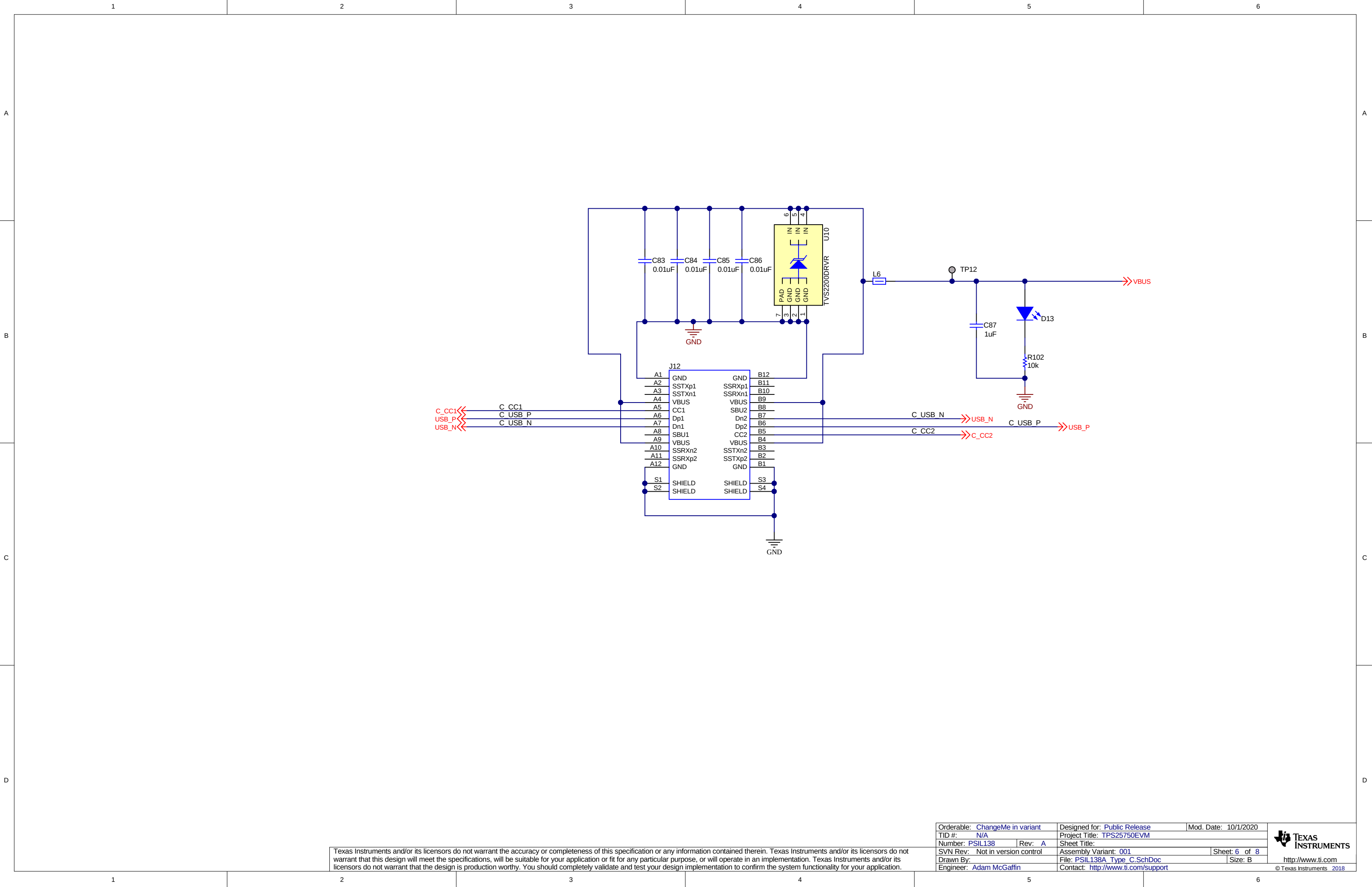
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| TID #: <a href="#">N/A</a>                      | Project Title: <a href="#">TPS25750EVM</a>          |                                                    |
| Number: <a href="#">PSIL138</a>                 | Rev: <a href="#">A</a>                              | Sheet: 4 of 8                                      |
| SVN Rev: <a href="#">Not in version control</a> | Assembly Variant: <a href="#">001</a>               | Size: B                                            |
| Drawn By: <a href="#">Adam McGaffin</a>         | File: <a href="#">PSIL138A Power_Supply2.SchDoc</a> | Contact: <a href="#">http://www.ti.com/support</a> |
| Engineer: <a href="#">Adam McGaffin</a>         | Contact: <a href="#">http://www.ti.com/support</a>  |                                                    |



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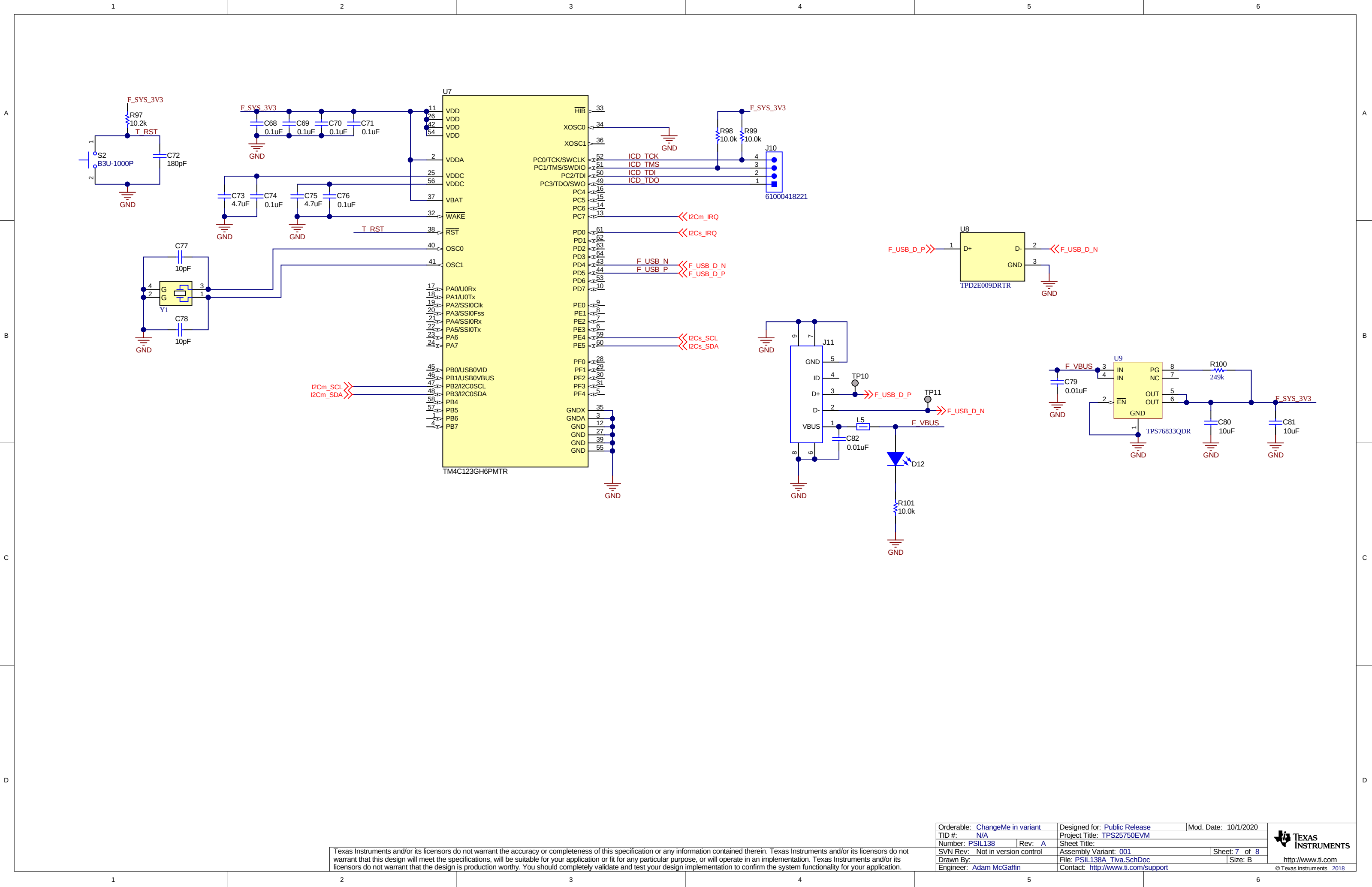
|                                                |                                                     |                                               |
|------------------------------------------------|-----------------------------------------------------|-----------------------------------------------|
| Orderable: <a href="#">ChangeMe in variant</a> | Designed for: <a href="#">Public Release</a>        | Mod. Date: 9/30/2020                          |
| TID #: N/A                                     | Project Title: <a href="#">TPS25750EVM</a>          |                                               |
| Number: <a href="#">PSIL138</a>                | Rev: <a href="#">A</a>                              | Sheet Title:                                  |
| SVN Rev: Not in version control                | Assembly Variant: <a href="#">001</a>               | Sheet: <a href="#">5</a> of <a href="#">8</a> |
| Drawn By:                                      | File: <a href="#">PSIL138A_Debug_Headers.SchDoc</a> | Size: <a href="#">B</a>                       |
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| TID #: <a href="#">N/A</a>                     |                        | Project Title: <a href="#">TPS25750EVM</a>         |                                               |
| Number: <a href="#">PSIL138</a>                | Rev: <a href="#">A</a> | Sheet Title:                                       |                                               |
| SVN Rev: Not in version control                |                        | Assembly Variant: <a href="#">001</a>              | Sheet: <a href="#">6</a> of <a href="#">8</a> |
| Drawn By:                                      |                        | File: <a href="#">PSIL138A_Type_C.SchDoc</a>       | Size: B                                       |
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| TID #: <a href="#">N/A</a>                      | Project Title: <a href="#">TPS25750EVM</a>         |                                               |
| Number: <a href="#">PSIL138</a>                 | Rev: <a href="#">A</a>                             | Sheet Title:                                  |
| SVN Rev: <a href="#">Not in version control</a> | Assembly Variant: <a href="#">001</a>              | Sheet: <a href="#">7</a> of <a href="#">8</a> |
| Drawn By:                                       | File: <a href="#">PSIL138A_Tiva.SchDoc</a>         | Size: <a href="#">B</a>                       |
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