Revision History	
Revision	Notes

Place Block Diagram here (if appropriate) or delete this text box.
If using a block diagram from another tool, save the picture as a .bmp file.
Then, use menu Place|Drawing Tools|Graphic to insert the .bmp file on the schematic.

Number: PWR568	Rev: A	Designed for: Public Release	Mod. Date: 9/11/2013
SVN Rev: Not in version control		Project Title: BQ27GDK000EVM	
Drawn By: Jared Casey		Sheet Title: Cover sheet	
Engineer: Jared Casey		Assembly Variant: [No Variations]	Sheet: 0 of 7
		File: PWR568A_Cover.SchDoc	Size: B
		Contact: http://www.ti.com/support	

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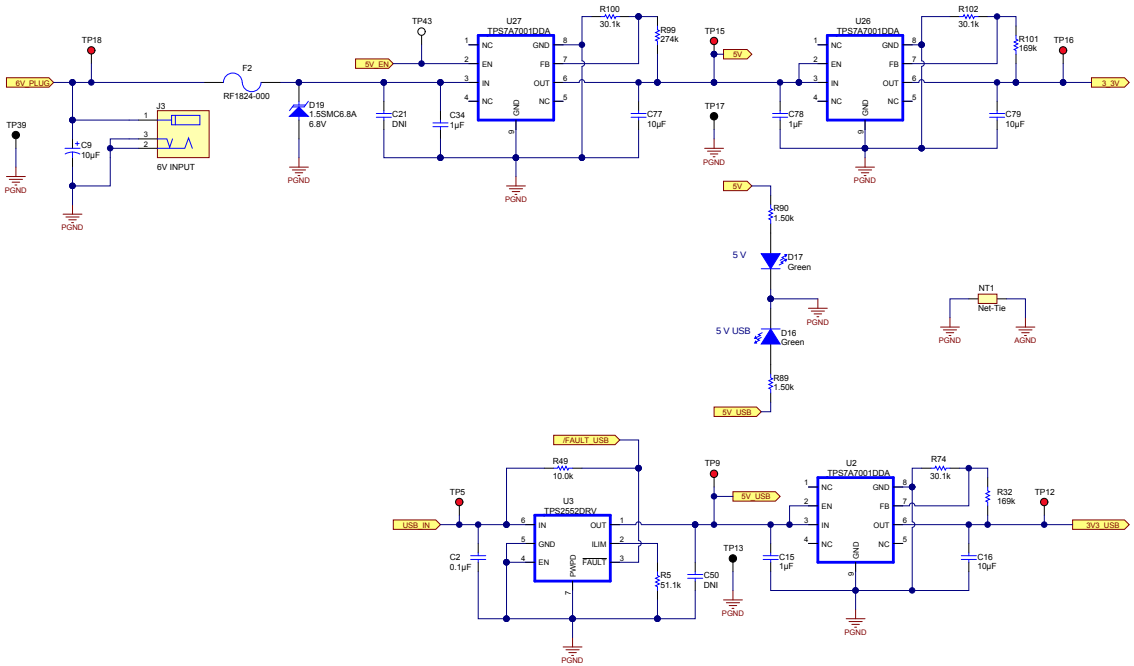

Number: PWR568	Rev: A
SVN Rev: Not in version control	
Drawn By: Jared Casey	
Engineer: Jared Casey	

Designed for: Public Release	Mod. Date: 3/5/2014
Project Title: BQ27GDK000EVM	
Sheet Title: Power	
Assembly Variant: [No Variations]	Sheet: 1 of 7
File: PWR568A. Power.SchDoc	Size: C
Contact: http://www.ti.com/support	

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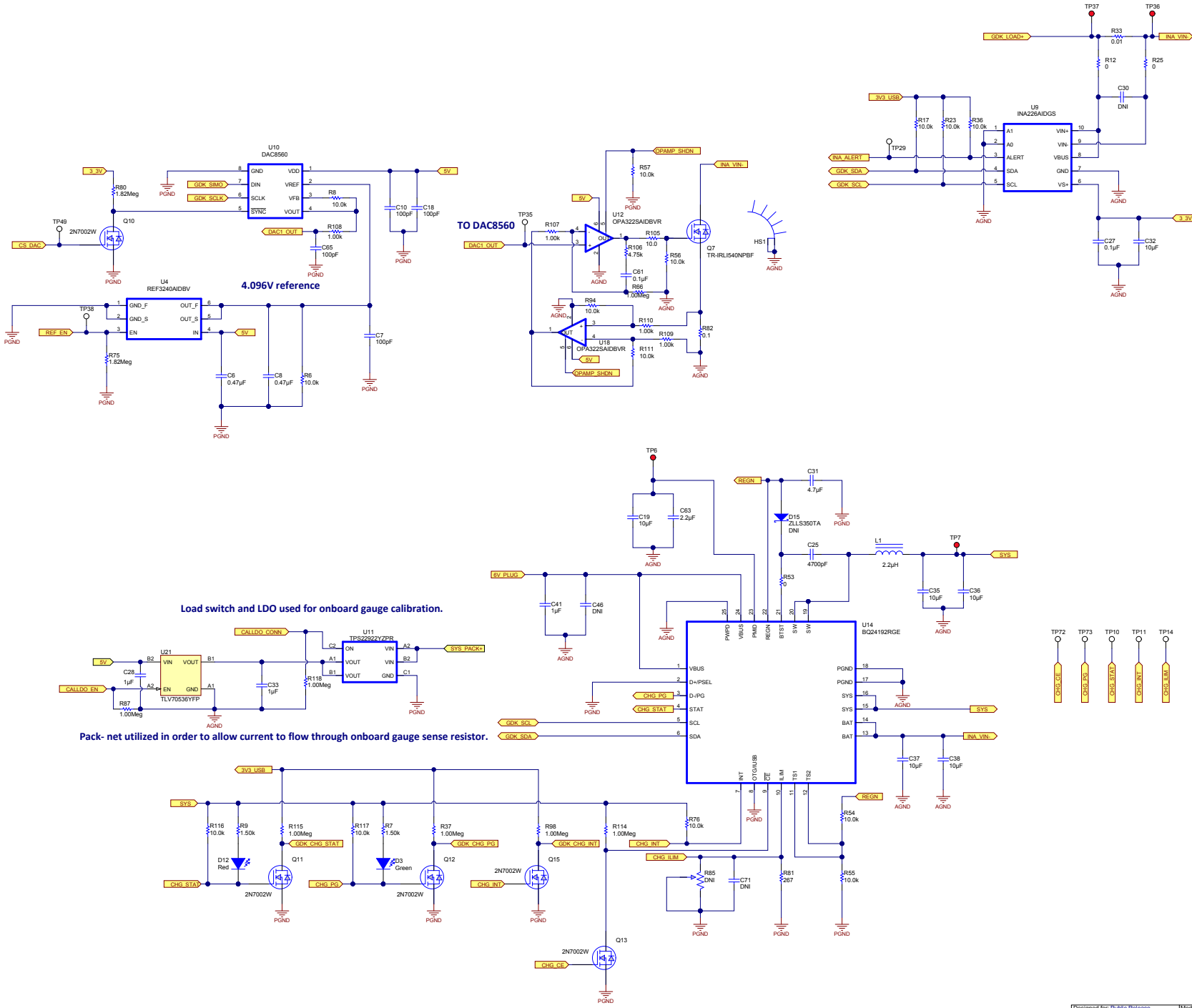

Designed for: Public Release	Mod. Date: 2/16/2014
Project Title: BQ27Z3DK000EVM	
Sheet Title: Charger Load	
Number: PWR568	Rev: A
SVN Rev: Not in version control	Assembly Variant: [No Variations]
Drawn By: Jared Casey	File: PWR568A_Charger_Load.SchDoc
Engineer: Jared Casey	Size: C
Sheet: 2 of 7	
Contact: http://www.ti.com/support	

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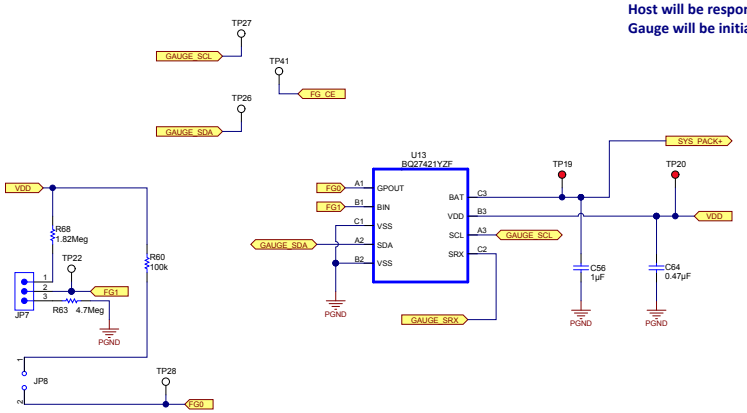
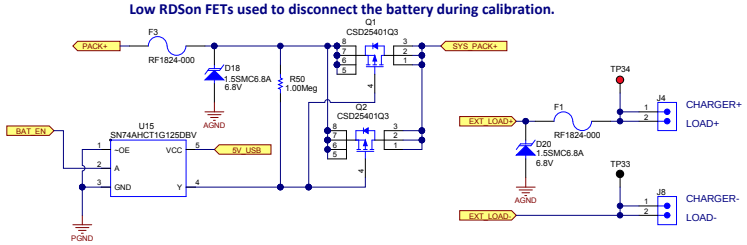
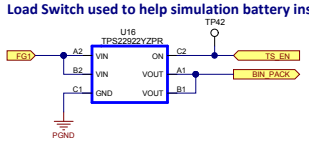
Host will be responsible for allowing the gauge to be powered from the battery.
Gauge will be initially powered via USB.

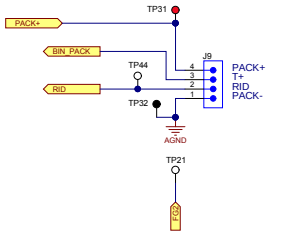


Low RDSon FETs used to disconnect the battery during calibration.



Load Switch used to help simulation battery insertion/removal.

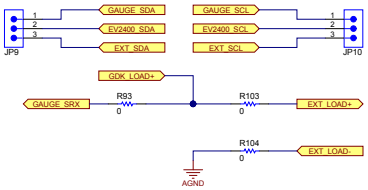




R93, R103, and R104 are 0 Ohm precision resistors used to connect GDK charge & load to on-board fuel gauge or to external EVM.

For on board gauge connection:
Populate R93, short JP9 pin 1 & 2, and short JP10 pins 1 & 2

For external EVM connection:
Populate R103 & R104, short JP9 pins 2 & 3, and short JP10 pins 2 & 3



Number: PWR568	Rev: A
SVN Rev: Not in version control	
Drawn By: Jared Casey	
Engineer: Jared Casey	

Designed for: Public Release	Mod. Date: 2/13/2014
Project Title: BQ27GDK000EVM	
Sheet Title: Gauge	
Assembly Variant: [No Variations]	Sheet: 3 of 7
File: PWR568A. Gauge.SchDoc	Size: C
Engineer: Jared Casey	
Contact: http://www.ti.com/support	

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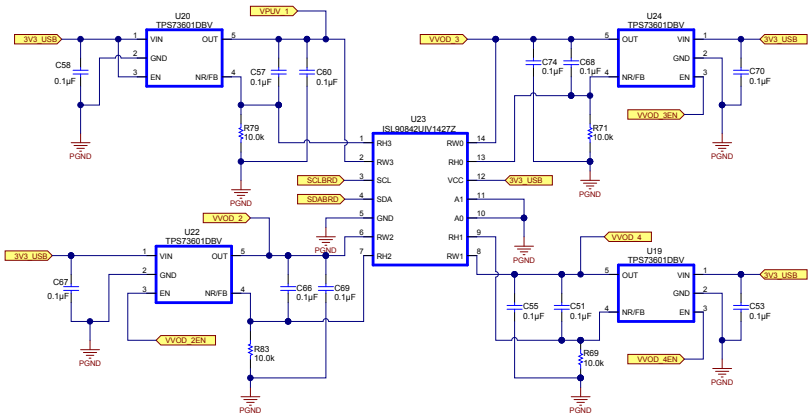
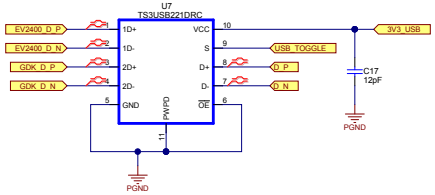
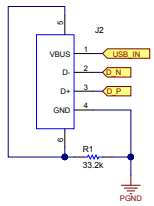
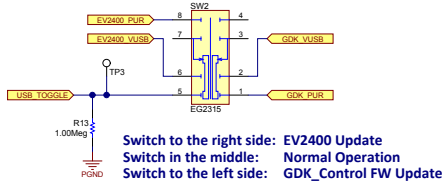
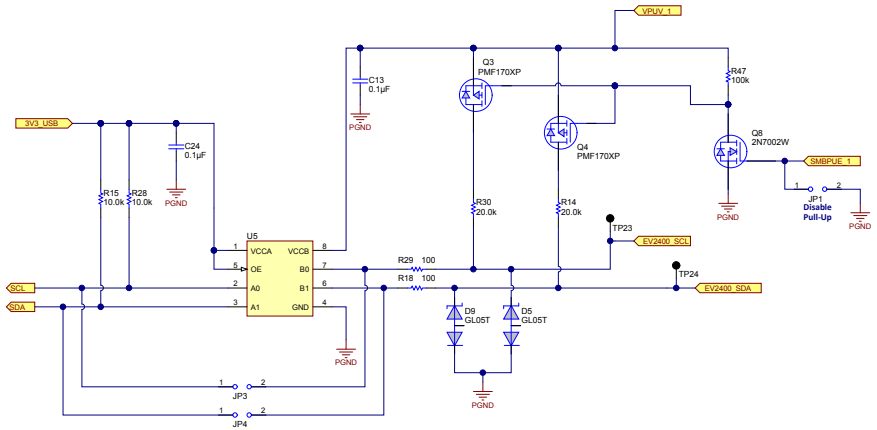


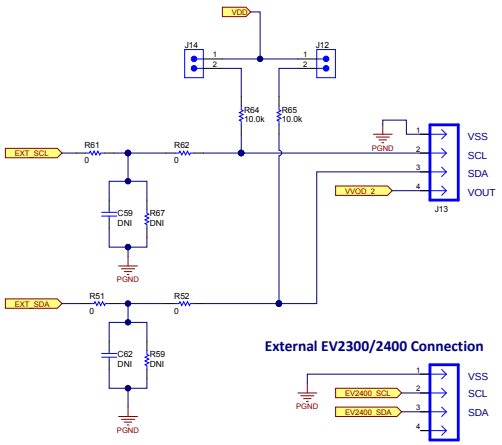




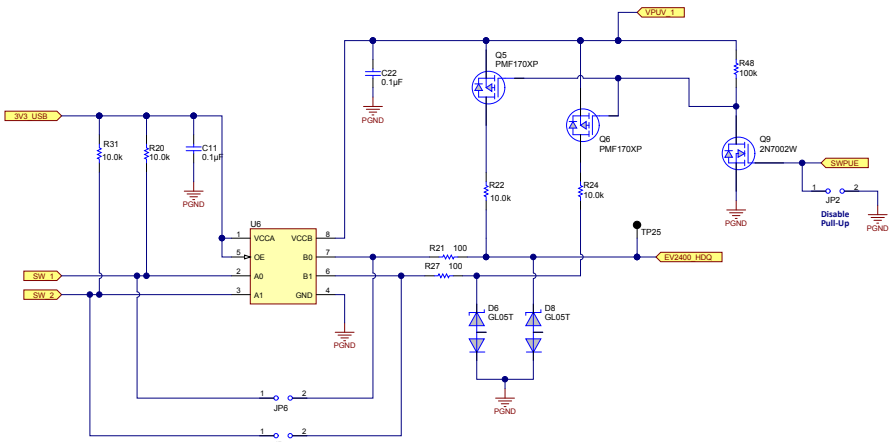


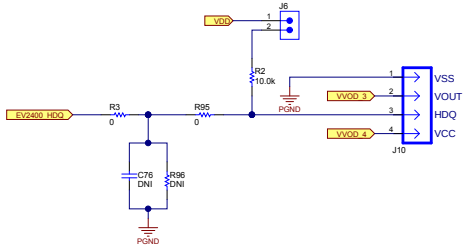
Switch to the right side: EV2400 Update
Switch in the middle: Normal Operation
Switch to the left side: GDK_Control FW Update



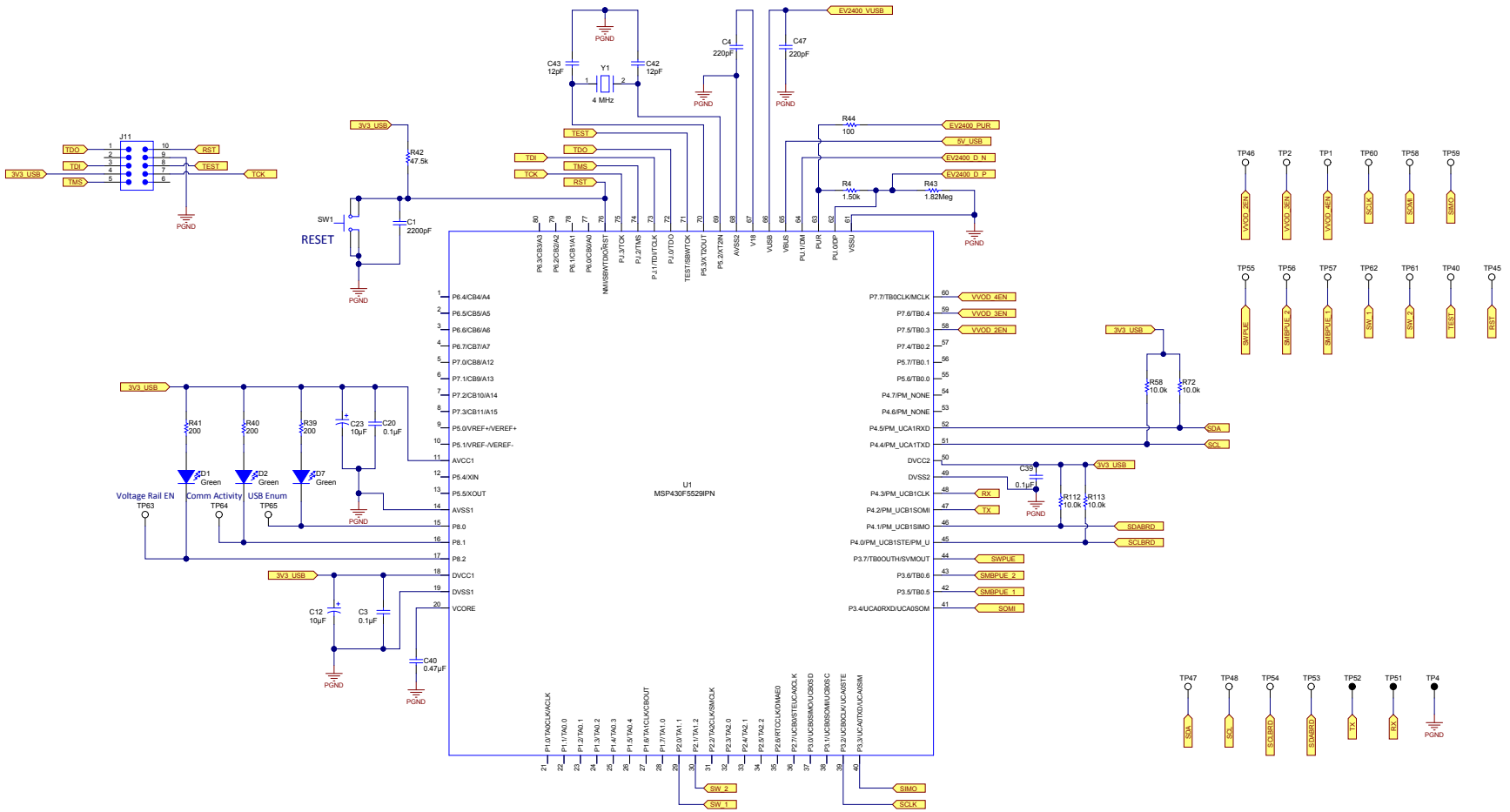


External EV2300/2400 Connection





Designed for: Public Release	Mod. Date: 3/10/2014
Project Title: BQ27GDK000EVM	
Number: PWR568	Rev: A
Sheet Title: Comms	
SVN Rev: Not in version control	Assembly Variant: [No Variations]
Drawn By: Jared Casey	File: PWR568A_Comms.SchDoc
Engineer: Jared Casey	Contact: http://www.ti.com/support
Sheet: 4 of 7	Size: C



Designed for: Public Release	Mod. Date: 2/16/2014
Project Title: BQ27GDK000EVM	
Number: PWR568	Rev: A
Sheet Title: MSP430 EV2400	
SVN Rev: Not in version control	Assembly Variant: [No Variations]
Drawn By: Jared Casey	Sheet: 5 of 7
Engineer: Jared Casey	File: PWR568A_MSP430_EV2400.SchDoc
Contact: http://www.ti.com/support	Size: C

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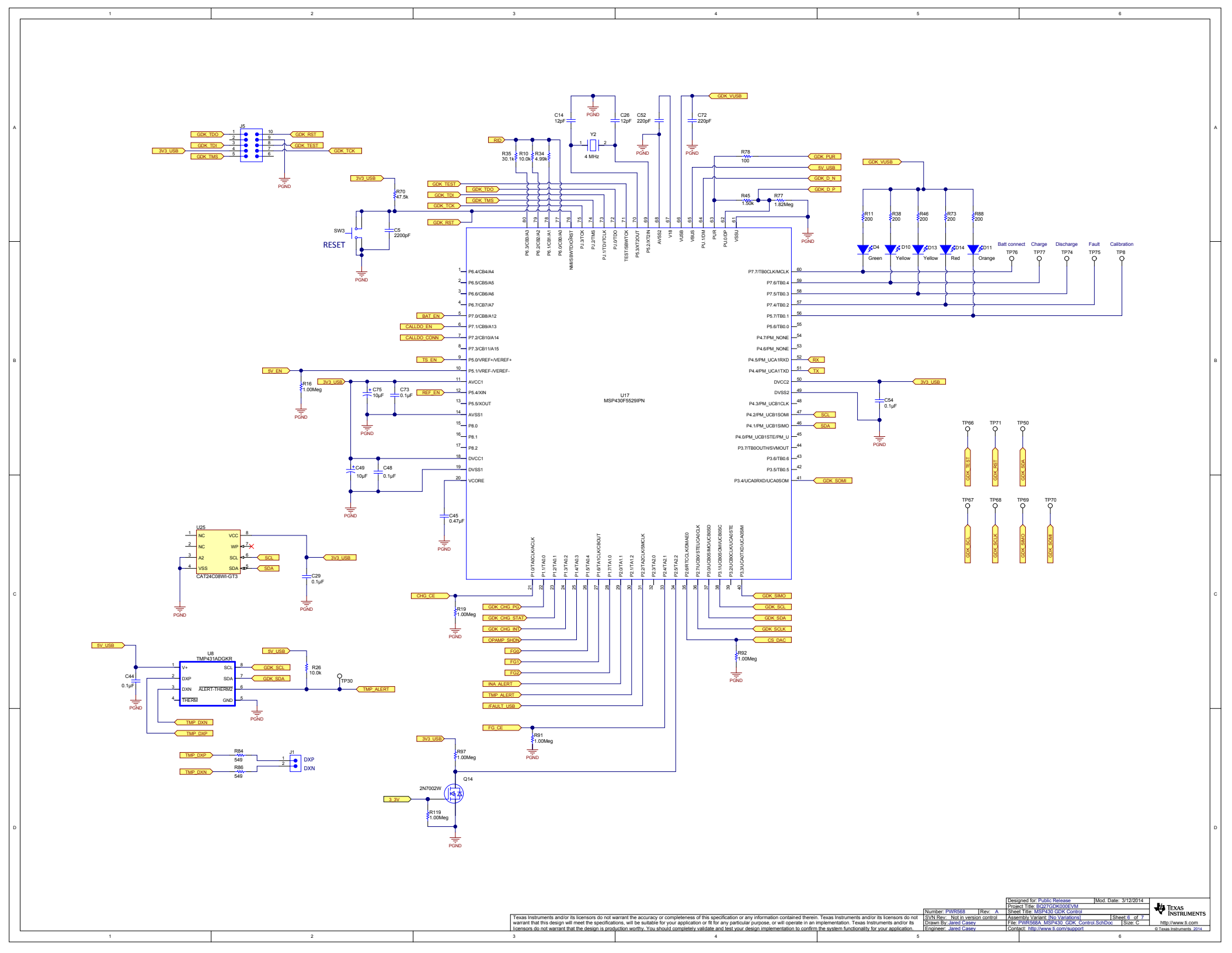

Designed for: Public Release	Mod. Date: 3/12/2014
Project Title: BQ27GDK000EVM	
Number: PWR568	Rev: A
Sheet Title: MSP430 GDK Control	
SVN Rev: Not in version control	Assembly Variant: [No Variations]
Sheet: 6 of 7	
Drawn By: Jared Casey	File: PWR568A_MSP430_GDK_Control.SchDoc
Size: C	
Engineer: Jared Casey	Contact: http://www.ti.com/support

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SH-JP1 SH-JP2 SH-JP3 SH-JP4 SH-JP5 SH-JP6 SH-JP7 SH-JP8 SH-JP9 SH-JP10

FID1 FID4

H1 NY PMS 440 0025 PH

H5 1902C

H2 NY PMS 440 0025 PH

H6 1902C

PCB Number: PWR568
PCB Rev: A

PCB
LOGO
Texas Instruments

PCB
LOGO
CAUTION HOT SURFACE

PCB
LOGO
CAUTION:

H3 NY PMS 440 0025 PH

H7 1902C

H4 NY PMS 440 0025 PH

H8 1902C

FID2 FID5

FID3 FID6

W1
MECH
CBL002

LBL1
PCB Label
Size: 0.65" x 0.20 "

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2., unless otherwise specified.

Number: PWR568	Rev: A
SVN Rev: Not in version control	
Drawn By: Jared Casey	
Engineer: Jared Casey	

Designed for: Public Release	Mod. Date: 4/3/2014
Project Title: BQ27GDK000EVM	
Sheet Title: Hardware	
Assembly Variant: [No Variations]	Sheet: 7 of 7
File: PWR568A_Hdwr.SchDoc	Size: C
Contact: http://www.ti.com/support	

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