

Registers		Data Memory			
				Filter/Search	
				Auto Export	Export
				Import	Write All
				Read All	
Data Memory					
Read/Write Data Memory Contents					
Calibration		Name		Value	
Enabled PF C		7f		hex	
Enabled PF D		30		hex	
Settings		Configuration			
Protections		Charging Configuration		00	
Permanent Fail		Temperature Enable		06	
Advanced Charge Algorithm		Temperature Mode		00	
Gas Gauging		DA Configuration		0014	
Power		Cell Configuration		06	
PF Status		FET Options		0100	
System Data		Sbs Gauging Configuration		04	
SBS Configuration		Sbs Configuration		20	
LED Support		Auth Config		00	
Black Box		Power Config		0003	
Lifetimes		IO Config		00	
Ra Table		Pin Configuration		0183	

Pin configuration :

- PIN 20, 21 and 22 = LED pins
 - Pin 17 = System Present Pin
 - Pin 16 = Pre-discharge (PDSG)
 - Pin 15 = GPIO PIN**
 - Pin 13 = Led Button
 - Pin 12 = GPIO PIN

Read/Write Data Memory Contents			
	Name	Value	Unit
Calibration	Enabled PF C	7f	hex
Settings	Enabled PF D	30	hex
Protections	Configuration		
Permanent Fail		00	hex
Advanced Charge Algorithm		06	hex
Gas Gauging		00	hex
Power		0014	hex
PF Status	Cell	X	FlagMapSetUp0
System Data	FET		
SBS Configuration	Sbs	Bit 7	Bit 6
LED Support	Sbs	MSB	FLAG_EN
Black Box	Aut	RSVD	FLAG_OD
	Pow	LSB	FLAG_OR
	IO C	FLAG_BIT3	FLAG_GPIO2
	Pin	FLAG_BIT2	FLAG_GPIO1
		FLAG_BIT1	FLAG_GPIO0
		FLAG_BIT0	FLAG_POL
		FLAG_REG3	FLAG_REG2
		FLAG_REG2	FLAG_REG1
		FLAG_REG1	FLAG_REG0
		+ Write to Data Memory	
	GPIO Shared ACCESS CONTROL	00	hex
	FlagMapSetUp0	8841	hex
	FlagMapSetUp1	8041	hex
	FlagMapSetUp2	0000	hex

- FlagMapSetUp0 = 0x 8841

Configure the control register to map BatteryStatus() [FD].

- Set FlagMapSetUp0[FLAG_EN] = 1 to enable the control.
 - Set FlagMapSetUp0[FLAG_REG2:0] = 0x01 for BatteryStatus().
 - Set FlagMapSetUp0[FLAG_BIT4:0] = 0x04 for BatteryStatus ()[FD].
 - Set FlagMapSetUp0[FLAG_POL:0] = 0 for no change in the polarity of the flag.
 - Set FlagMapSetUp0[FLAG_GPIO2:0] = 0x04 for the Pin 15 (RH2 pin).
 - Set FlagMapSetUp0[FLAG_OR] = 0 for the AND operation.
 - Set FlagMapSetUp0[FLAG_OD] = 0 for driven-high/driven low

- FlagMapSetUp1 = 0x 8041

Configure the control register to map BatteryStatus() [FD] with the PIN12

Registers Data Memory Commands Calibration SHA Authentication Advanced Comm SMB ECC Authentication Chemistry Firmware GPCPackager Watch Data Graph Errors

Registers Data Memory

Registers

Registers

Name	Value	Units	Name	Value	Units	Name	Value	Units	Name	Value	Units
Manufacturer Access	0x0187	hex	Cell 3 Current	0	mA	Cell 4 RaScale	1000	-	Cell 2 QMax	2950	mAh
Remaining Cap. Alarm	590	mAh	Cell 4 Current	0	mA	Cell 5 RaScale	1000	-	Cell 3 QMax	2950	mAh
Remaining Time Alarm	10	min	Cell 5 Current	0	mA	Cell 6 RaScale	1000	-	Cell 4 QMax	2950	mAh
At Rate	0	mA	Cell 6 Current	0	mA	Cell 7 RaScale	0	-	Cell 5 QMax	2950	mAh
At Rate Time To Full	65535	min	Cell 7 Current	0	mA	Cell 1 CompRes	0	mOhm	Cell 6 QMax	2950	mAh
At Rate Time To Empty	65535	min	Cell 1 Power	0	cW	Cell 2 CompRes	0	mOhm	Cell 7 QMax	0	mAh
At Rate OK	1	-	Cell 2 Power	0	cW	Cell 3 CompRes	0	mOhm	Cell 1 QMax DODO	0	-
Temperature	26.0	degC	Cell 3 Power	0	cW	Cell 4 CompRes	0	mOhm	Cell 2 QMax DODO	0	-
Voltage	24086	mV	Cell 4 Power	0	cW	Cell 5 CompRes	0	mOhm	Cell 3 QMax DODO	0	-

Bit Registers

	Value	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
Battery Mode (high)	0x6081	CapM	ChgM	AM	RSVD	RSVD	RSVD	PB	CC
Battery Mode (low)		CF	RSVD	RSVD	RSVD	RSVD	RSVD	PBS	ICC
Battery Status (high)	0x02C0	OCA	TCA	RSVD	UITA	TDA	RSVD	RCA	RTA
Battery Status (low)		INIT	DSG	FC	FD	EC3	EC2	EC1	EC0
Operation Status A (high)	0x0187	SLEEP	XCHG	XDSG	PF	SS	SDV	SEC1	SEC0
Operation Status A (low)		BT_P_INT	RSVD	FUSE	POSG	PCHG	CHG	DSG	PRES
Operation Status B (high)	0x0000	IATA_CTERM	PSSHUT	EMSHUT	CB	SLPCC	SLPAD	SMBLCAL	INIT
Operation Status B (low)		SLEEPM	XL	CAL_OFFSET	CAL	AUTOCLM	AUTH	LED	SDM
Temp Range (high)	0x10	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD	RSVD
Temp Range (low)		RSVD	OT	HT	STH	RT	STL	LT	UT
Charoing Status (high)	0x0008	RSVD	RSVD	RSVD	RSVD	NCT	CCC	CVR	CCR

TEXAS INSTRUMENTS Scanning: (100%)

FD bit = 0 but Pin 15 is always to 1,5V.