



- Filter capacitors – make COUT1 smaller than COUT2. COUT1 is typically ceramic.
- Filter inductors – make L2 smaller than L1. Make sure that ISAT is greater than IOUT max.
- Filter resonance $2\pi f_r = 1/\sqrt{L \cdot C}$ – make the second-stage filter resonance three times larger than the crossover frequency (max $1/5 \cdot f_{sw}$).
- Damping – damp the second-stage filter to a Q of 1.

Power Designer Calculations
Current ripple: 1.76A(64.04%)

NOTES:
1) R? for test purposes only
3) Due to losses provide copper area to T?/D?

Revision History		Title <i>Power Board Schematic Diagram</i>		
Revision	Notes	Size: A3	Number: PWR_BRD	
1.0	* First release	Date: 27.3.2020	Time: 20:23:45	
		Sheet 1 of 1	File: C:\Users\Public\Documents\Altium\Projects\RPShield\Power Board 1.0\PowerBoard_RevA_SCH.SchDoc	