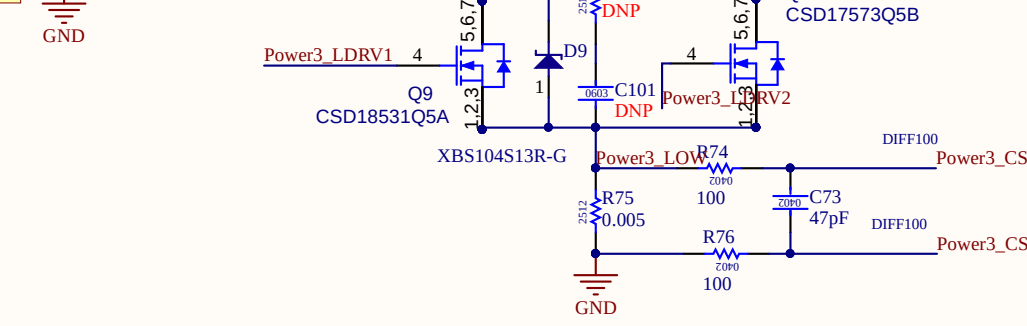
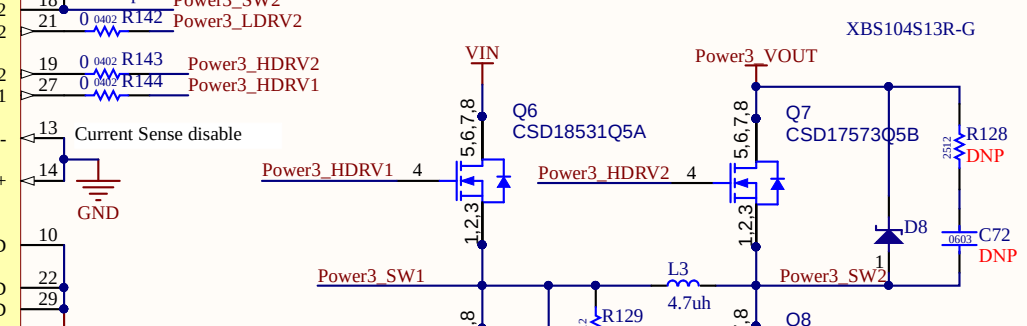
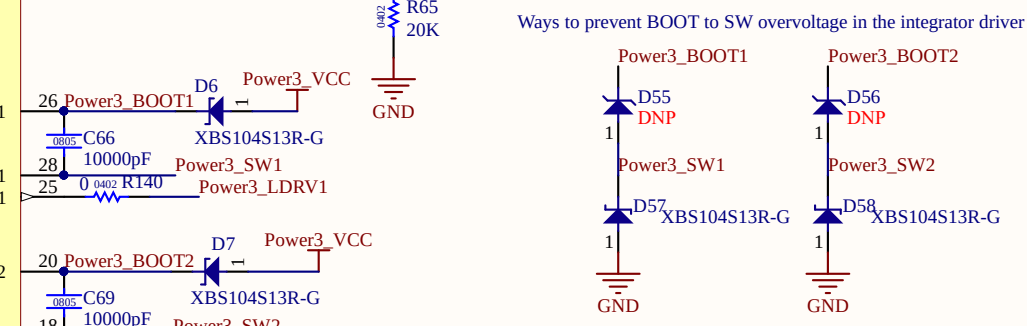
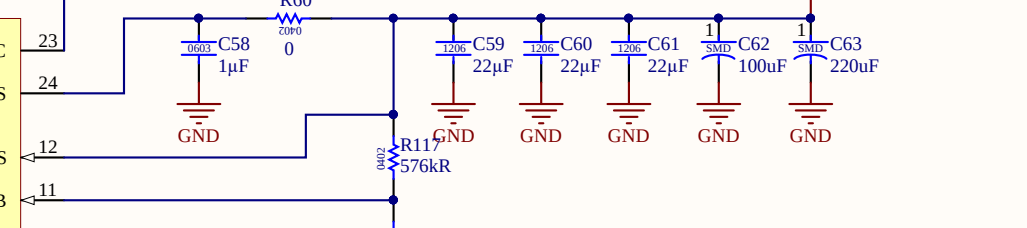
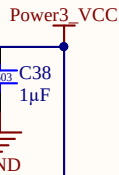
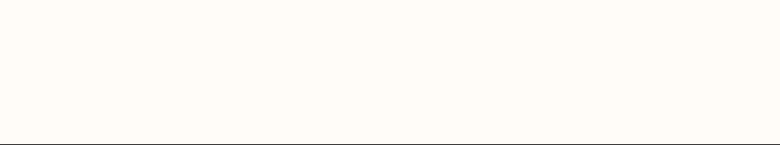
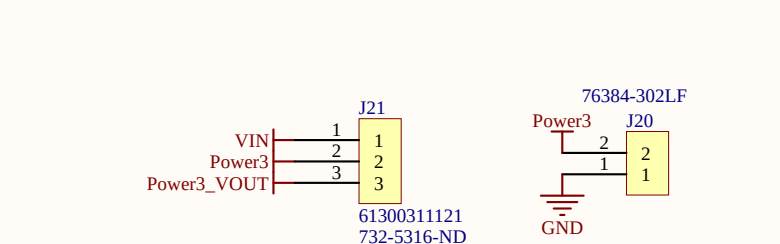
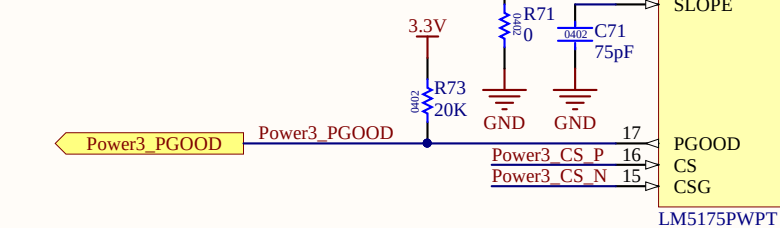
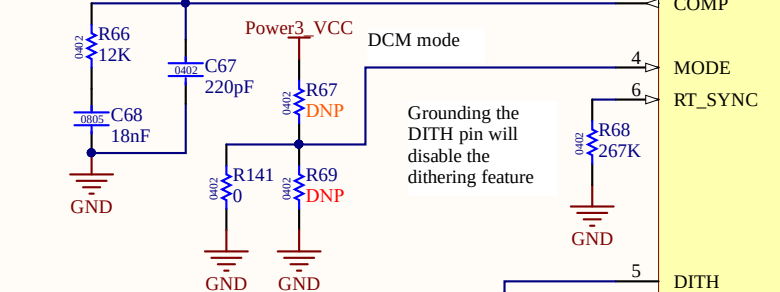
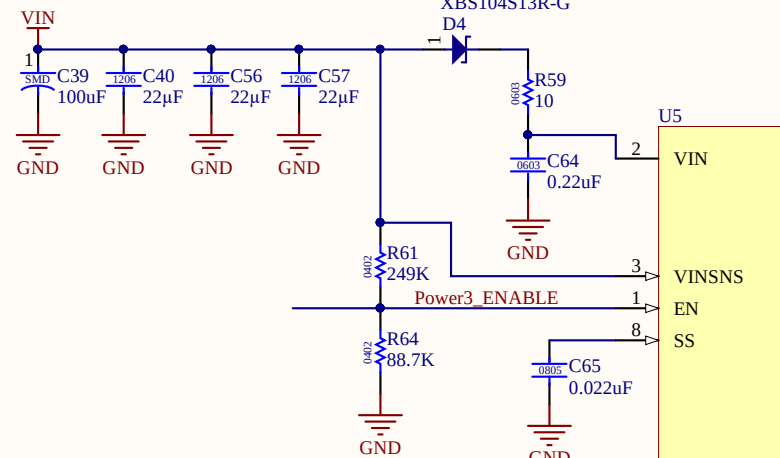


5 V to 24 V



Connecting the BIAS pin to VOUT in applications with VOUT greater than 8.5 V improves the efficiency of the regulator in the buck mode

24V

Ways to prevent BOOT to SW overvoltage in the integrator driver

