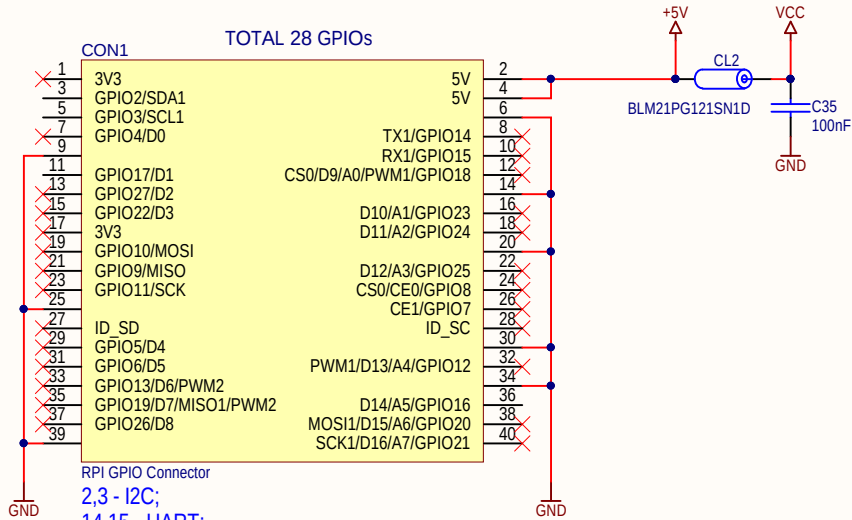
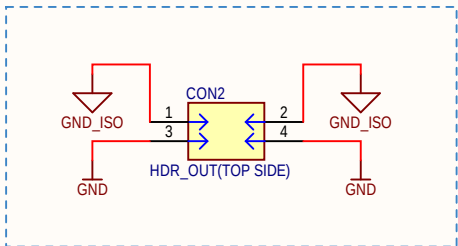




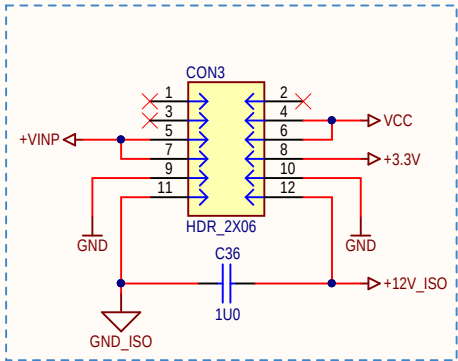
RPI GPIO Connector
2,3 - I2C;
14,15 - UART;
18,19,20,21 - I2S;
12, 13 - PWM;
9, 10, 11, 7, 8 - SPI;
16, 17 - buttons interrupts(16 - floor 1/17 - floor 2)

4, 5, 6, 22, 23, 24, 25, 26, 27 - VERSION 3.1

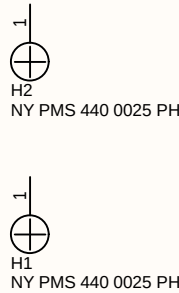
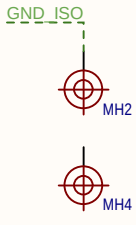
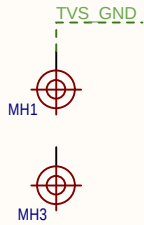
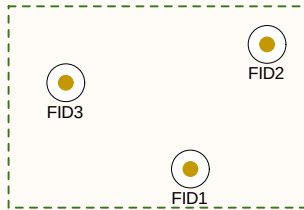
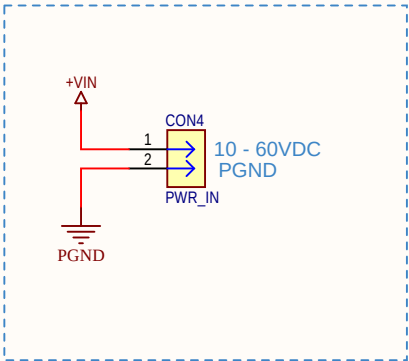
FLOOR DETECTION CONNECTOR



POWER THROUGH CONNECTOR(3A per pin)



POWER INPUT CONNECTOR(7A per pin)



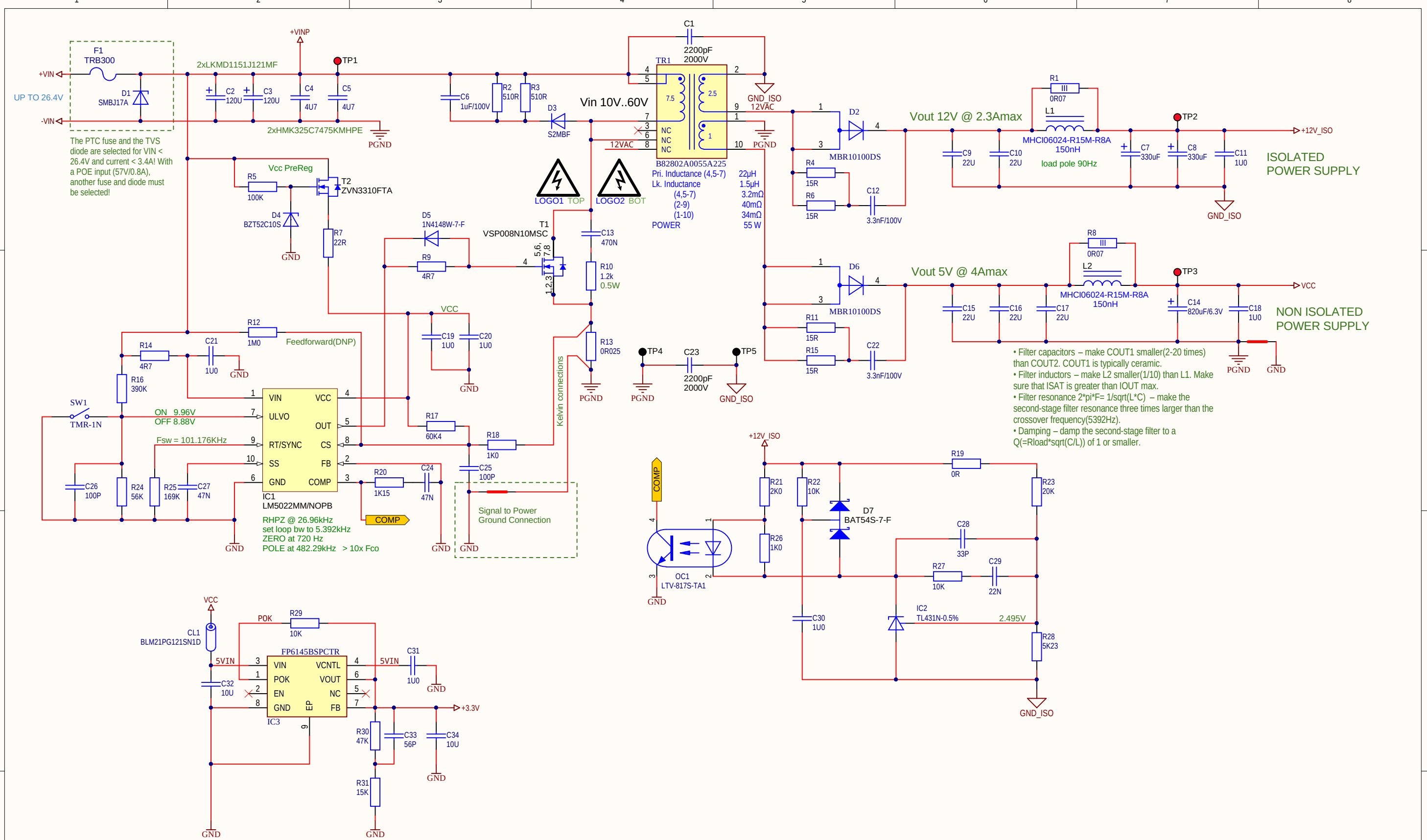
PCB Number: PWR_BRD
PCB Rev: 1.0

Design (New Product)
|
Version (Major Changes / New Functions)
|
Revision (Minor Changes)

Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!



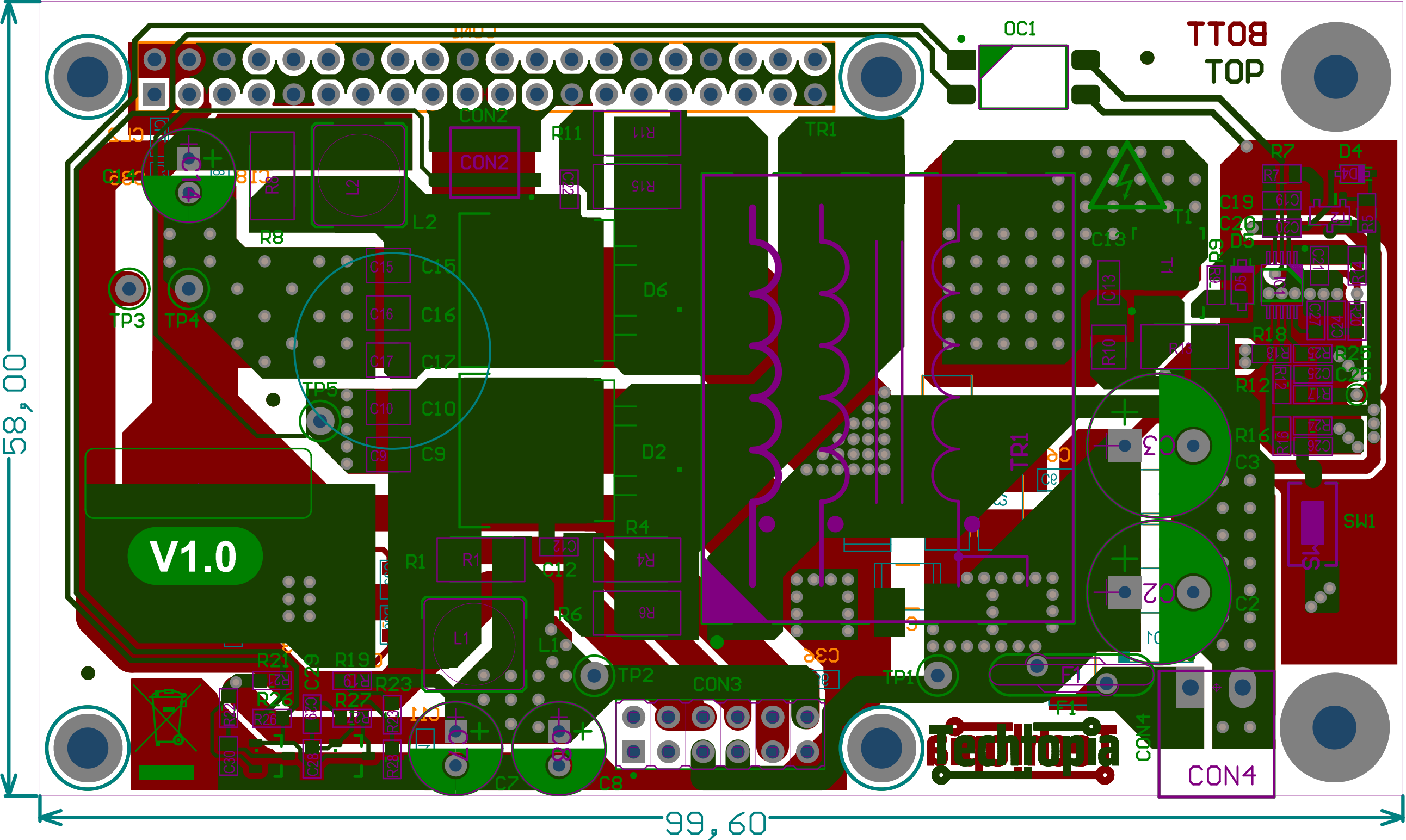
LBL1
PCB Label
Size: 0.65" x 0.20 "



NOTES:
1) R? for test purposes only
3) Due to losses provide copper area to T?/D?

Revision History		
Revision	Notes	
1.0	* First release	

Title Power Board Schematic Diagram			
Size: A3	Number: PWR_BRD	Revision: 1.0	
Date: 8.4.2020 r.	Time: 11:10:33	Sheet 1 of 2	
File: C:\Users\Public\Documents\Altium\Projects\RPiShield\Power Board 1.0\PowerBoard_V1.0_SH1.SchDoc			



25,40mm