We think that if the S5 de-assertion time is less than 200us,
VDDQ will not re-rise.

After first de-assertion of S5,
VDDQ does not re-rise

After second de-assertion of S5,
VDDQ re-rises.
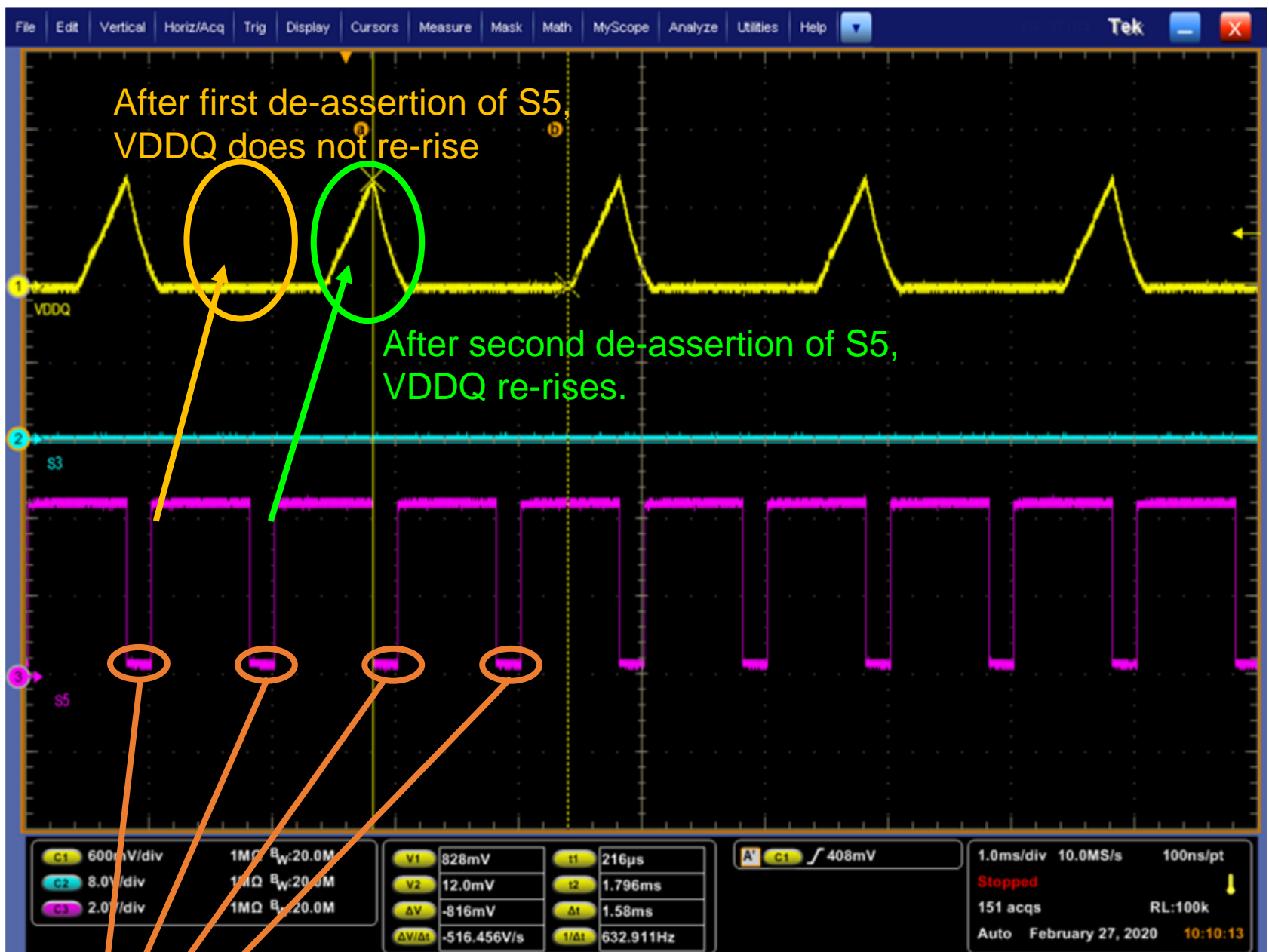
(Request)
Could you make the assertion
time of the S5 signal 100msec?