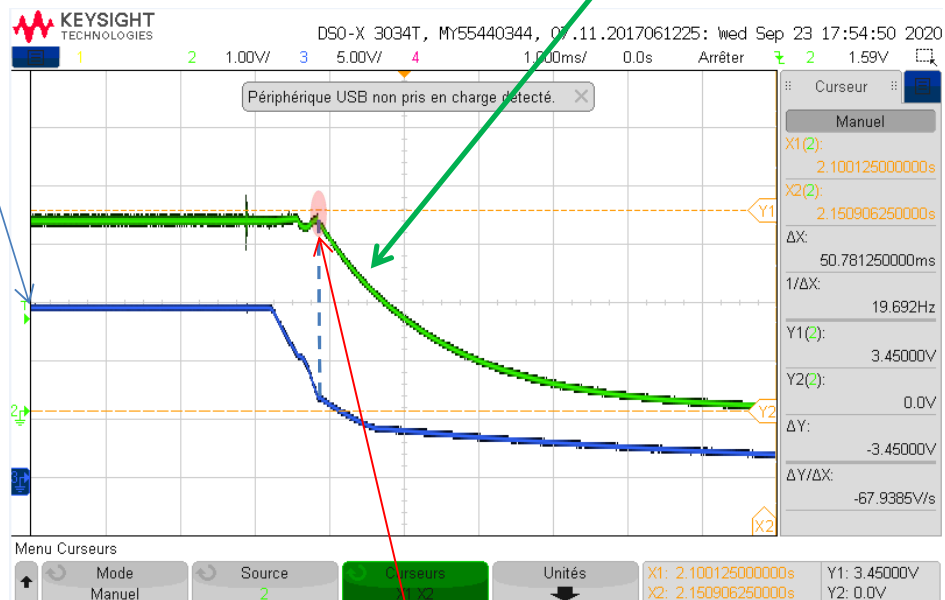
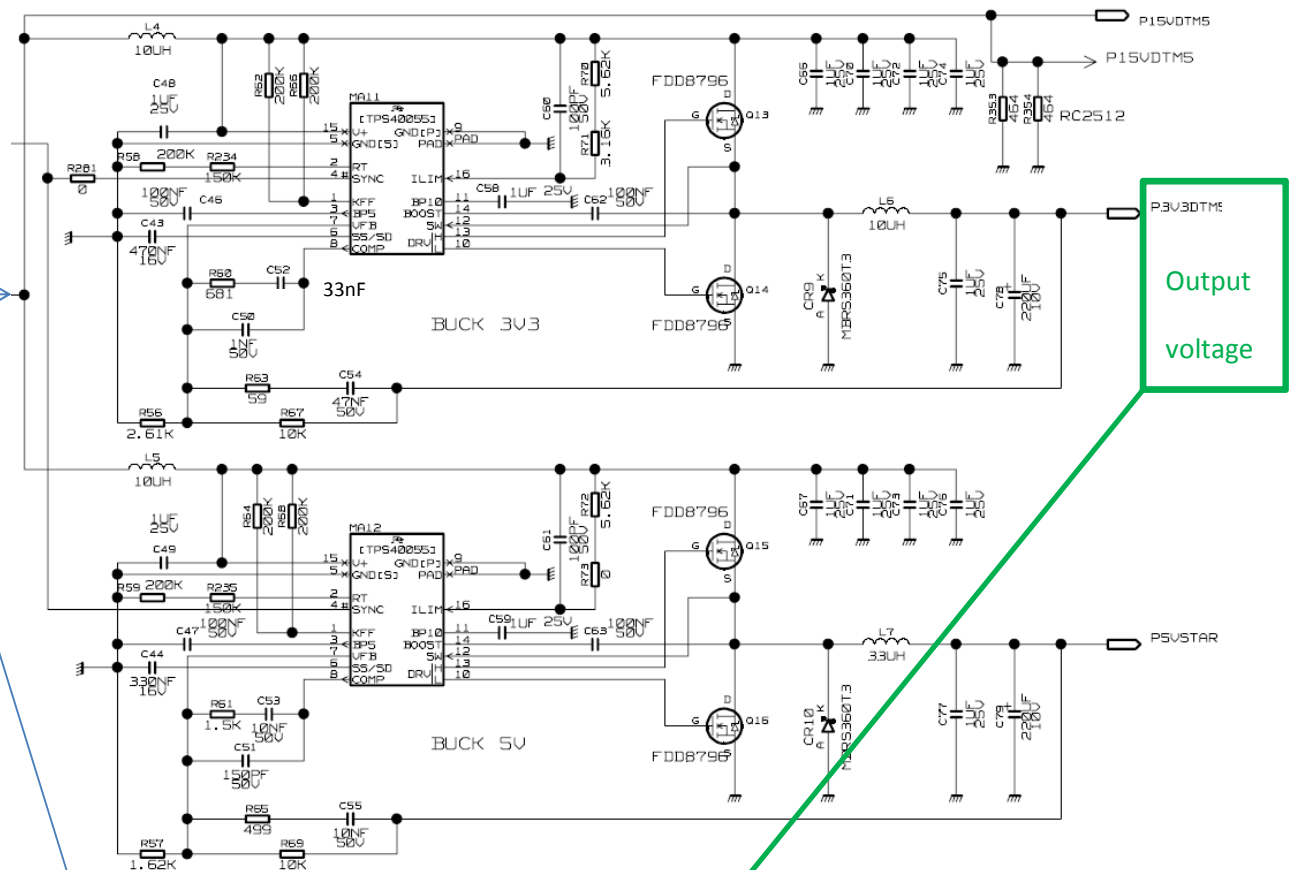


Electrical diagram using TPS40055



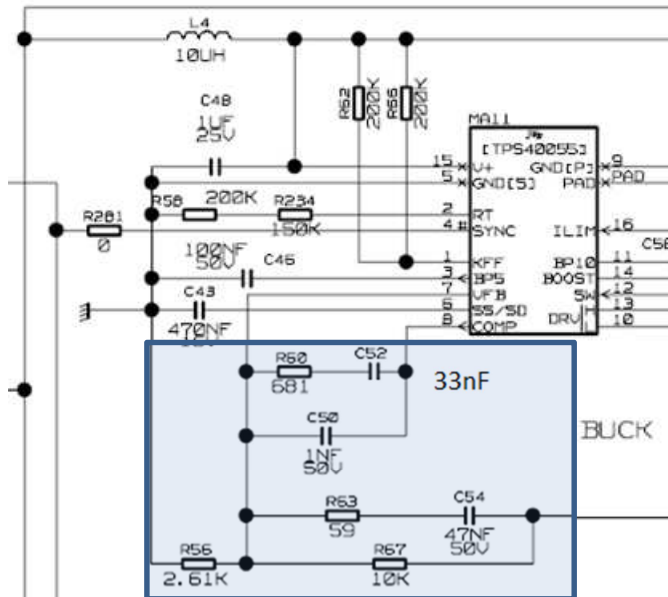
At cut off, during the following edge of the 15V power supply, an over voltage occurs on the 3V3 output. In some case, this overshoot exceeds the steady state voltage 3.3V, up to 3.45V.

At this time, the input voltage is approximately 6 volts.

What could be the root cause? How could we prevent this overvoltage?

Action undertaken :

We have tried to act on the feedback. And, the better finding compromise is with the following values.



With these, phase margin is the following : 89° at 5.07KHz. Acceleration of the dynamic, above 5Khz, have a wrong impact on the overvoltage at cut off.

