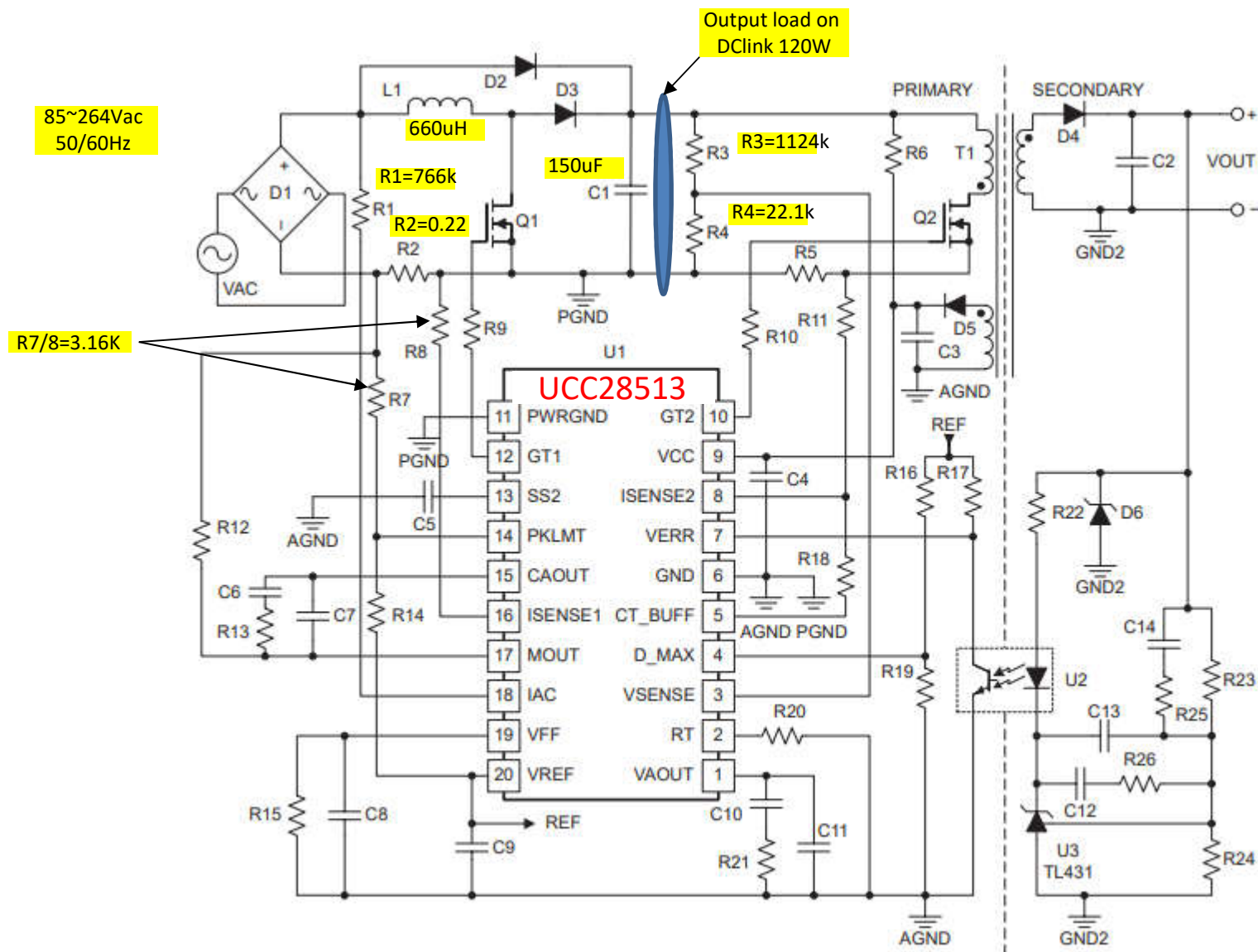


UCC28513 questions, Alex W., 11/17/2022

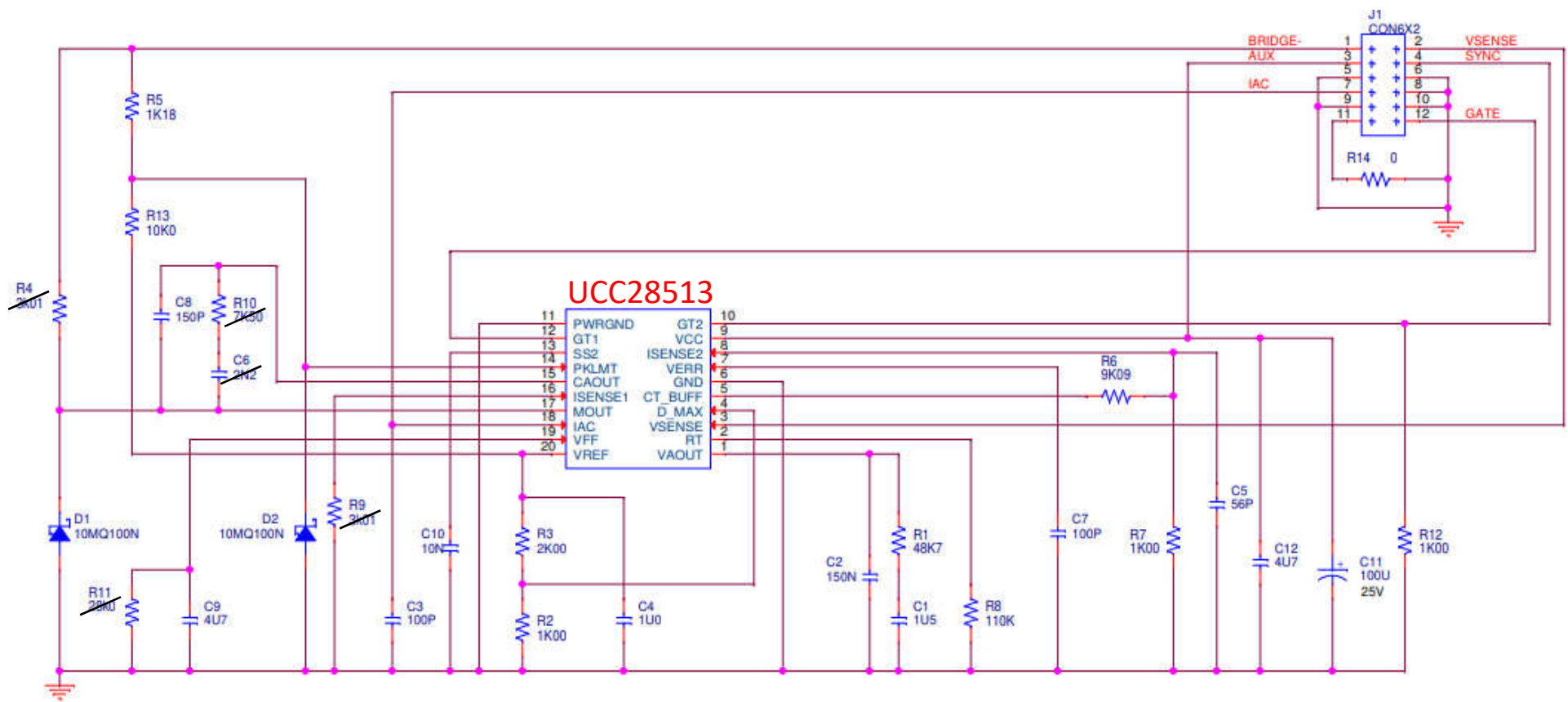
- The boost stage design is based on UCC28513. The PFC stage works well up to 200-220Vac input. Above this voltage range there are significant distortions at the points where sinusoidal input voltage reaches to its positive and negative peaks
- Output voltage is 385Vac
- Input voltage range is 85-264Vac; 50/60Hz
- The load on DC link output is 120W max
- Boost stage switching frequency is 277kHz
- Verified that the Vcc aux supply and Vref are stable
- Observed that the AC ripple on DC link goes up at around 200Vac but not sure if this is the reason or the result
- Observed that the duty cycle is about 70% at the peak sine wave at 85Vac but drops down to ~15% at the peaks of 220Vac at which point I start get distortions on the input current.
- The provided test results are at ~30W output power. It was observed that changing the load did not affect the behavior at around 220Vac.

Questions:

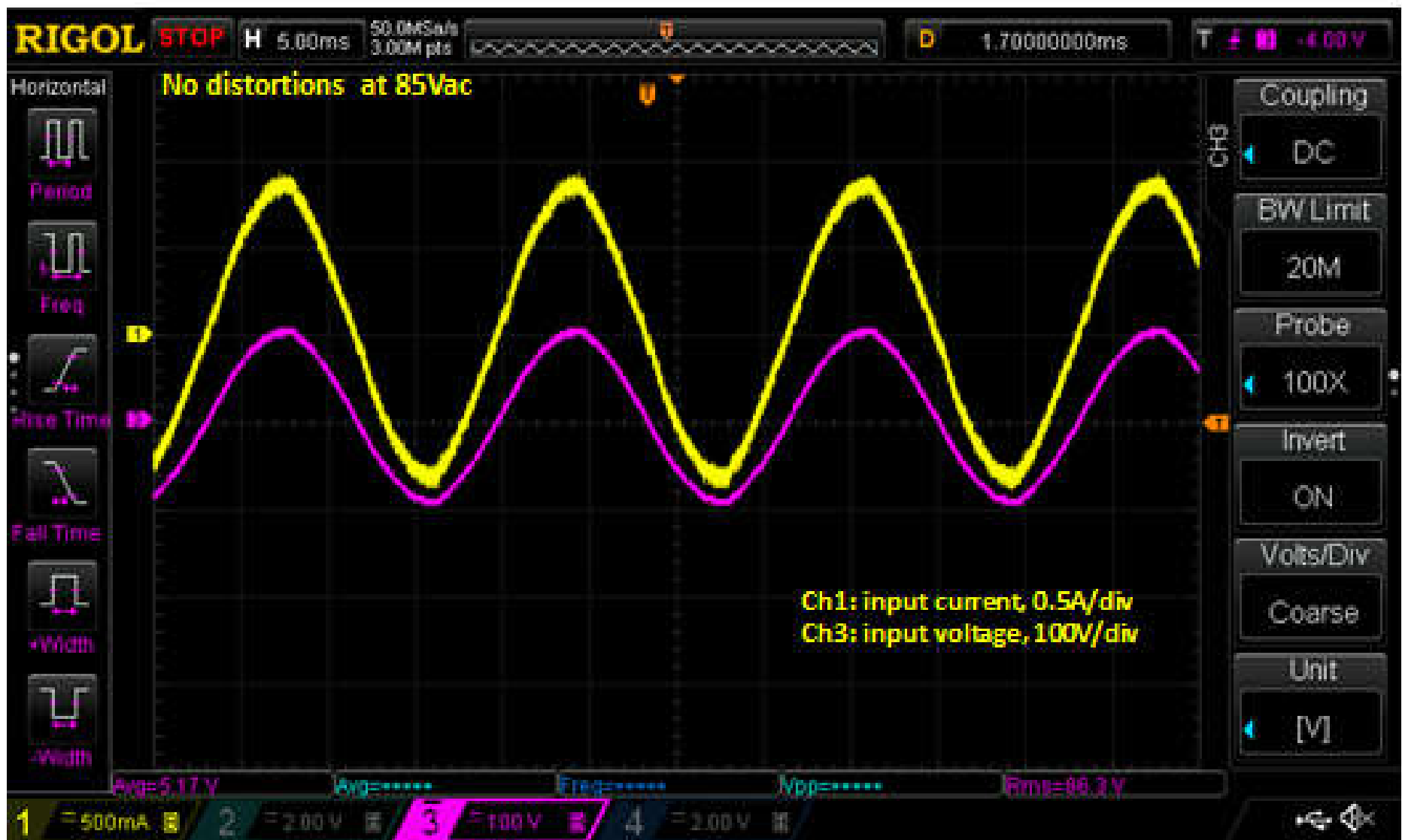
1. Based on the design values, what could potentially cause the distortions at the peak of sine waves at and above ~ 200Vac?
2. Fig. 36 on data sheet talks about max capacitance vs min duty cycle. What capacitor is referred in Fig 36?

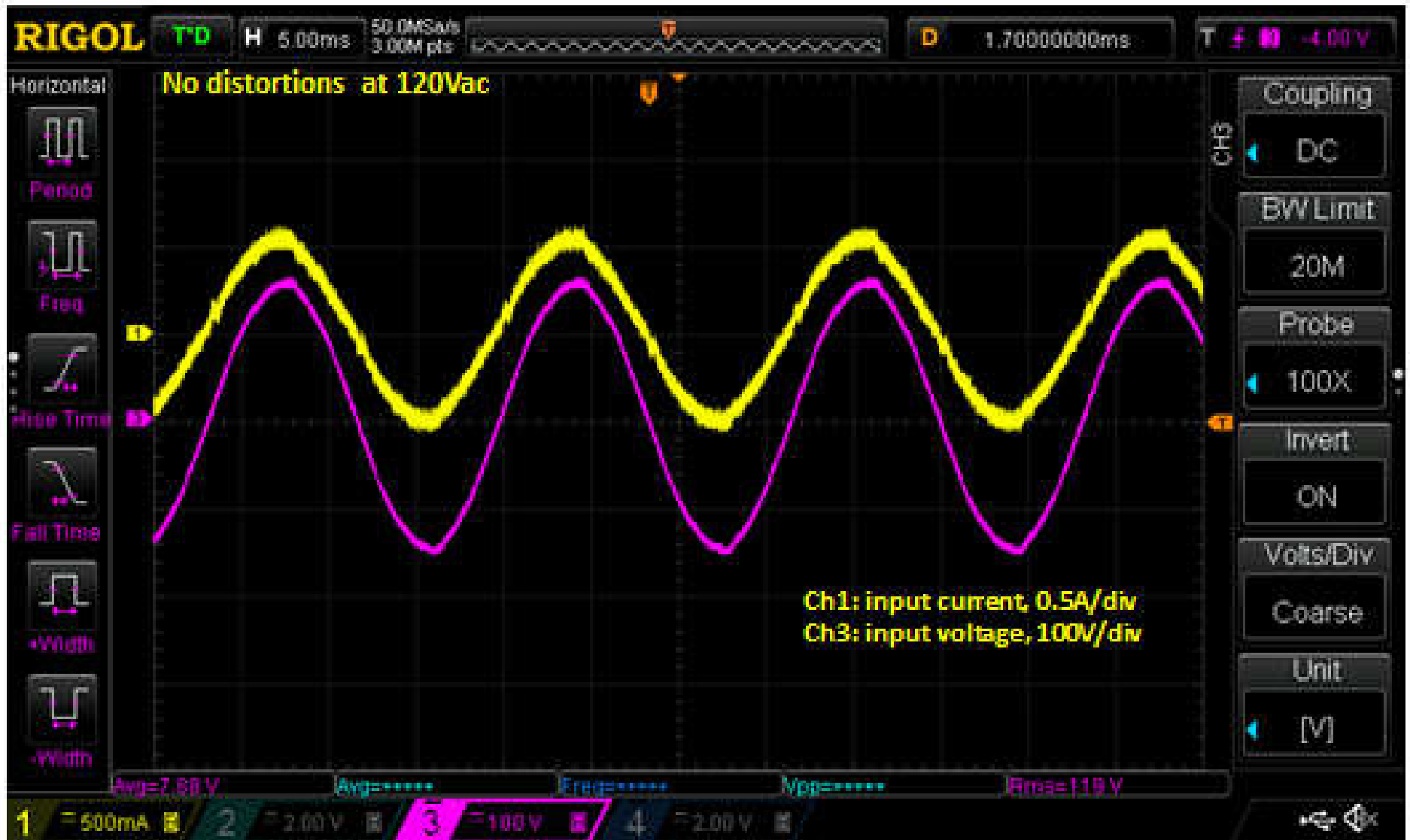


RefDes on data sheet	RefDes in design	values
R1	R47+R50	766K
R2	R43	0.22
R8/R12	R4/R9	3.16K
R3	R44+R49	1124K
R4	R60	22.1k
C1	C32	150uF
L1	L2	660uH
R13	R10	15.8K
C6	C6	680pF
C7	C8	150pF
R15	R11	30.1K
C8	C9	4.7uF
C10	C1	1.5uF
C11	C2	150nF
R21	R1	48.7K
R14	R13	10K
R7	R5	1.18K
C9	C4	1uF

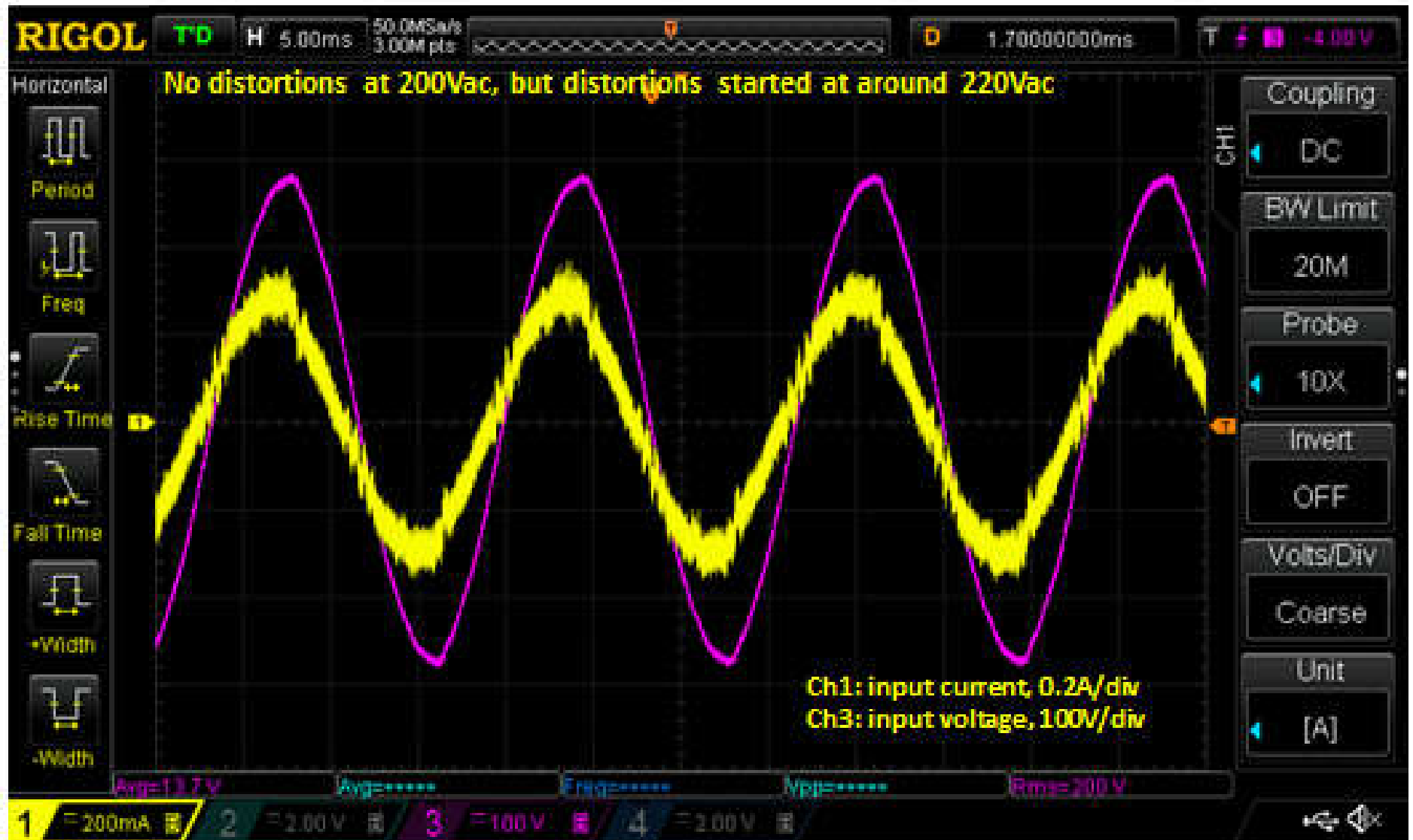


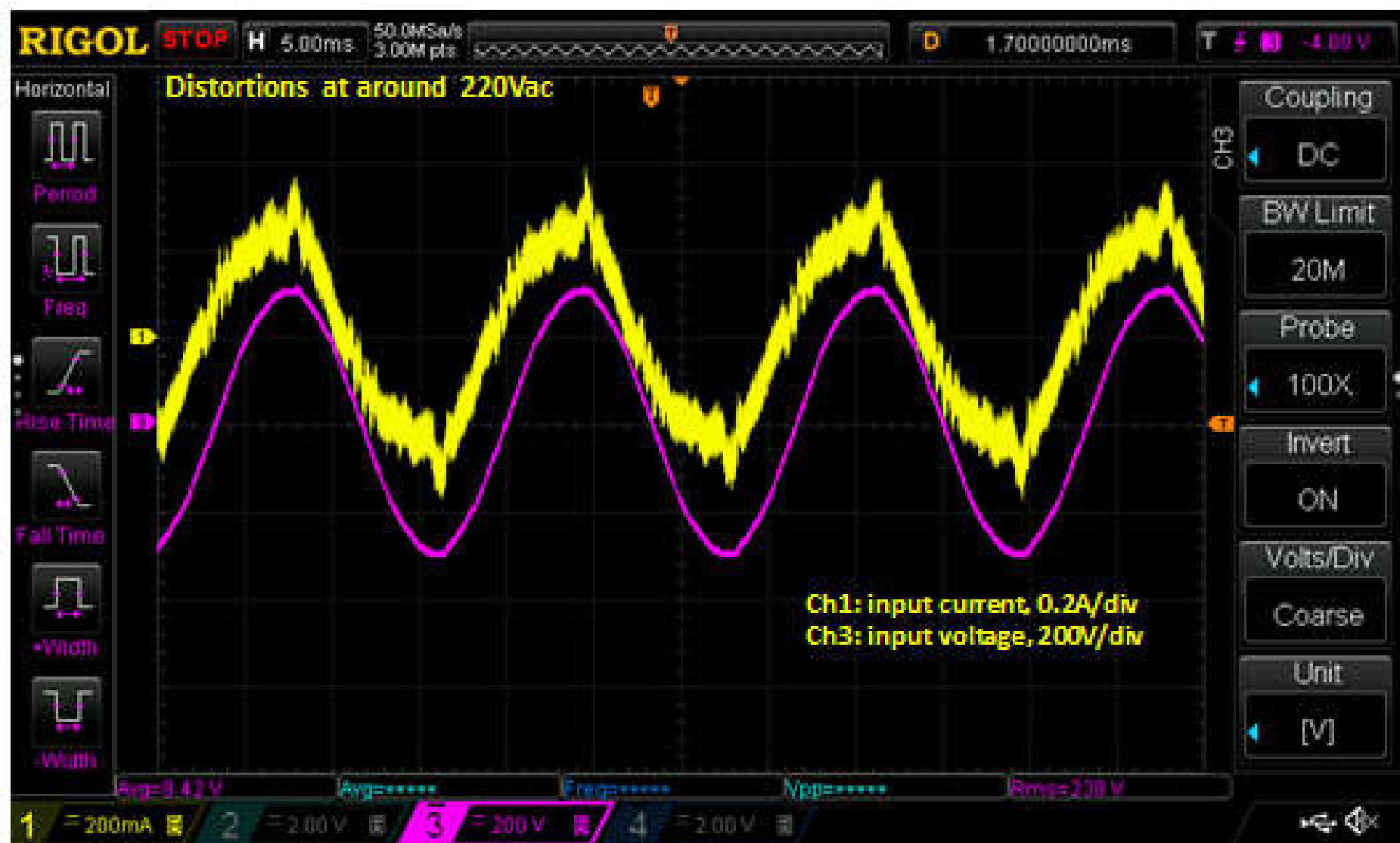
RefDes on data sheet	RefDes in design	values
R8/R12	R4/R9	3.16K
R13	R10	15.8K
C6	C6	680pF
R15	R11	30.1K

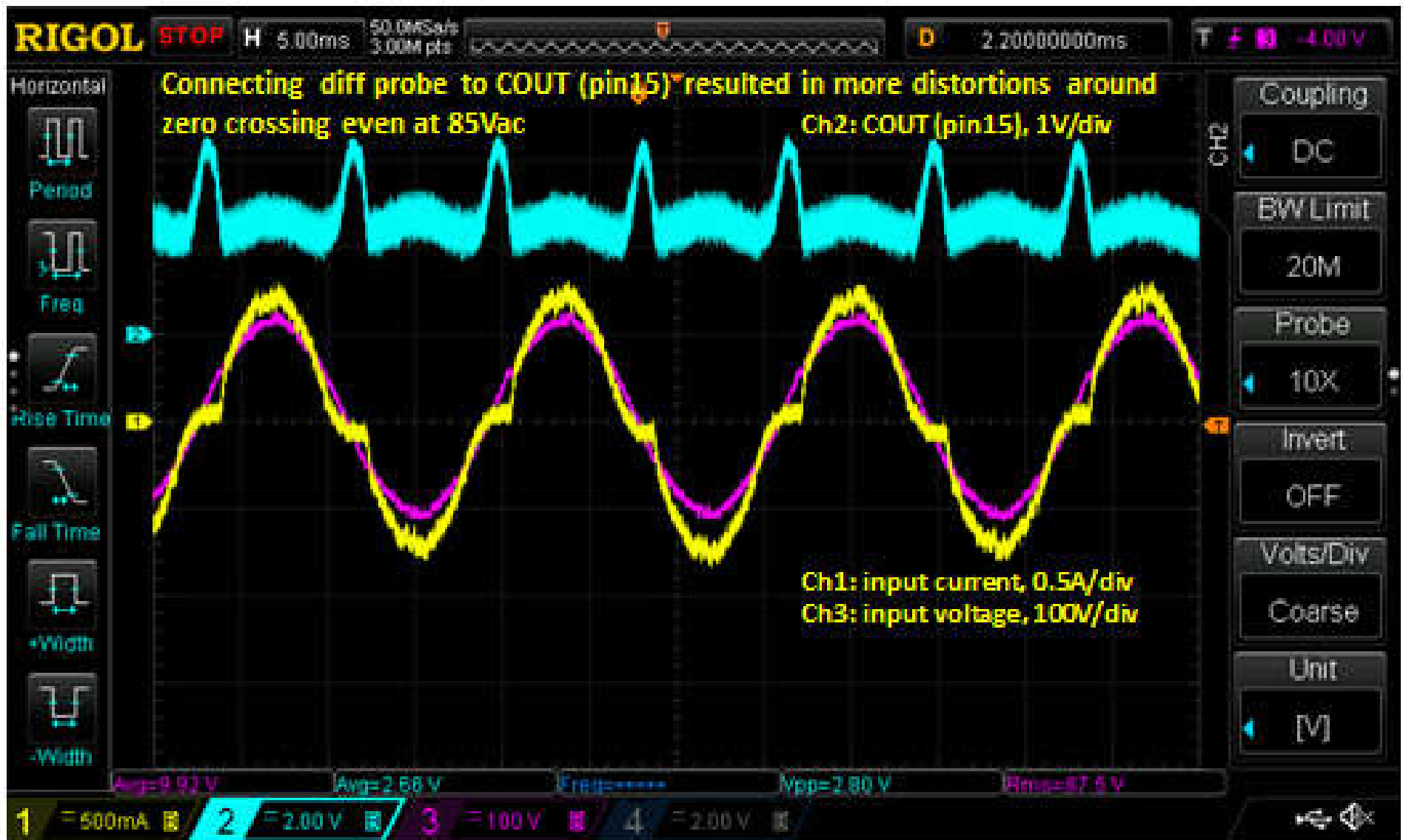


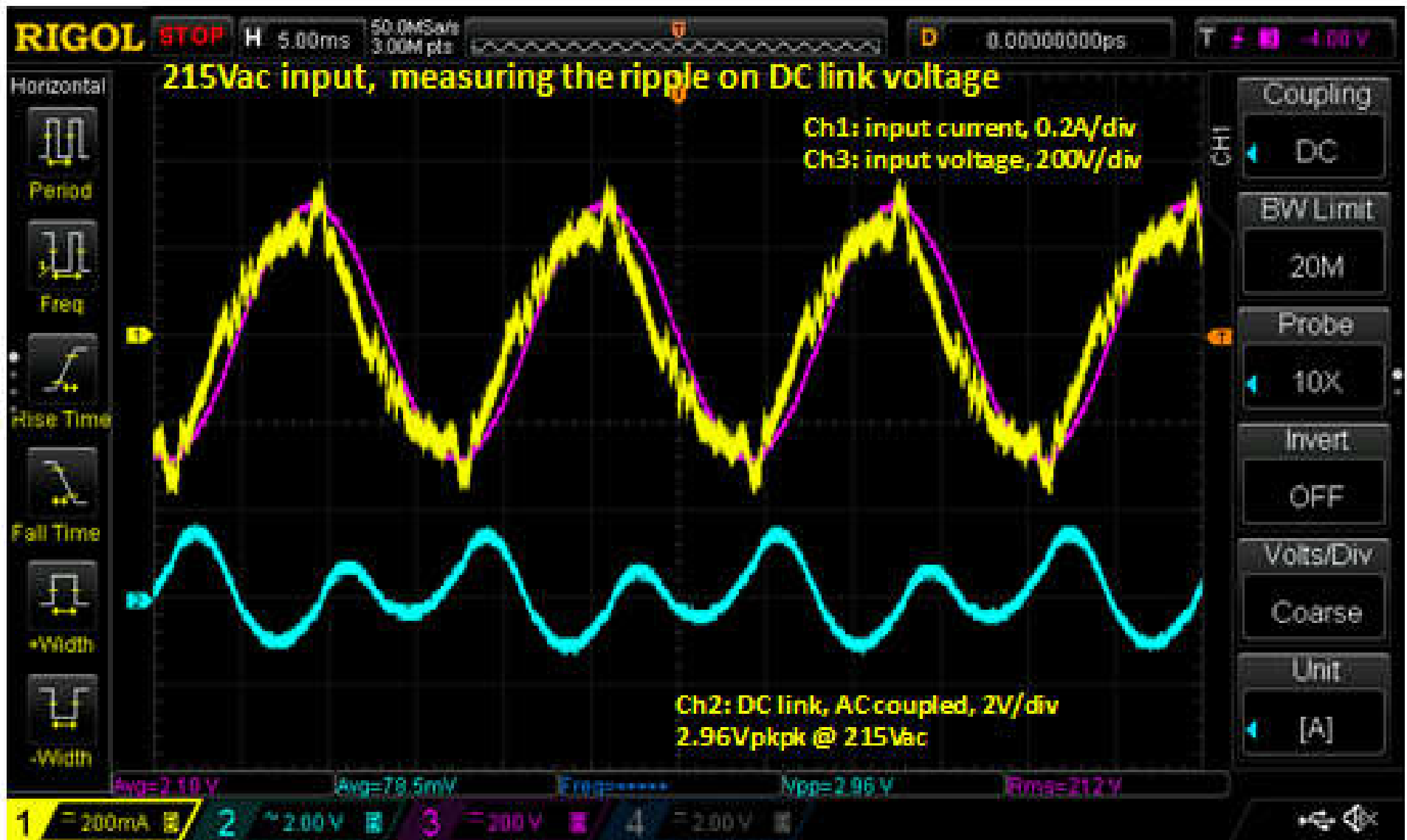


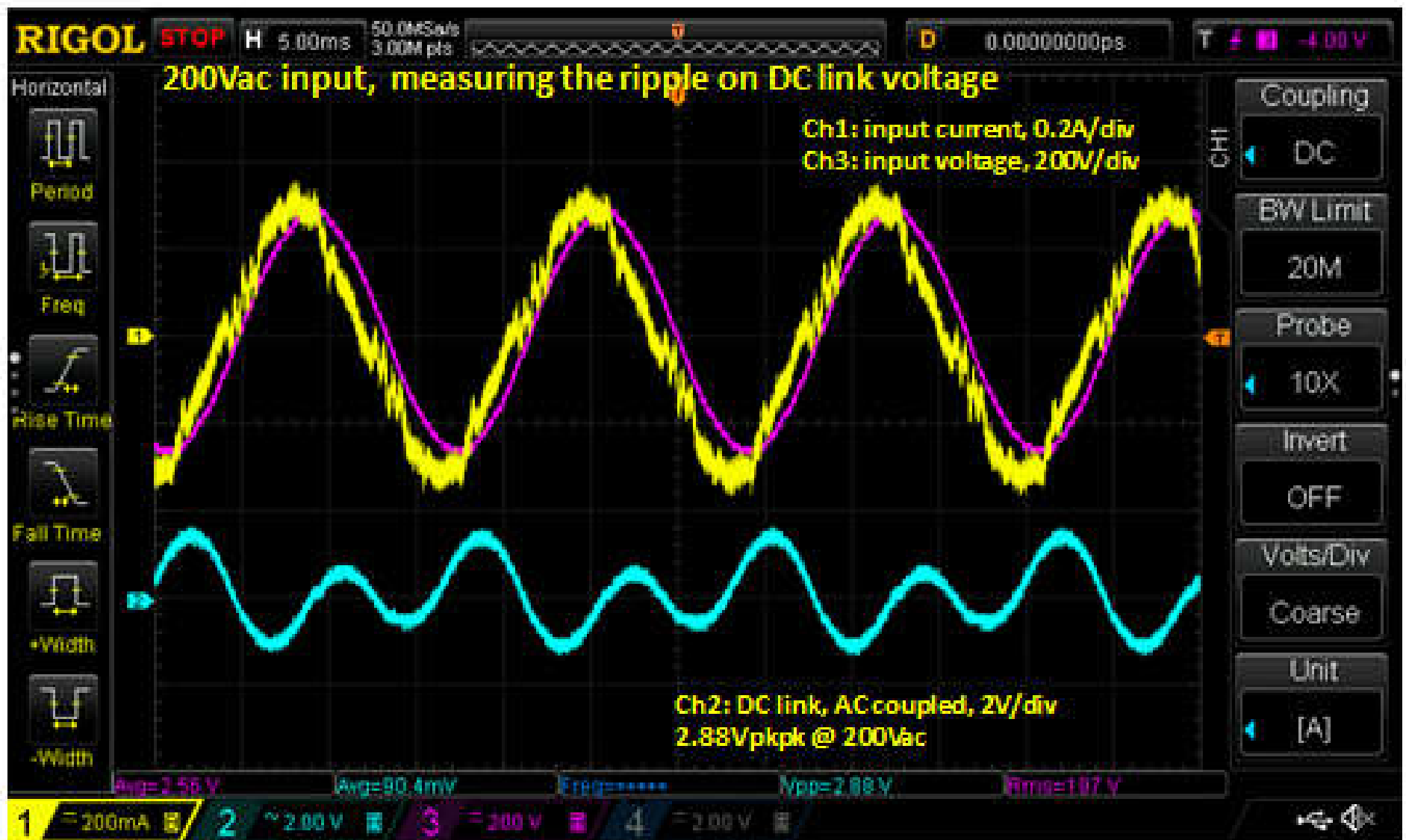
No distortions at 200Vac input, displacement between current and voltage is probably caused by the input capacitors on EMI filter









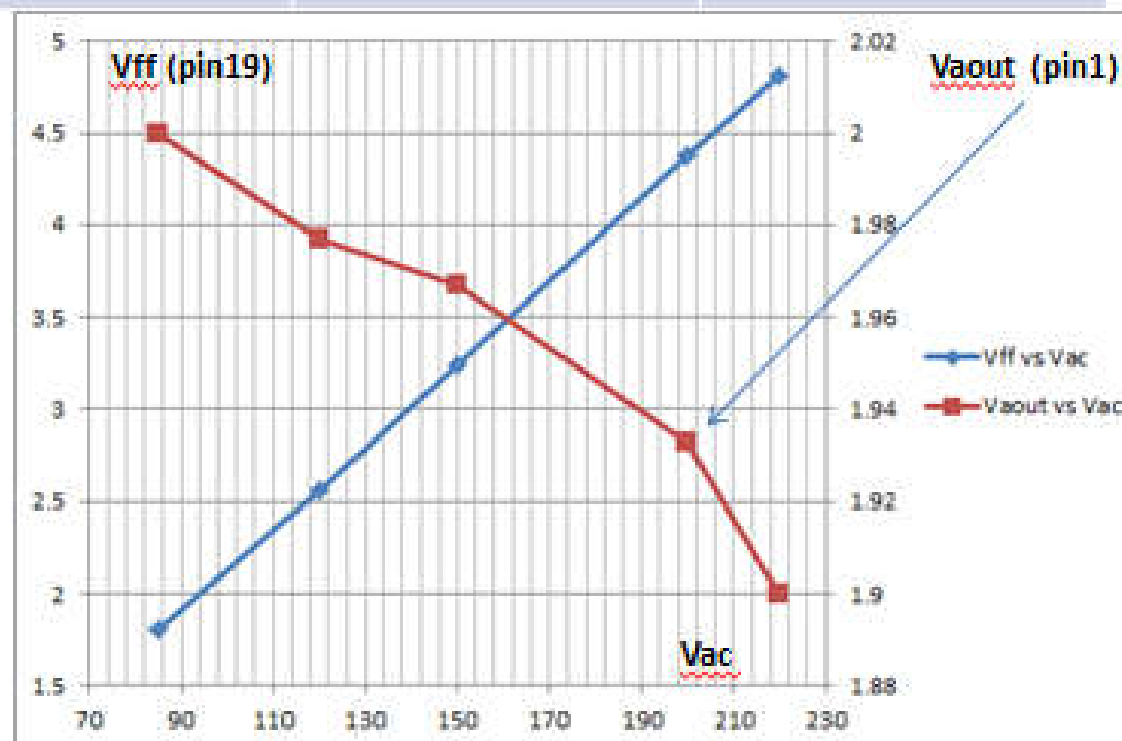


Input voltage (<u>Vrms</u>)	<u>Vff</u> on pin19 w/ DiffProbe (<u>Vdc</u>)	<u>Vaout</u> on pin1 w/ DVM (<u>Vdc</u>)
85	1.80	2.000
120	2.56	1.977
150	3.24	1.967
200	4.38	1.933
220	4.81	1.900

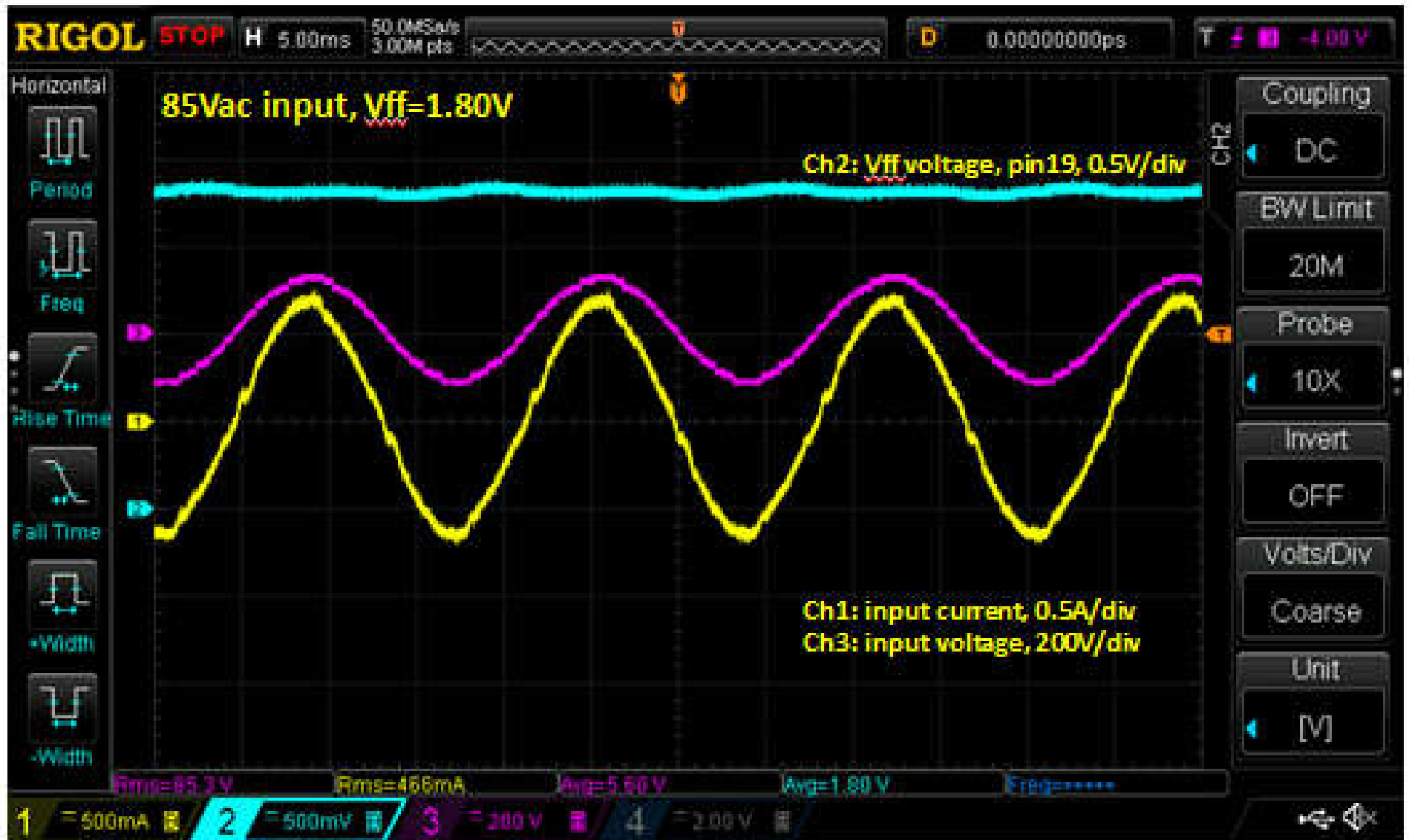
$$0 \leq i_{IAC}(t) \leq 500 \mu A,$$

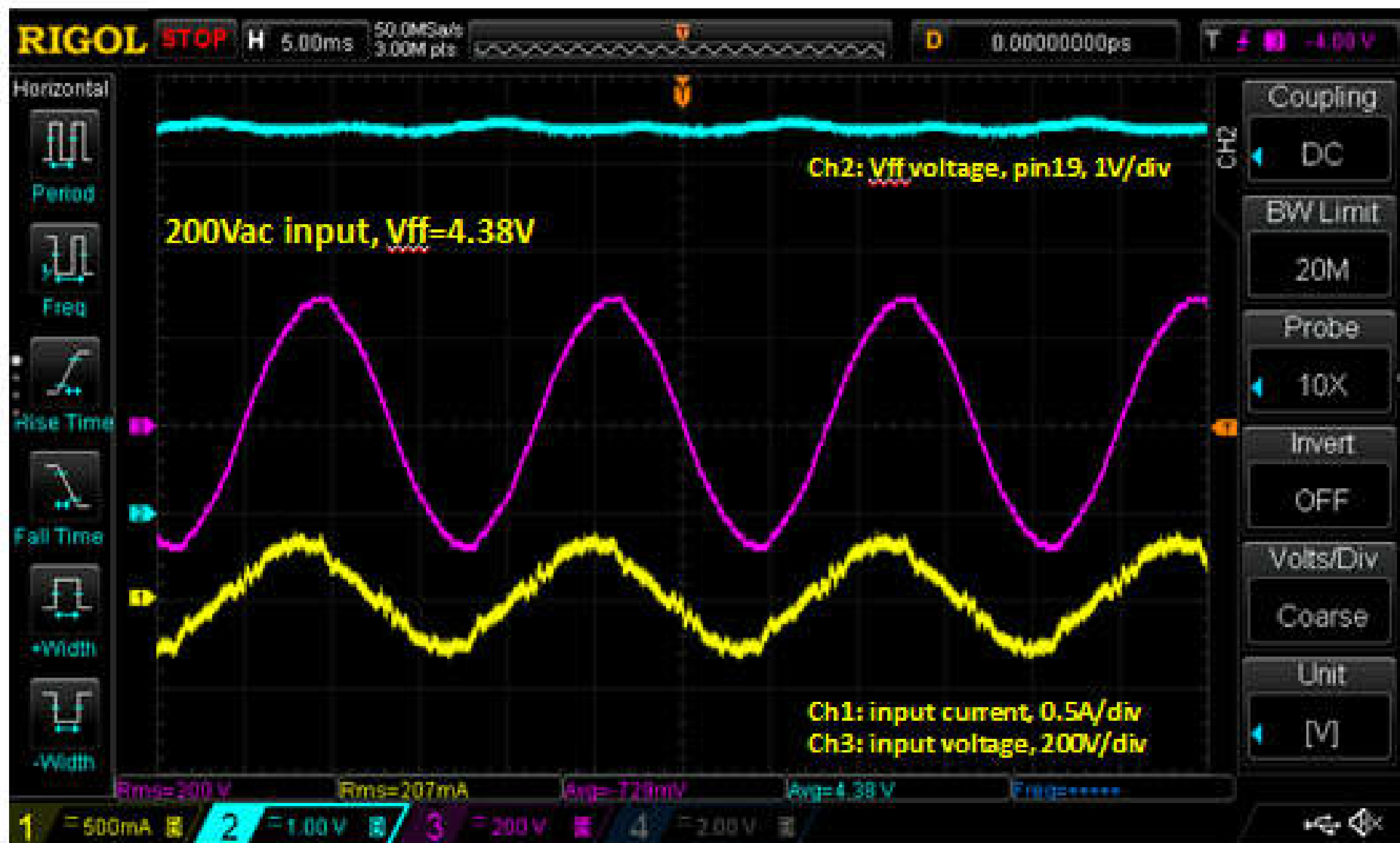
$$0 \leq V_{VAOUT}(t) \leq 5 V,$$

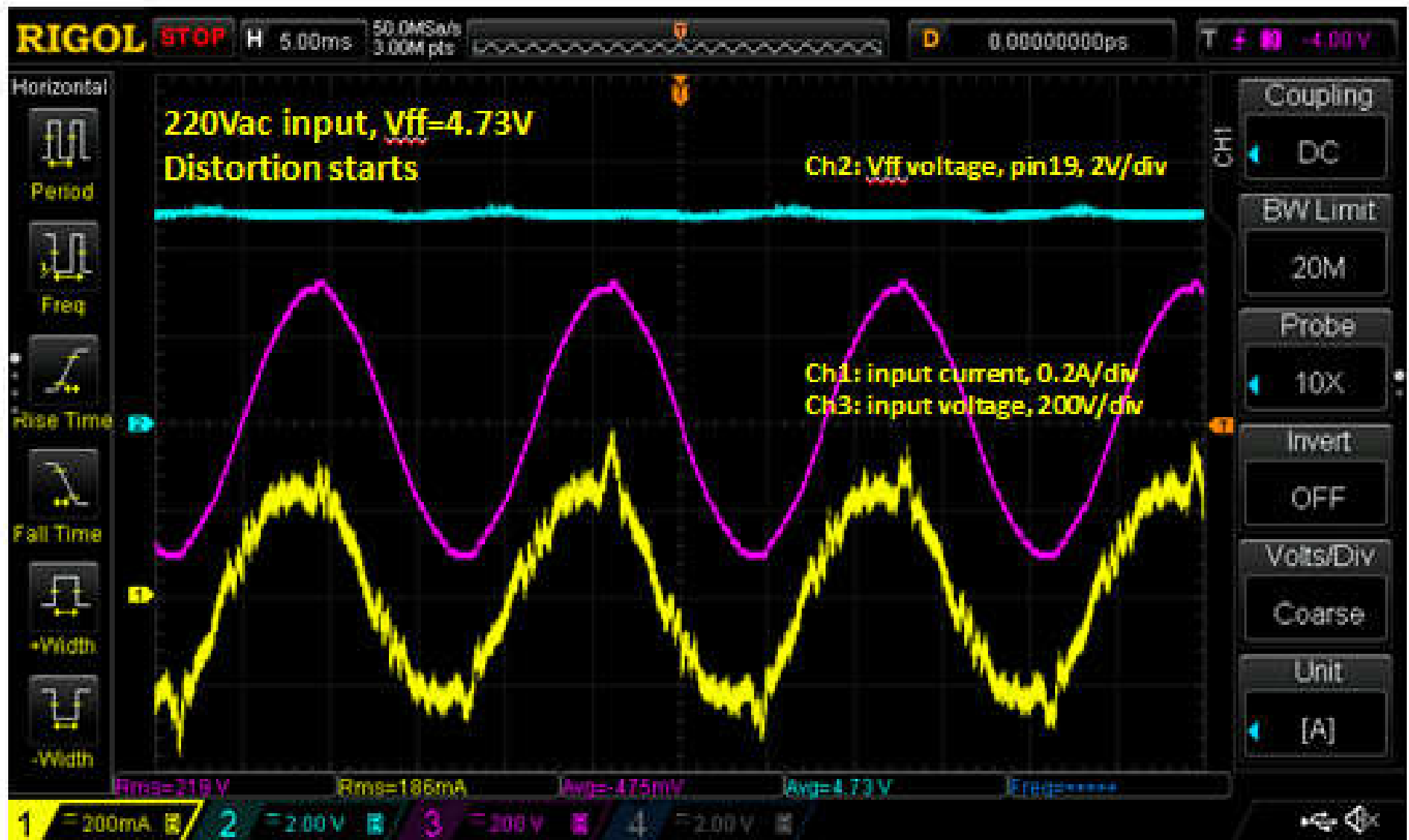
$$1.4 V \leq V_{VFF} \leq V_{VREF} - 1.4 V$$



- Vff increases proportionally and Vaout drops inversely as Vac increases.
- Vaout seems to start low at 2V and not varying within its full range of 0-5V

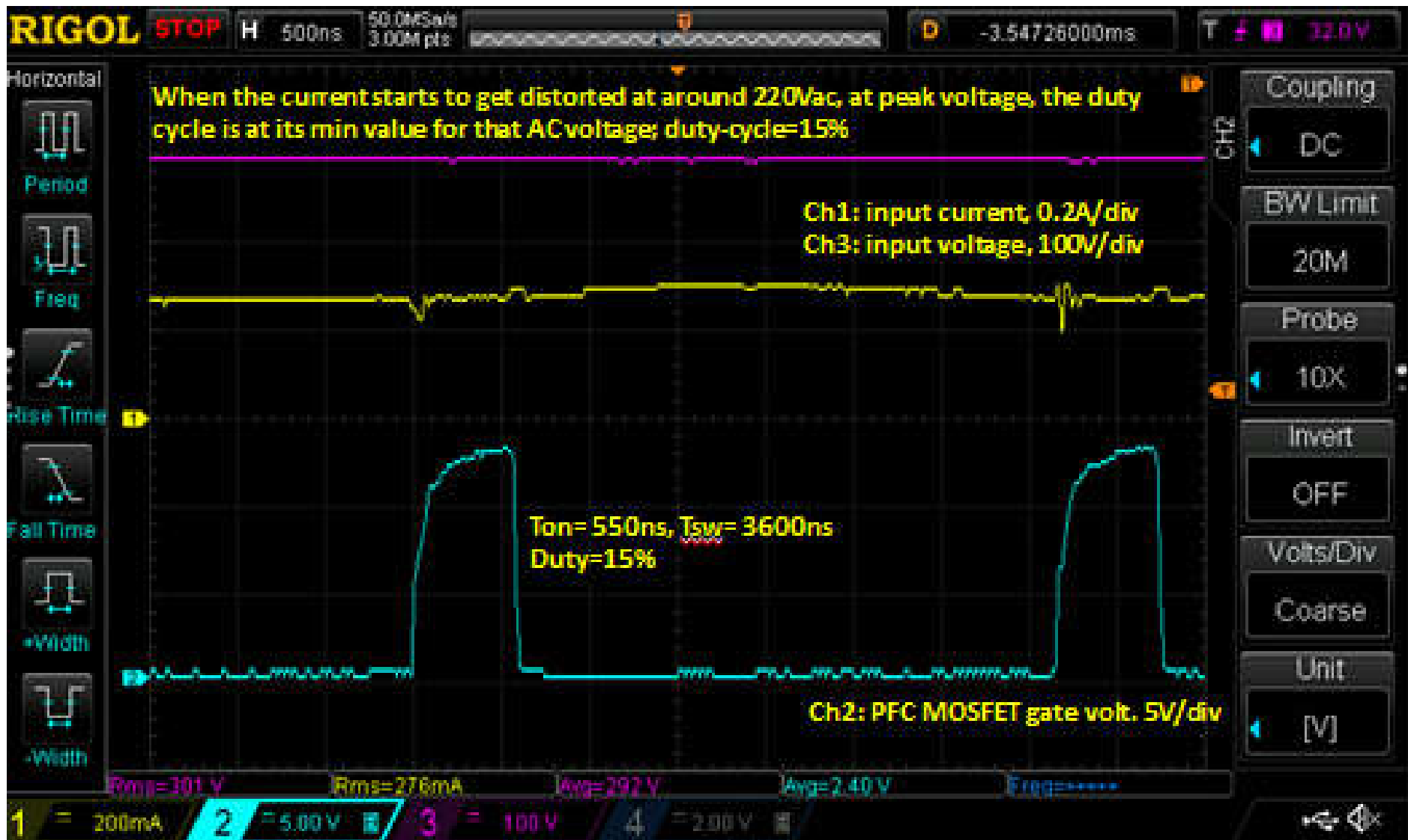






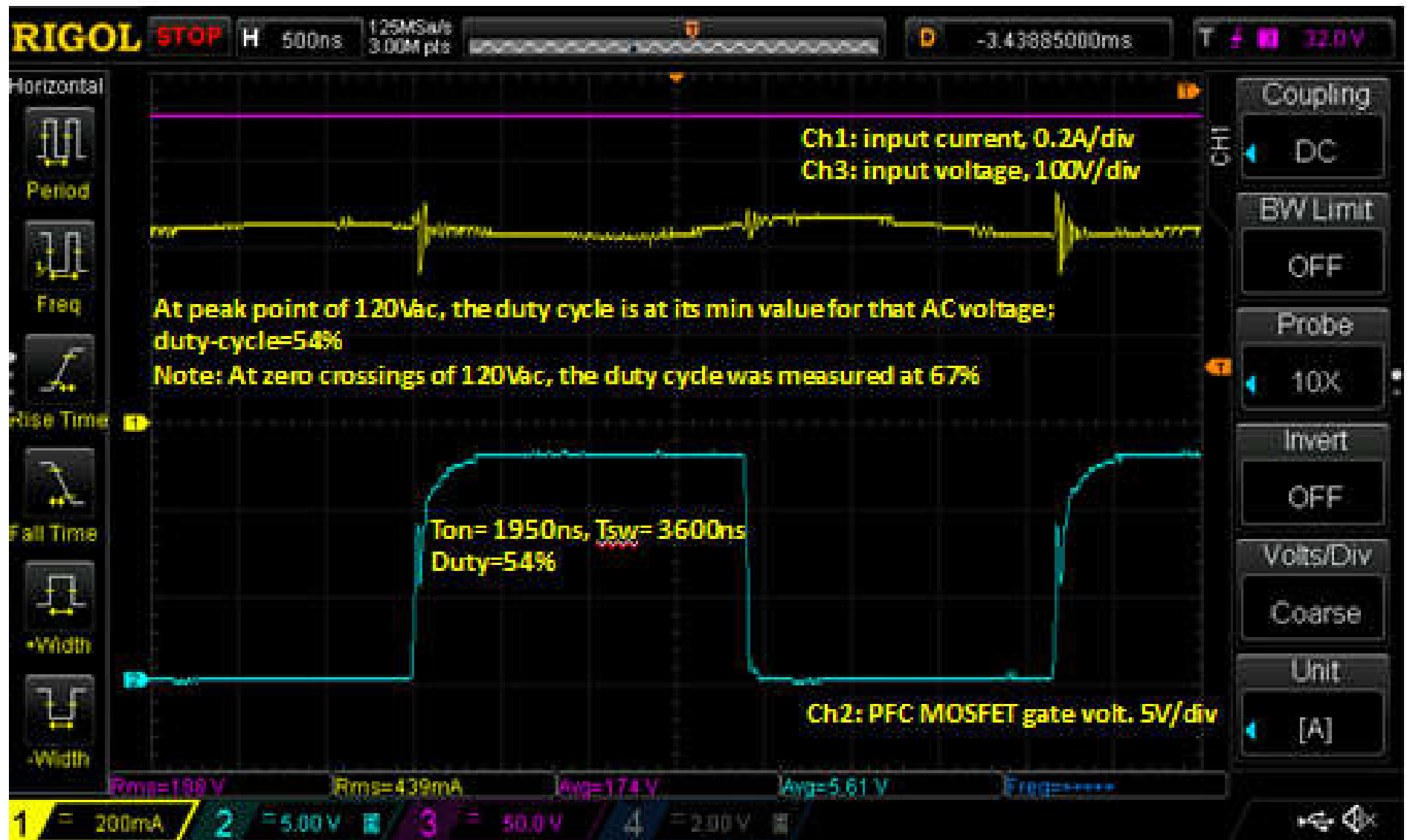
UCC28513 questions to TI, Alex W., 11/17/2022

At the peak of sinewaves when distortion starts, the duty cycle of boost MOSFET is about 15%



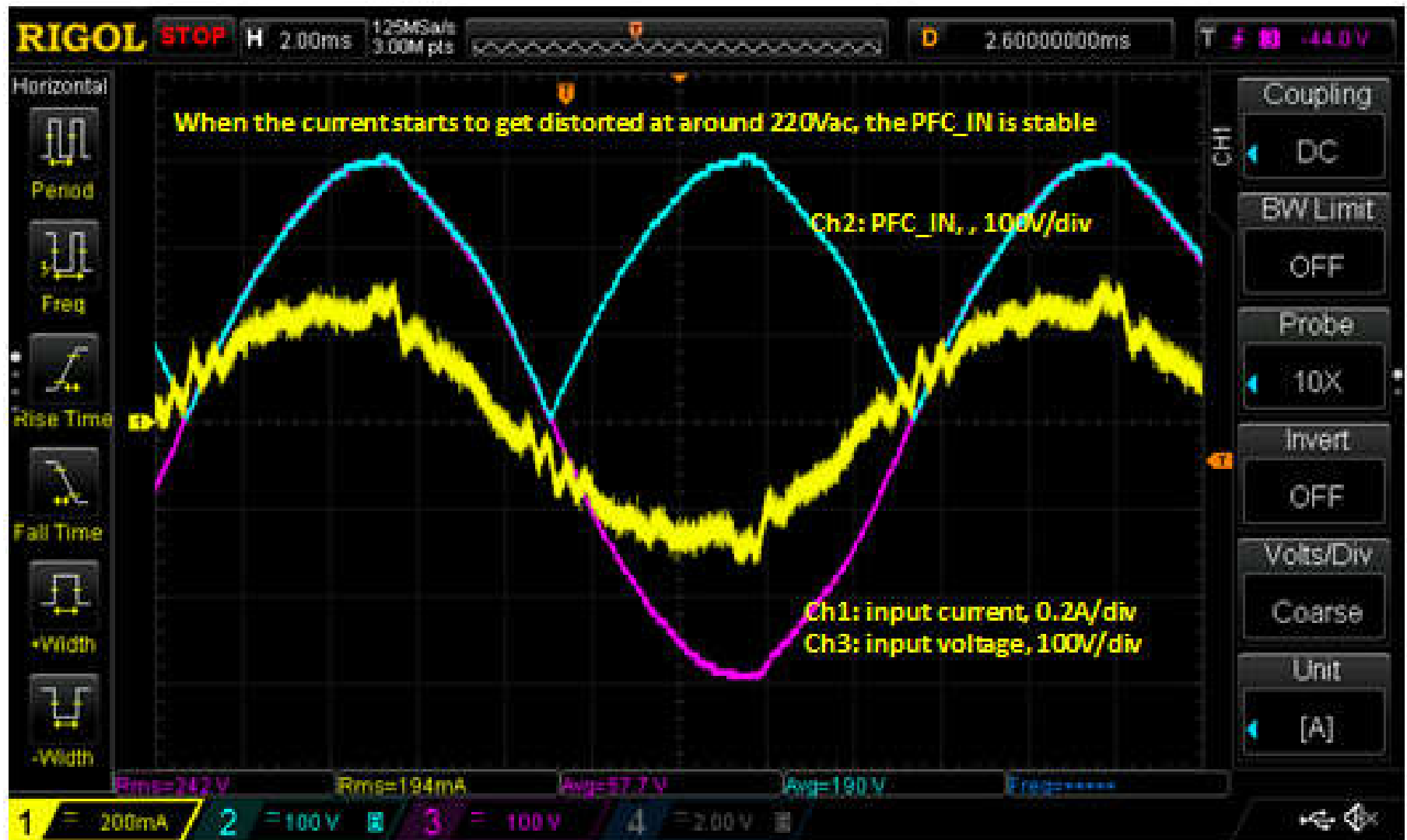
UCC28513 questions to TI, Alex W., 11/17/2022

At the peak of sinewaves at 120Vac, the duty cycle of boost MOSFET is about 54%

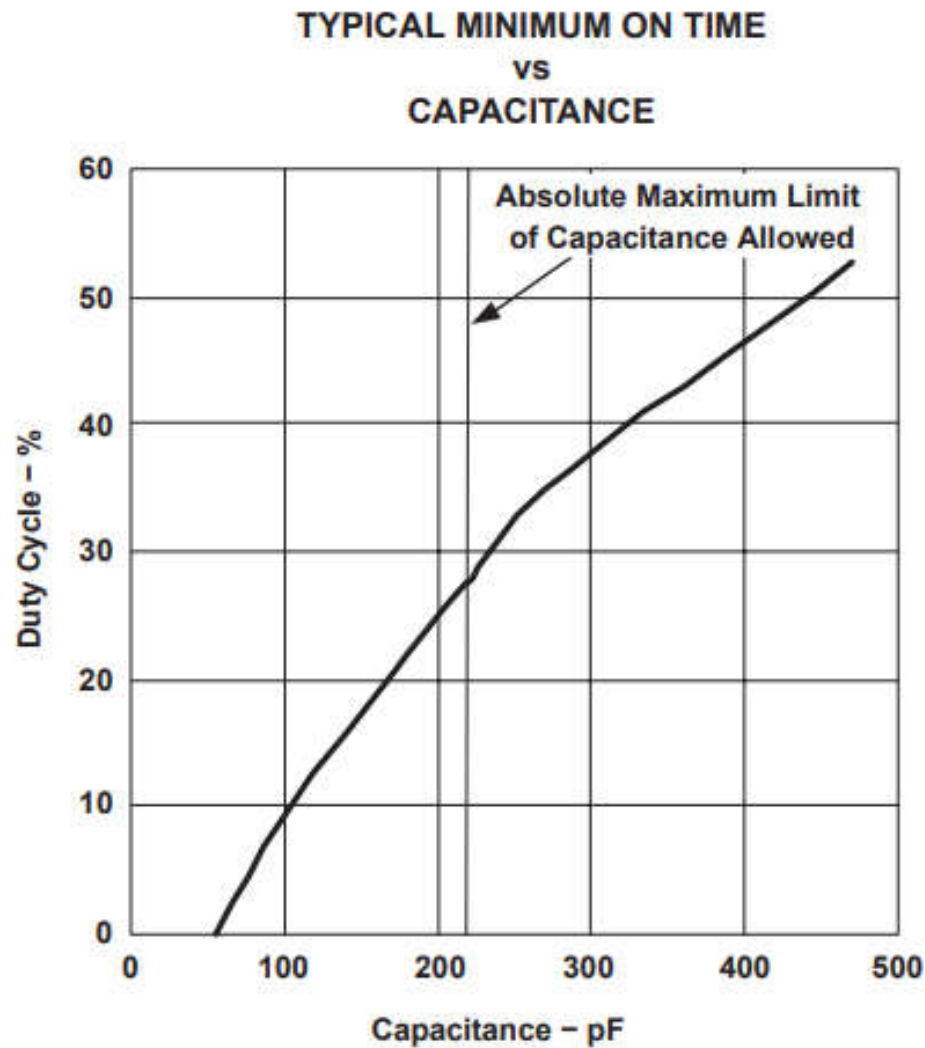


UCC28513 questions to TI, Alex W., 11/17/2022

At 220Vac when distortions starts, the rectified input voltage (used for Vff) is shown below



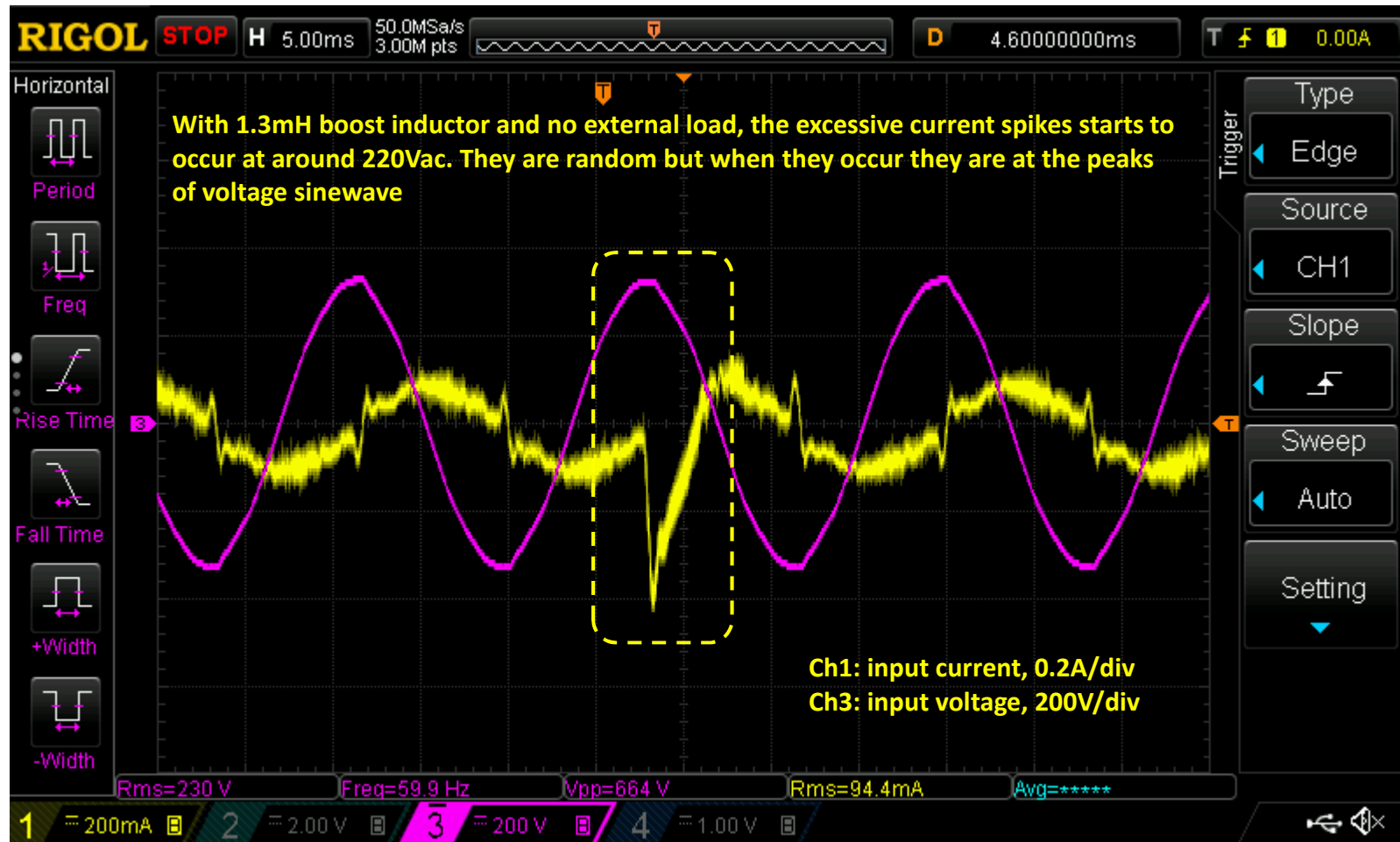
Referring to Fig 36 in the data sheet of UCC28513DW, what specific capacitor controls the min duty cycle?



Note: The capacitor on Isense2 affects the min duty cycle on GATE2 output, PFC is on GATE1 output

Figure 36

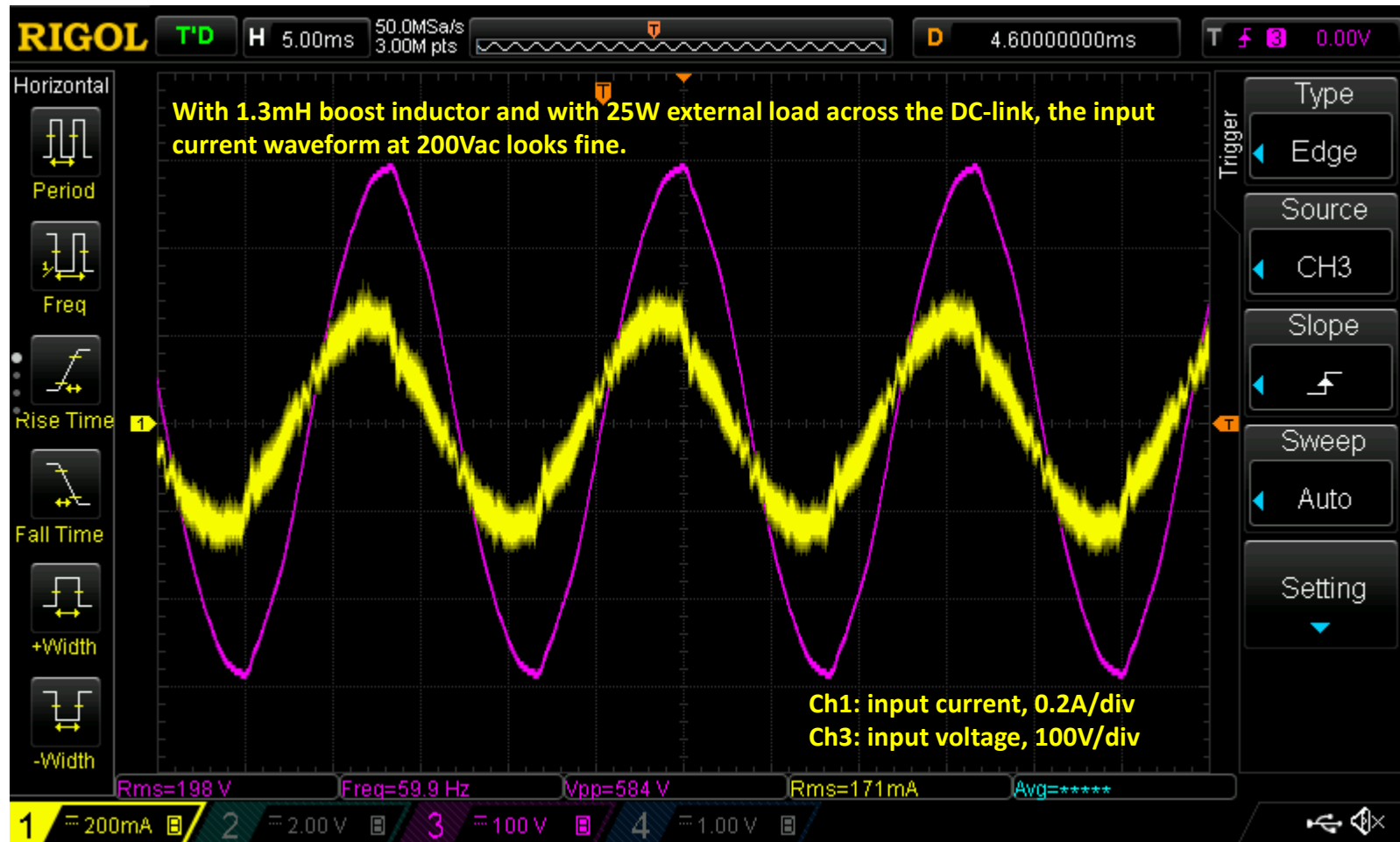
Boost inductor is changed from 660uH to $2 \times 660\mu\text{H} = 1.32\text{mH}$



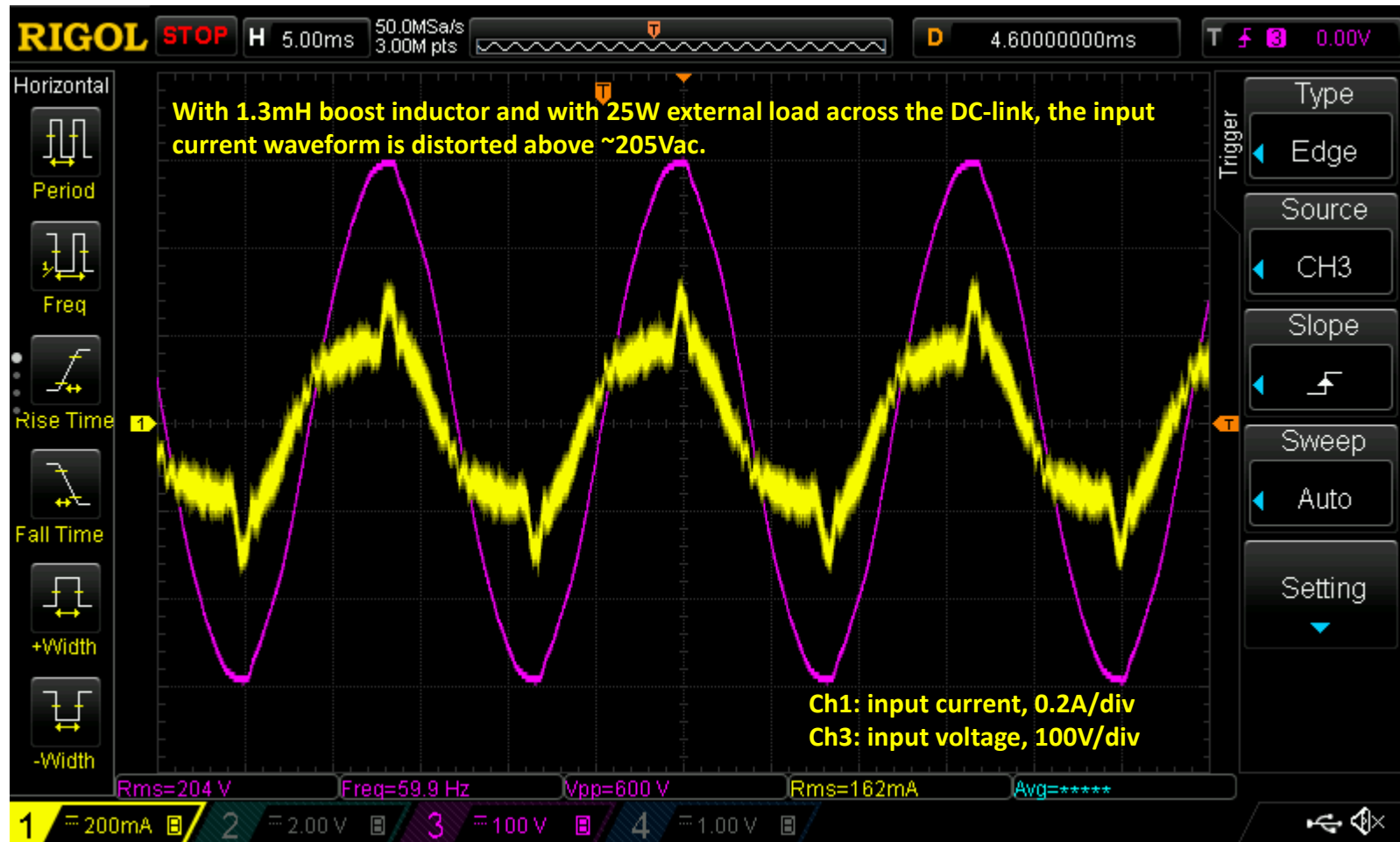
Boost inductor is changed from 660uH to $2 \times 660\mu\text{H} = 1.32\text{mH}$



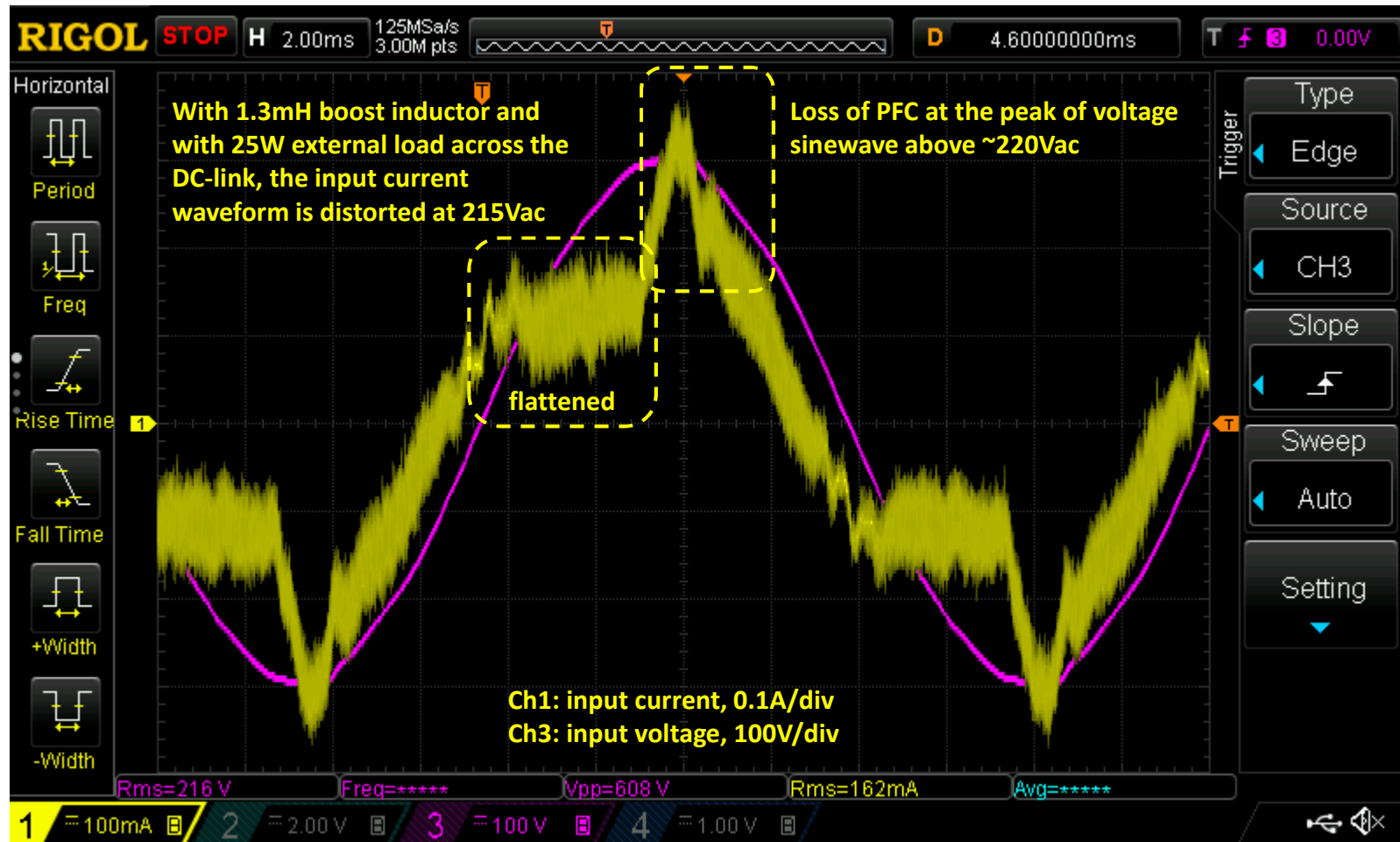
Boost inductor is changed from 660uH to $2 \times 660\mu\text{H} = 1.32\text{mH}$



Boost inductor is changed from 660uH to $2 \times 660\mu\text{H} = 1.32\text{mH}$



Boost inductor is changed from 660uH to $2 \times 660\mu\text{H} = 1.32\text{mH}$

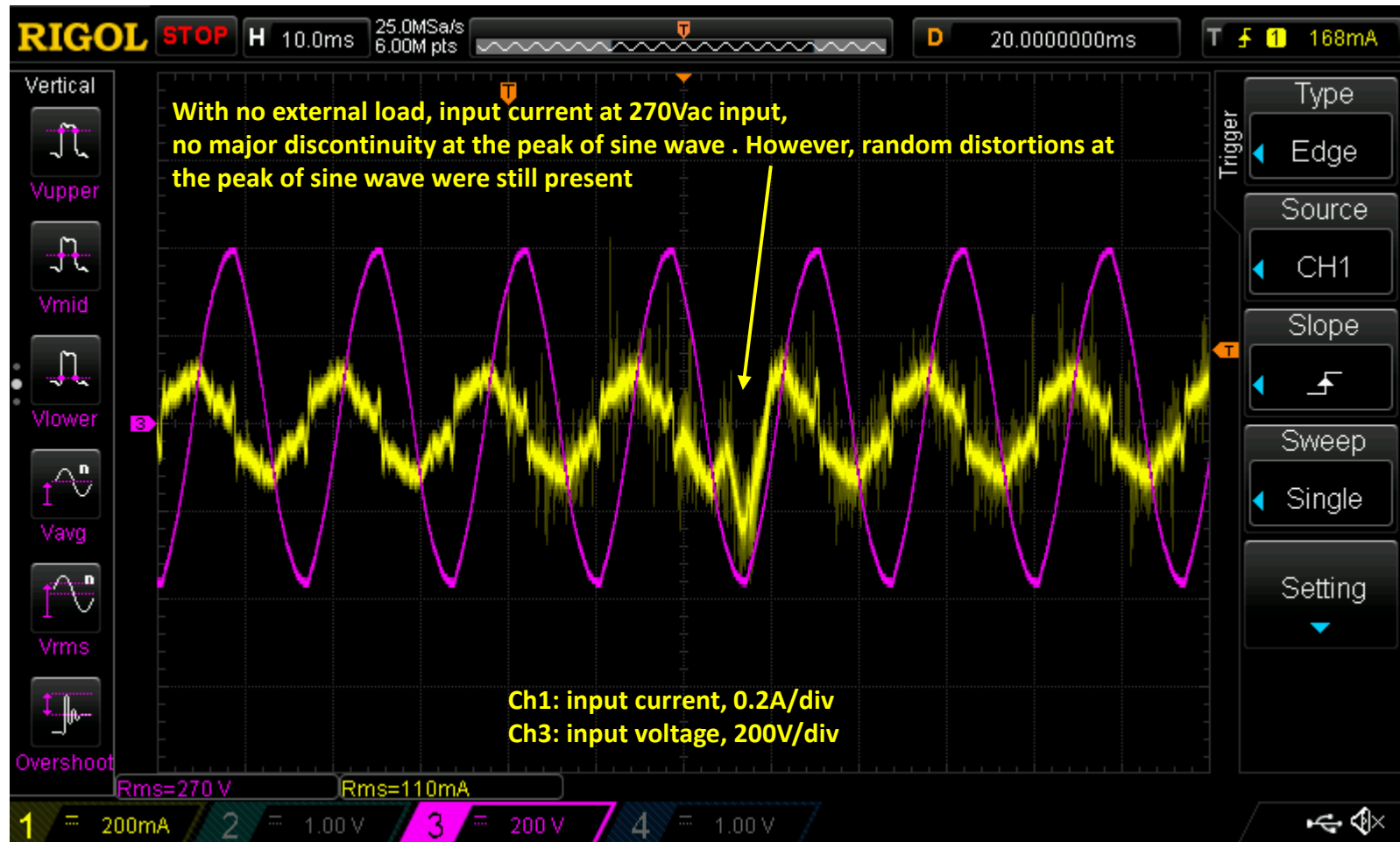


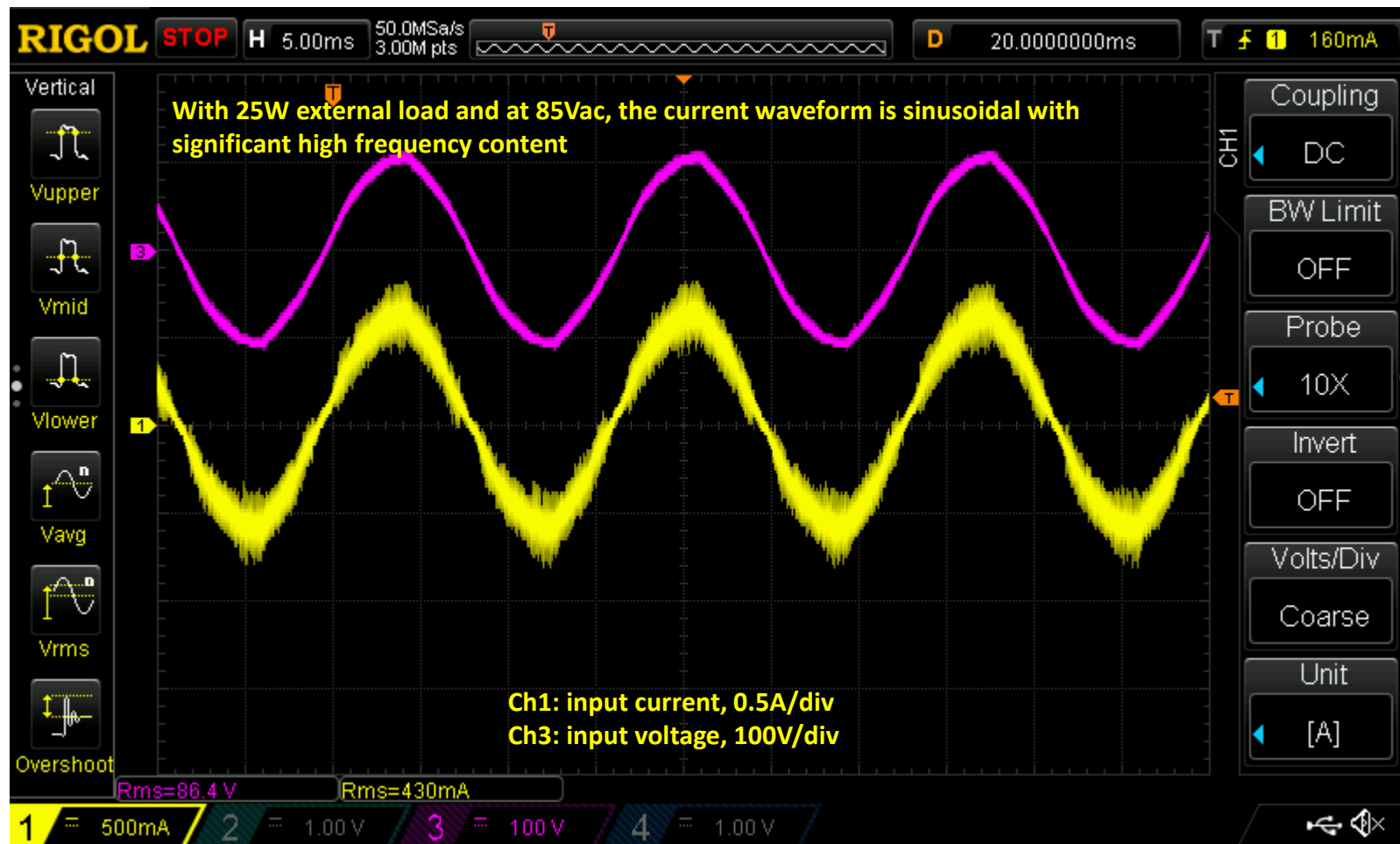
RefDes on data sheet	RefDes in design	values	Values on 12/11/2022
R1	R47+R50	766K	
R2	R43	0.22	0.44
R8/R12	R4/R9	3.16K	2.74K
R3	R44+R49	1124K	
R4	R60	22.1k	
C1	C32	150uF	
L1	L2	660uH	
R13	R10	15.8K	55K (56K//3.3Meg)
C6	C6	680pF	233pF (200p//33p)
C7	C8	150pF	33pF
R15	R11	30.1K	
C8	C9	4.7uF	
C10	C1	1.5uF	
C11	C2	150nF	
R21	R1	48.7K	
R14	R13	10K	
R7	R5	1.18K	1.50K (3.3K//2.74K)
C9	C4	1uF	

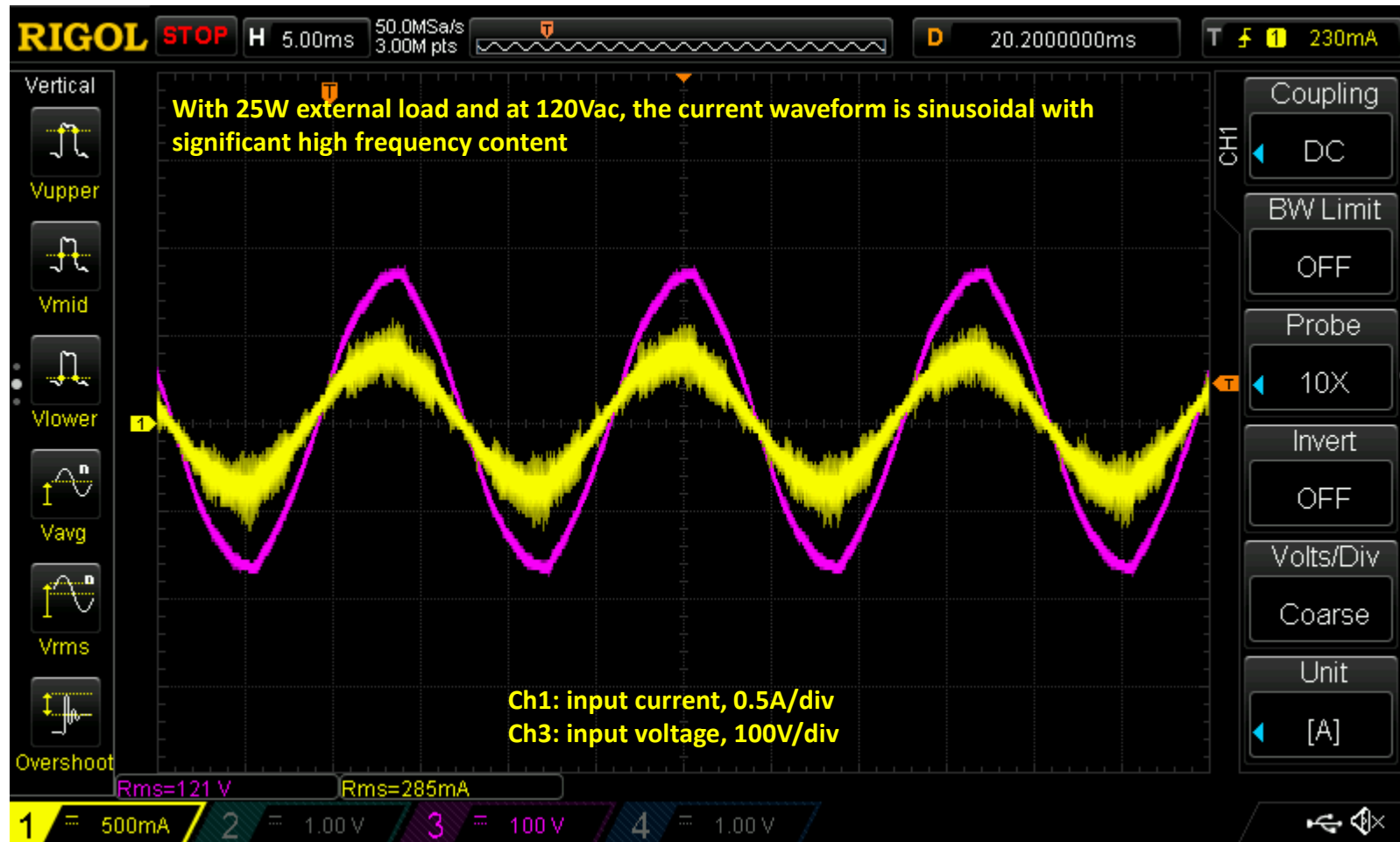
**Changes made
on 12/11 are
highlighted**

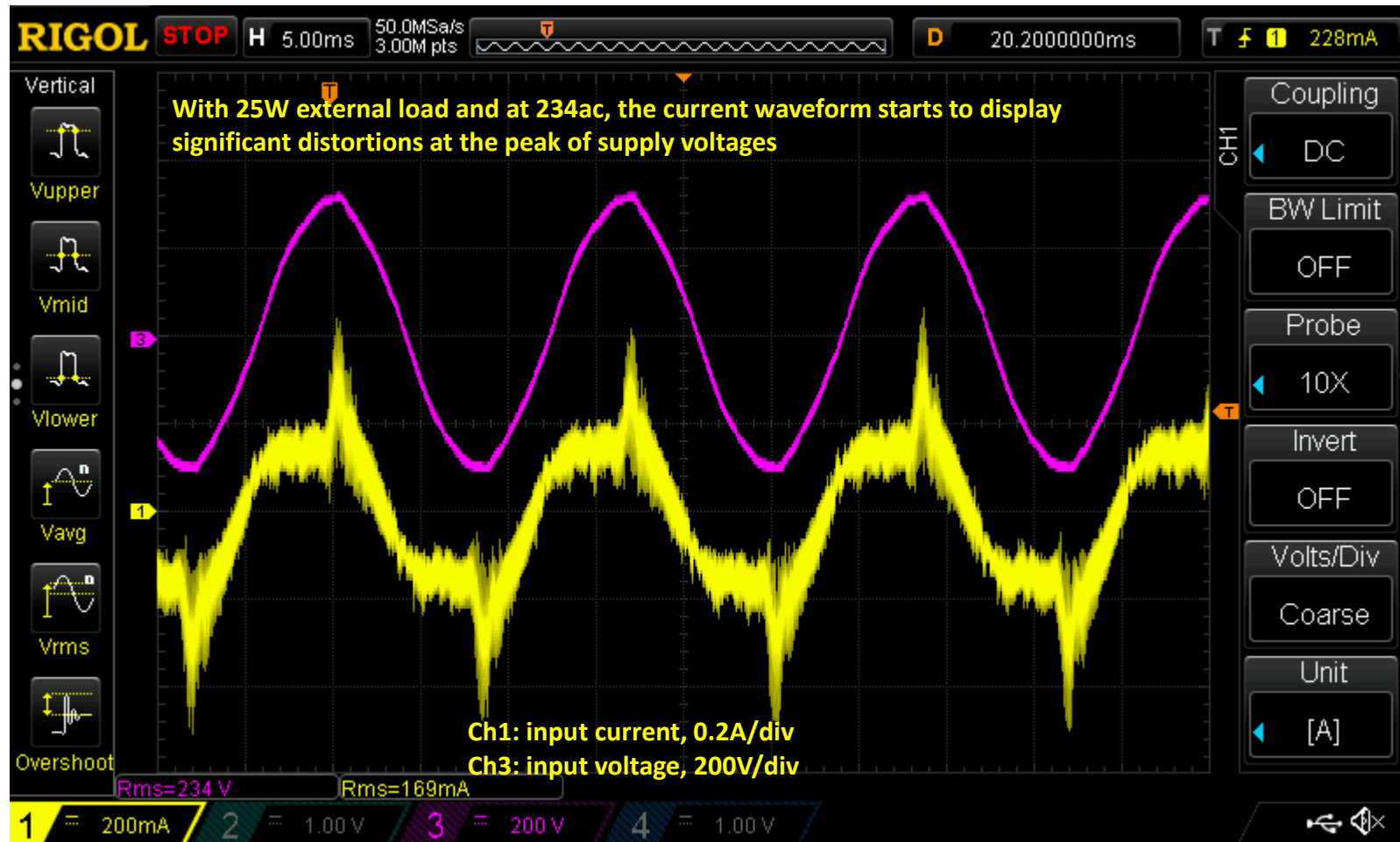
← **Also tried 15.8k**











Measuring inductor current vs gate voltages, 12/18

There are only two current probes at the moment:

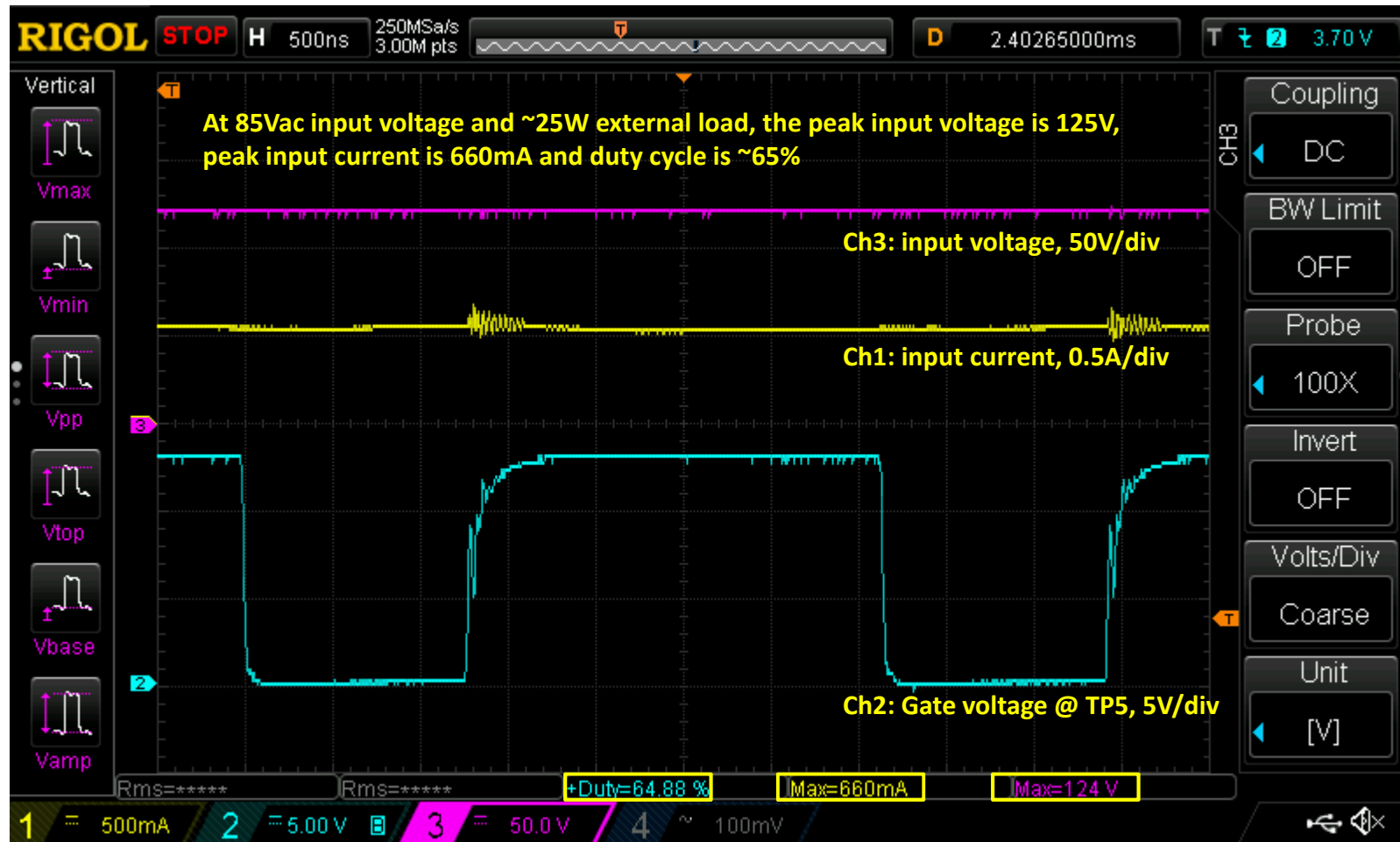
Ch1: Fluke 80i-110S has limited bandwidth and is used to measure 60Hz input current only.

Ch4: Rogowski probe, CWTUM-015-B, is used to measure the high frequency switching ripple only.

In addition, a $100\text{m}\Omega$ and $20\text{m}\Omega$ sense resistor was connected in series with inductor and the voltage across the resistor was measured with a differential probe. However, doing so resulted in further instability and distortion and therefore abandoned.

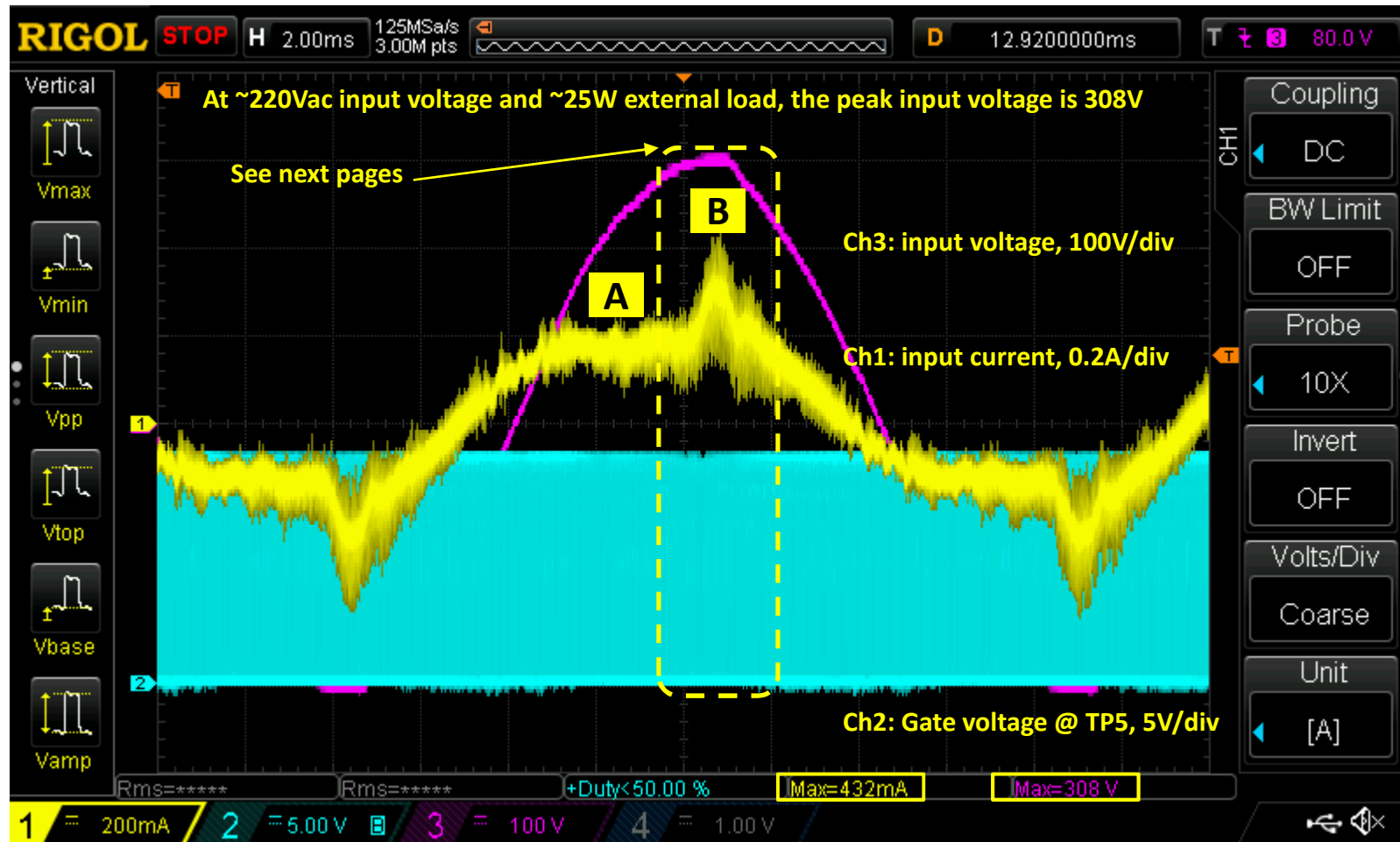
Measuring inductor current vs gate voltages, 12/18

Ch1: Current probe is Fluke 80i-110S, which has limited bandwidth and is used to measure 60Hz input current



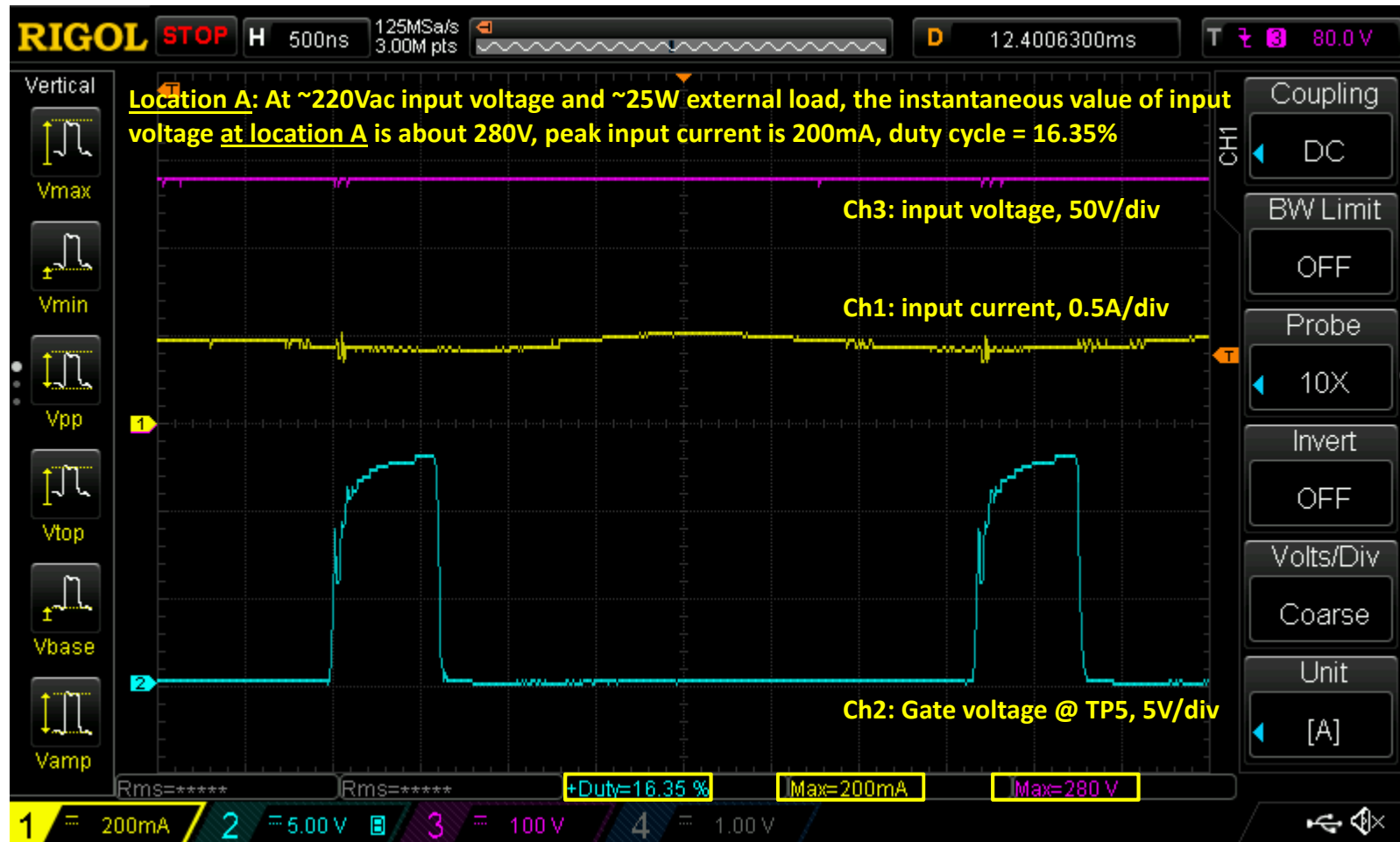
Measuring inductor current vs gate voltages, 12/18

Ch1: Current probe is Fluke 80i-110S, which has limited bandwidth and is used to measure 60Hz input current



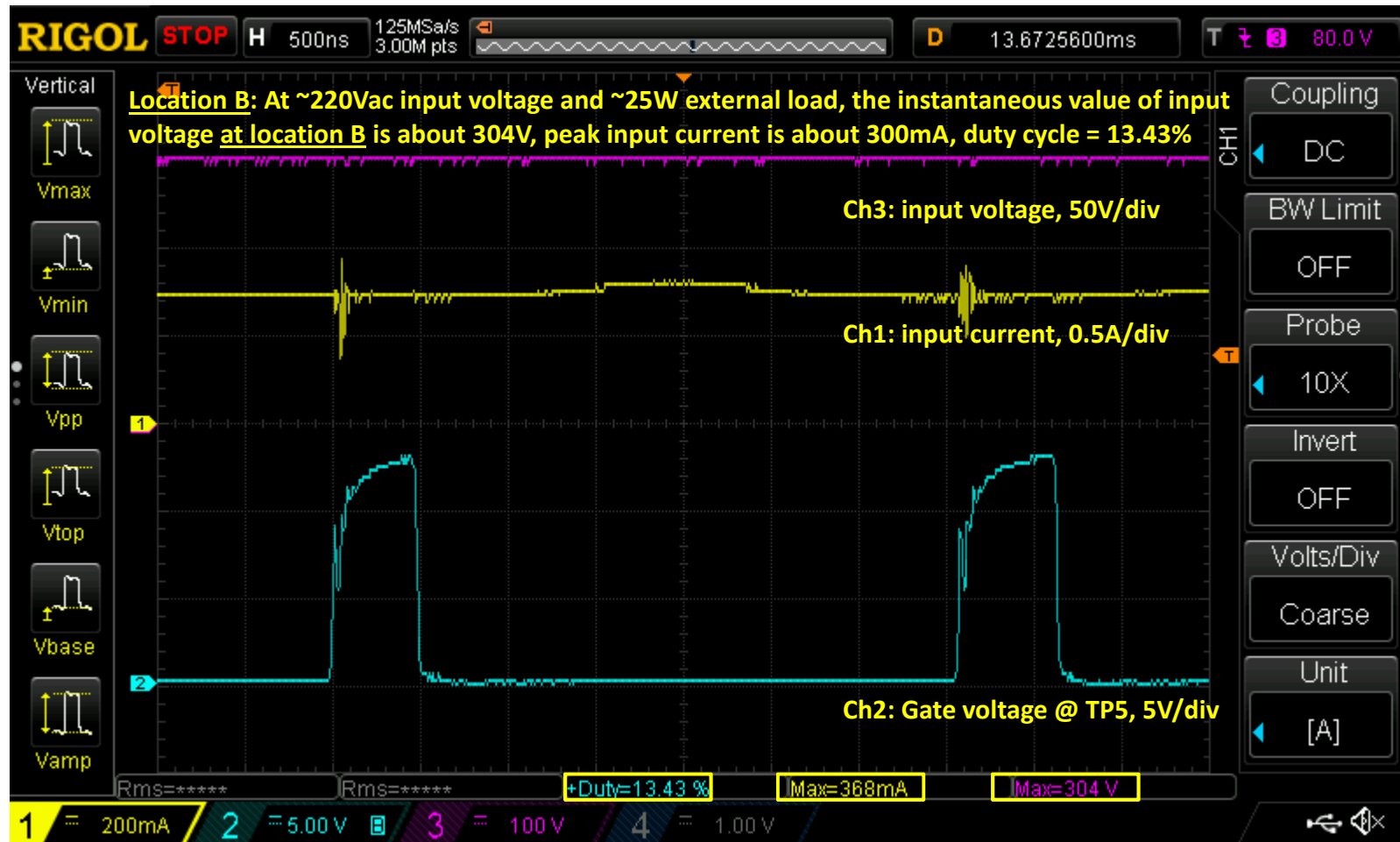
Measuring inductor current vs gate voltages, 12/18

Ch1: Current probe is Fluke 80i-110S, which has limited bandwidth and is used to measure 60Hz input current



Measuring inductor current vs gate voltages, 12/18

Ch1: Current probe is Fluke 80i-110S, which has limited bandwidth and is used to measure 60Hz input current

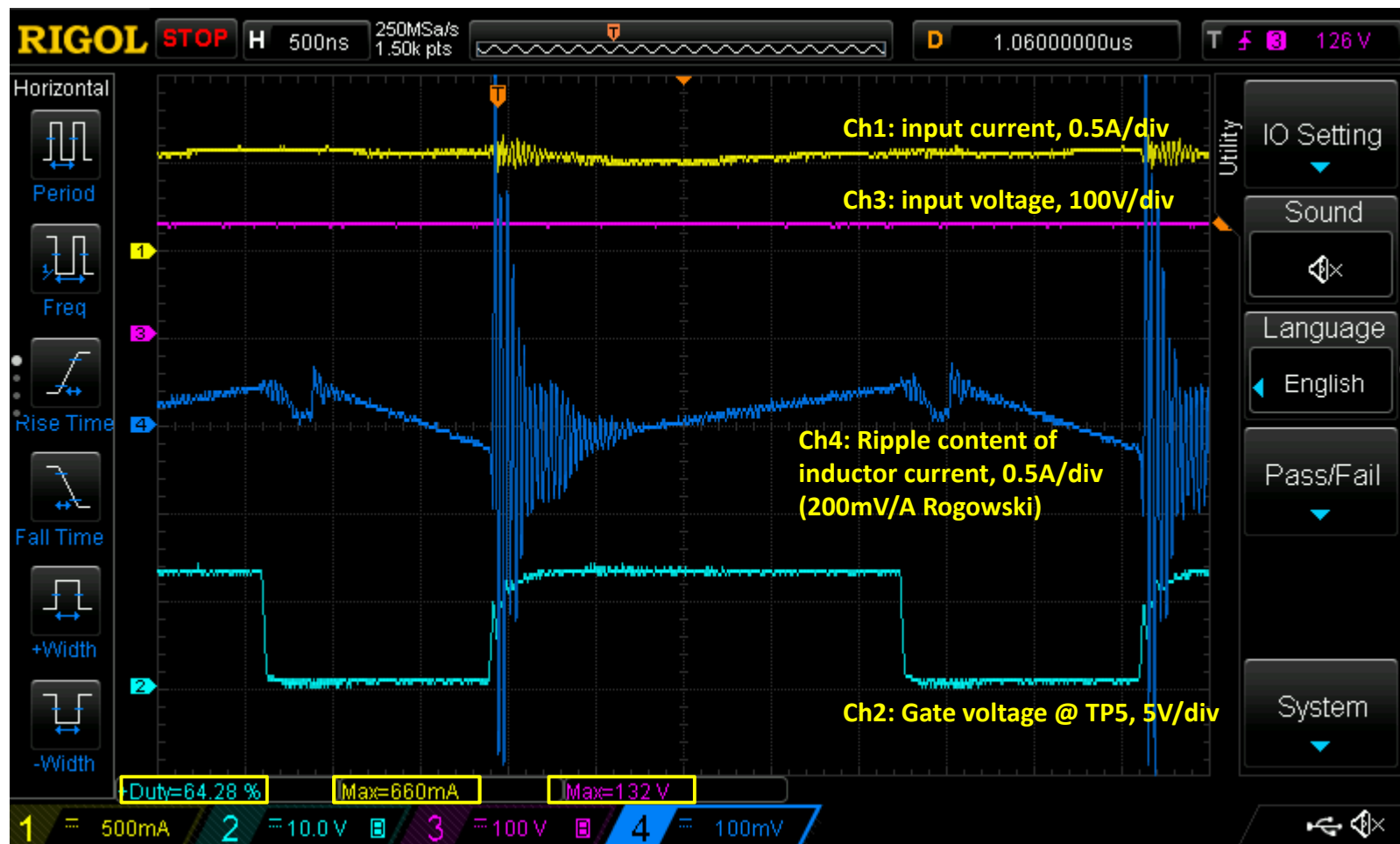


Measuring inductor current vs gate voltages, 12/18

Ch1: Current probe is Fluke 80i-110S, which has limited bandwidth and is used to measure 60Hz input current

Ch4: Current probe is Rogowski CWTUM-015-B, which can measure high frequency only.

At the peak of 85Vac input voltage and ~25W external load, the instantaneous value of input voltage is 132V, peak input current is 660mA, duty cycle = 64.28%

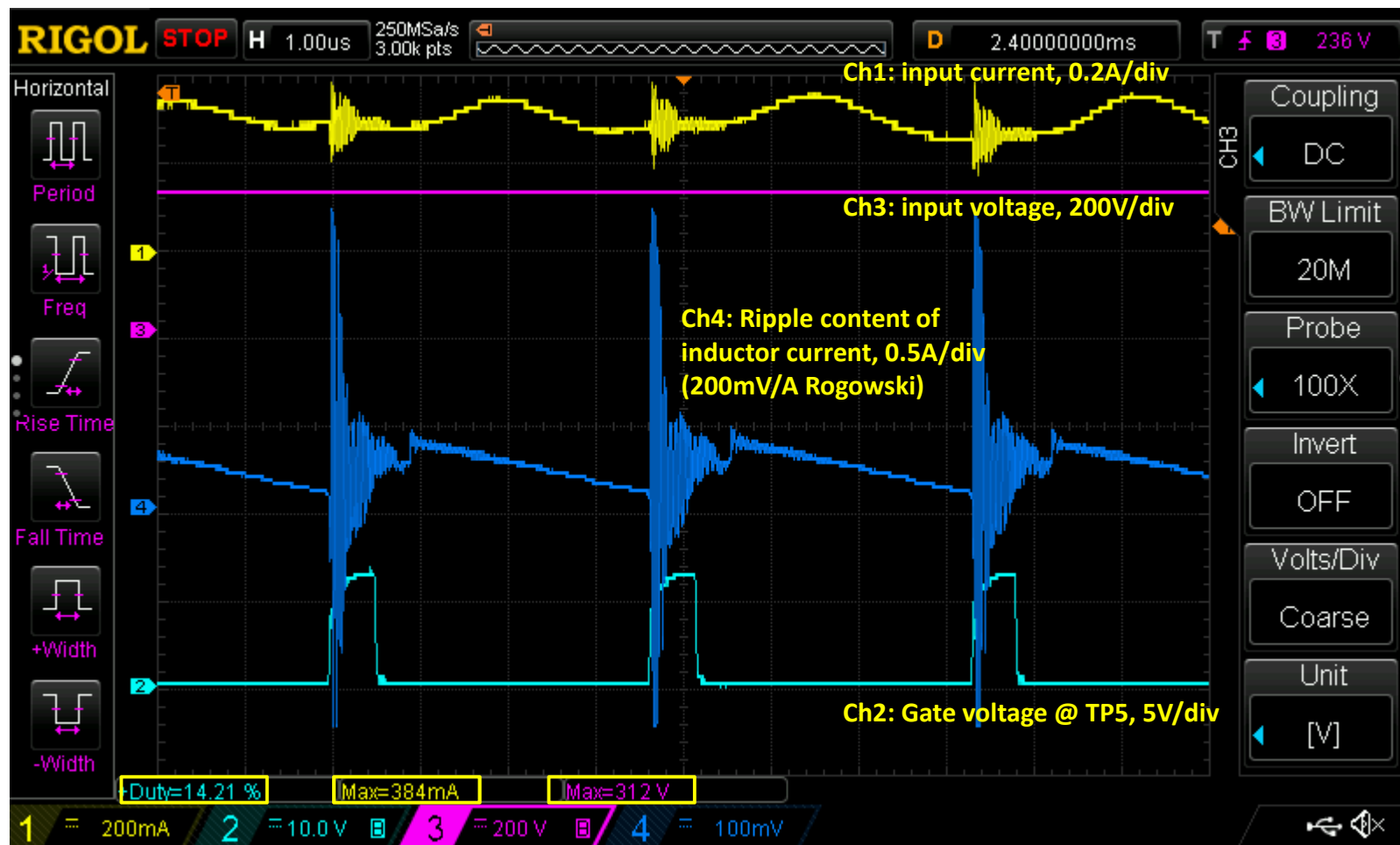


Measuring inductor current vs gate voltages, 12/18

Ch1: Current probe is Fluke 80i-110S, which has limited bandwidth and is used to measure 60Hz input current

Ch4: Current probe is Rogowski CWTUM-015-B, which can measure high frequency only.

At the peak of 220Vac input voltage and ~25W external load, the instantaneous value of input voltage is 312V, peak input current is ~300mA (location B), duty cycle is about 14.2%

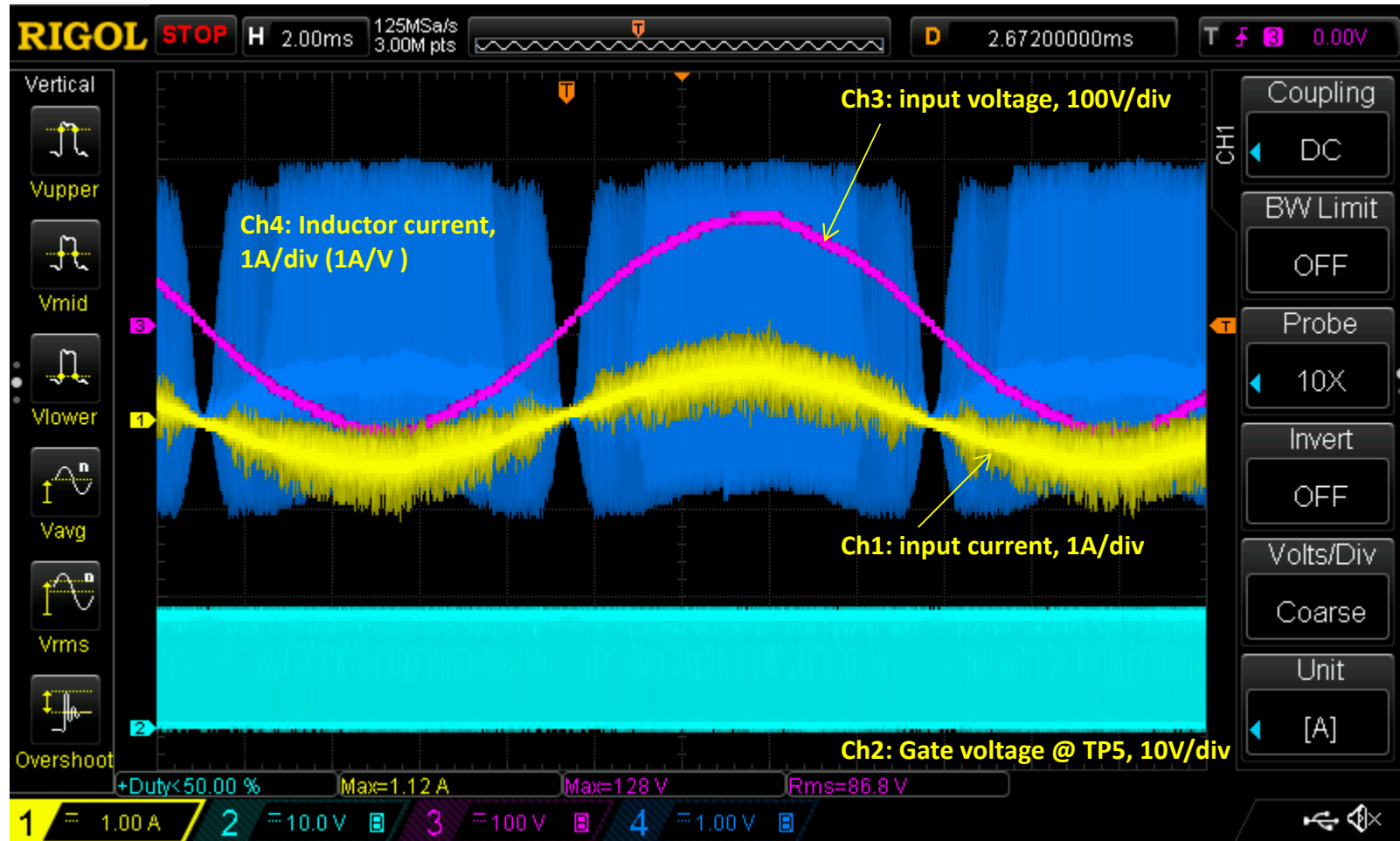


Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At 85Vac input voltage and ~25W external load, no major distortions observed

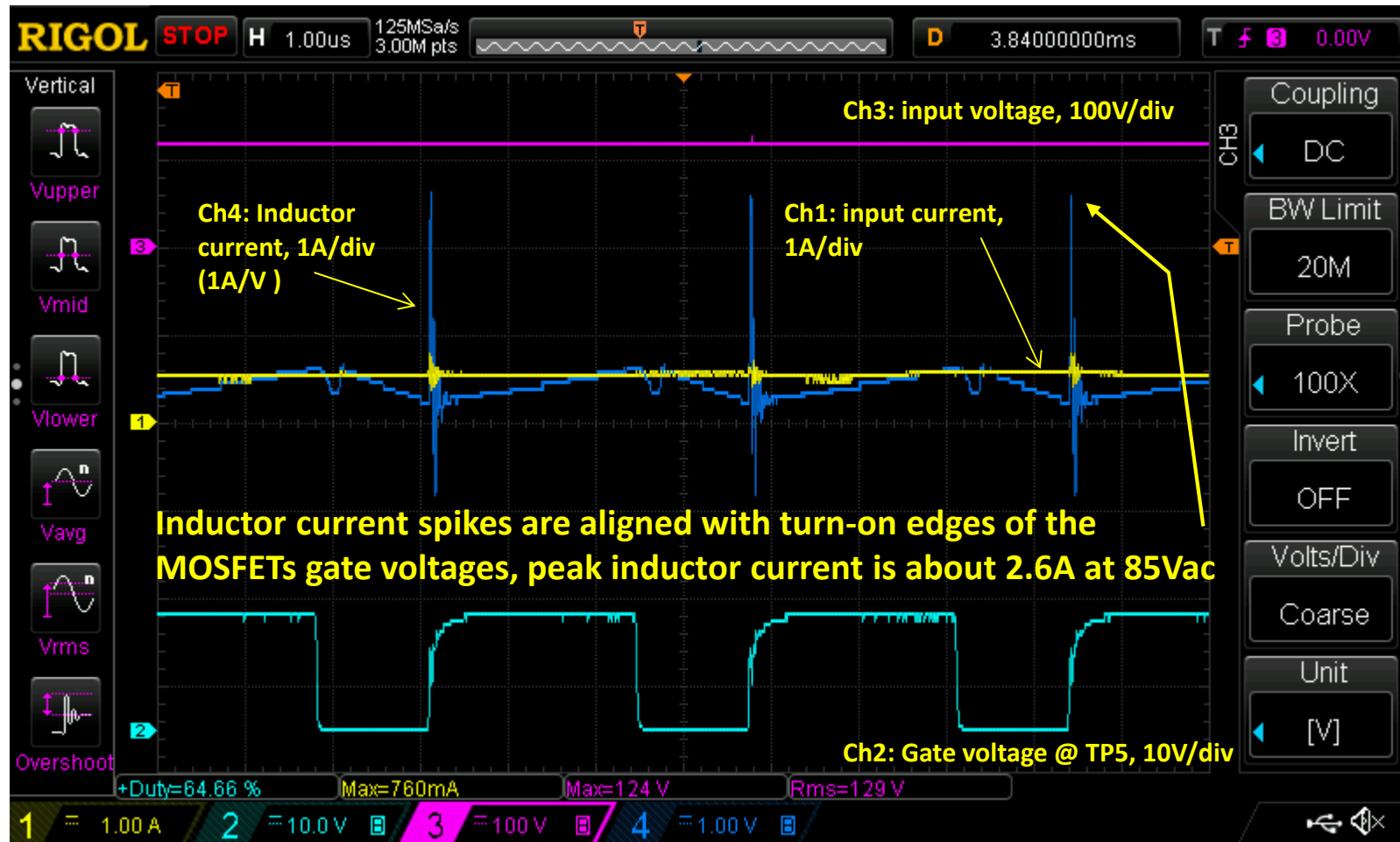


Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At 85Vac input voltage and ~25W external load, no major distortions observed

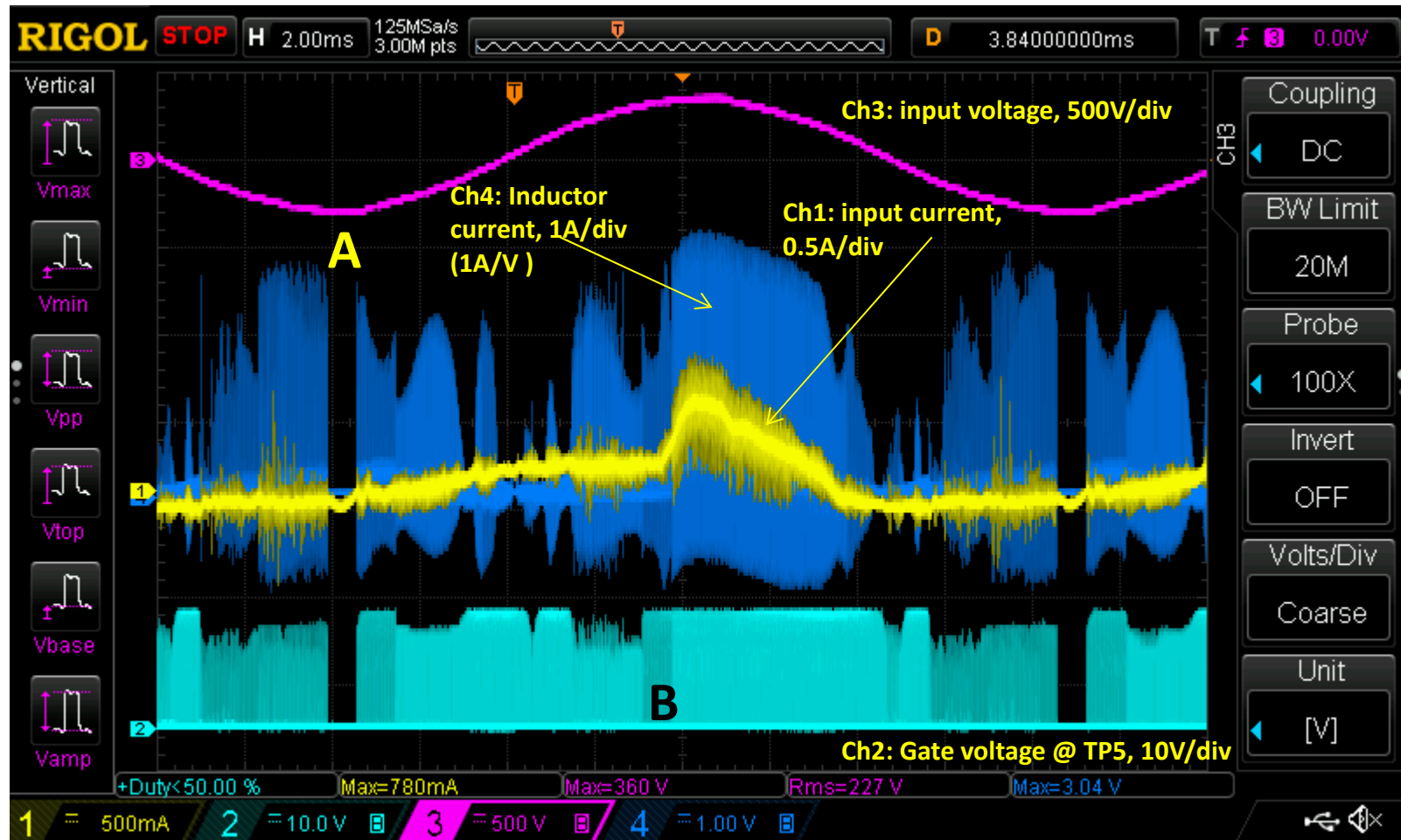


Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed. See next slides for location A, B details

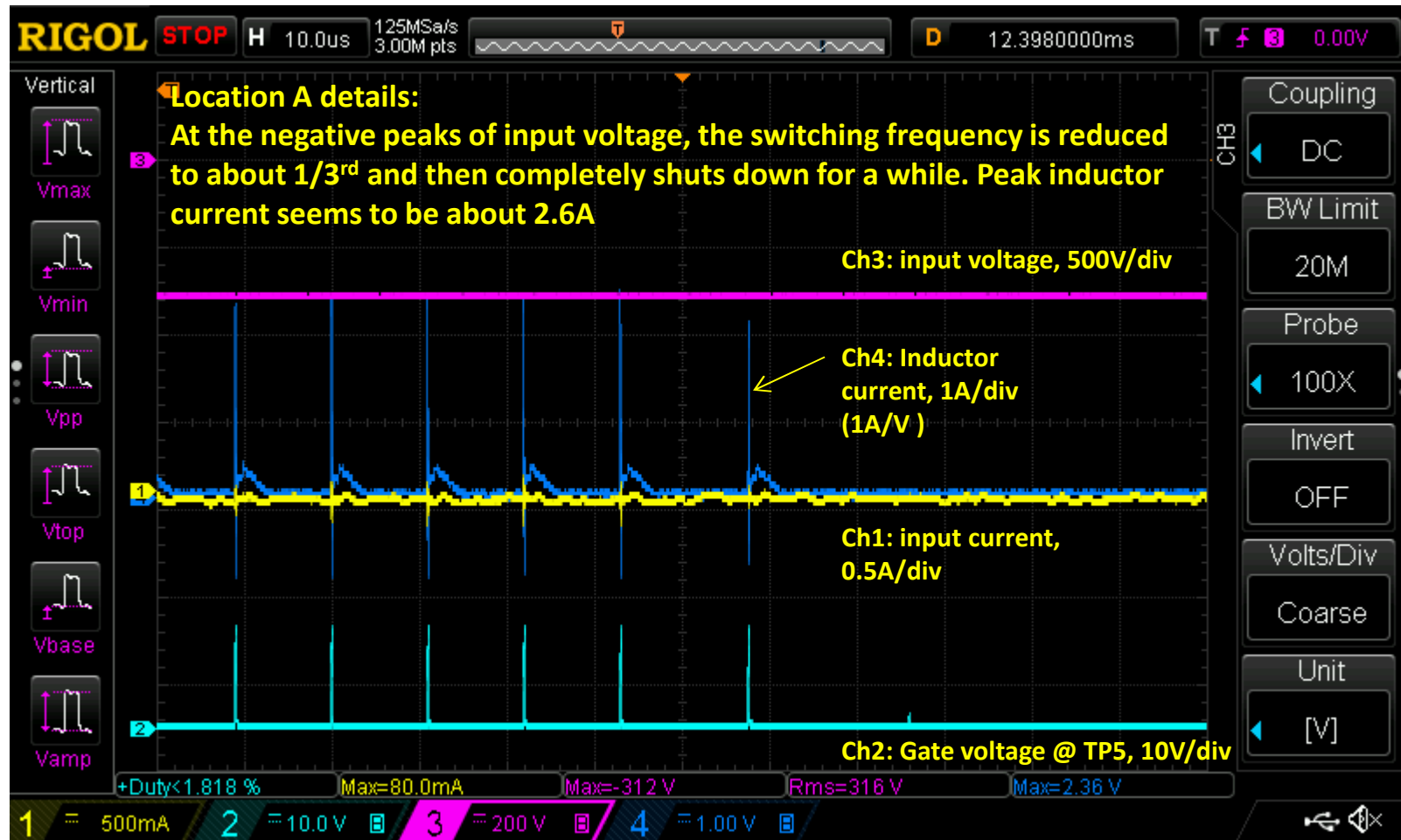


Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.



Measuring inductor current vs gate voltages, 12/25

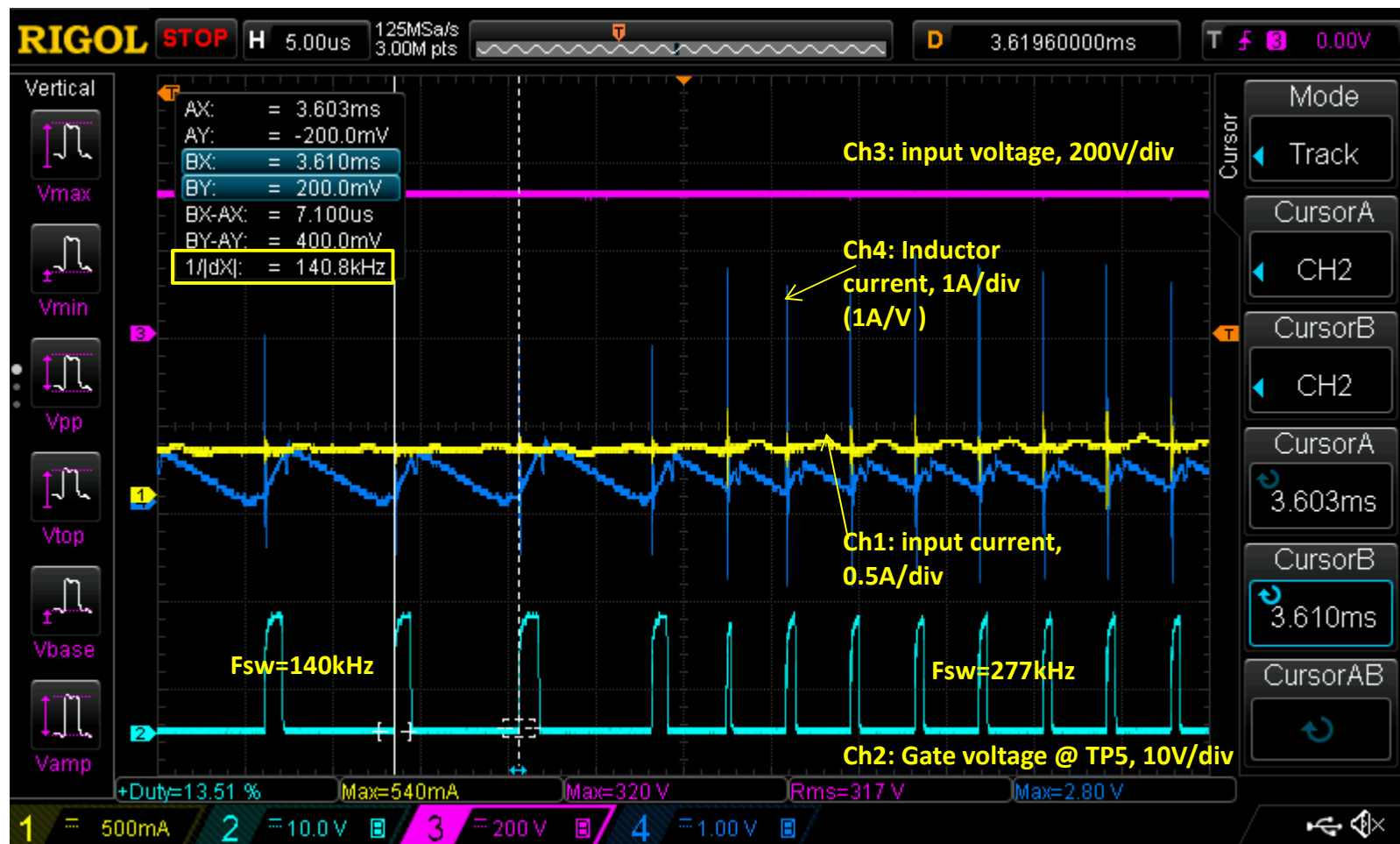
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Location B details:

At the positive peaks of input voltage, the switching frequency is reduced to about $\frac{1}{2}$ and this results in “flat top” on the current waveform. After that the switching frequency is raised and around that time the current surge is observed



Measuring inductor current vs gate voltages, 12/25

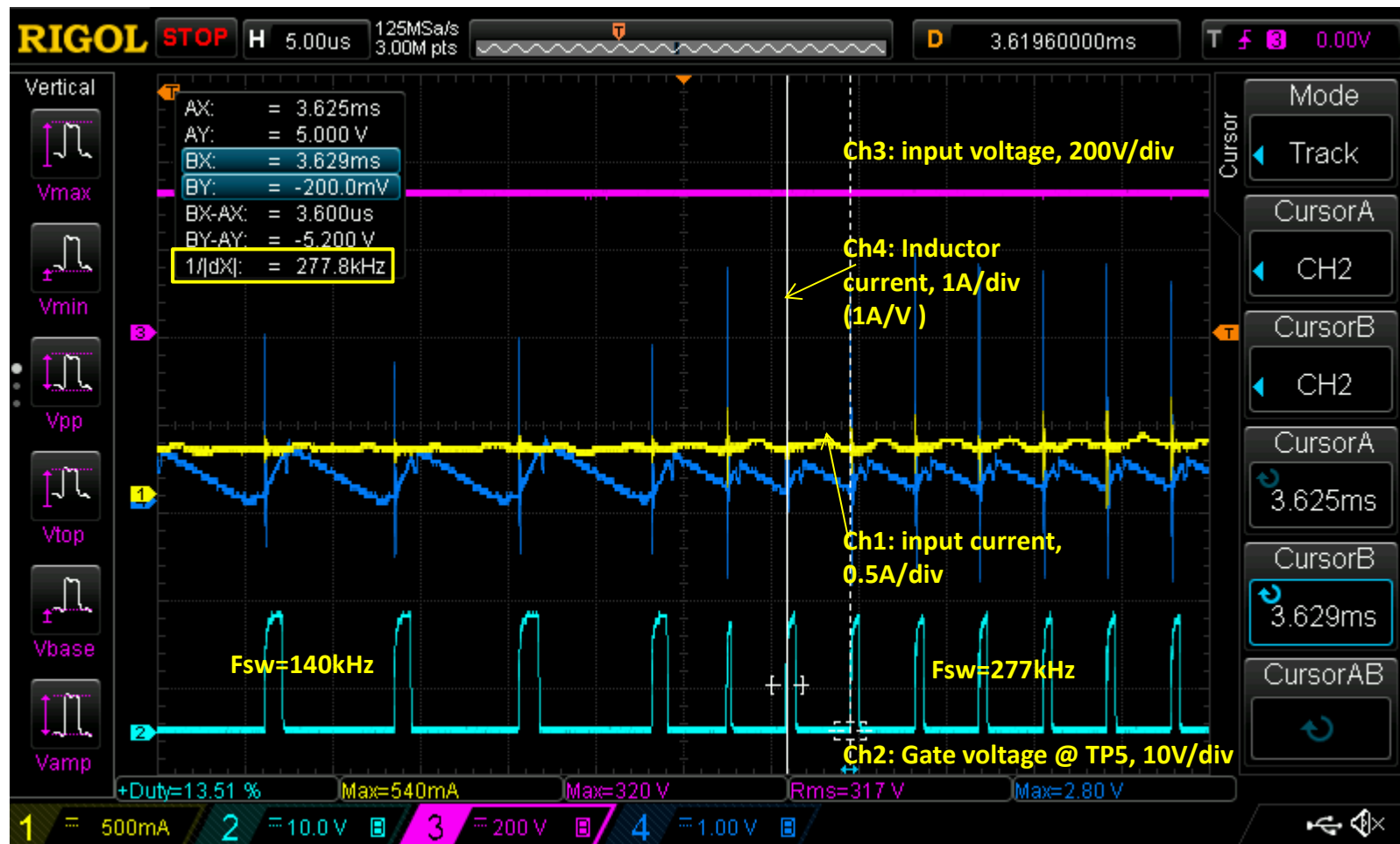
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Location B details:

At the positive peaks of input voltage, the switching frequency is reduced to about $\frac{1}{2}$ and this results in “flat top” on the current waveform. After that the switching frequency is raised and around that time the current surge is observed



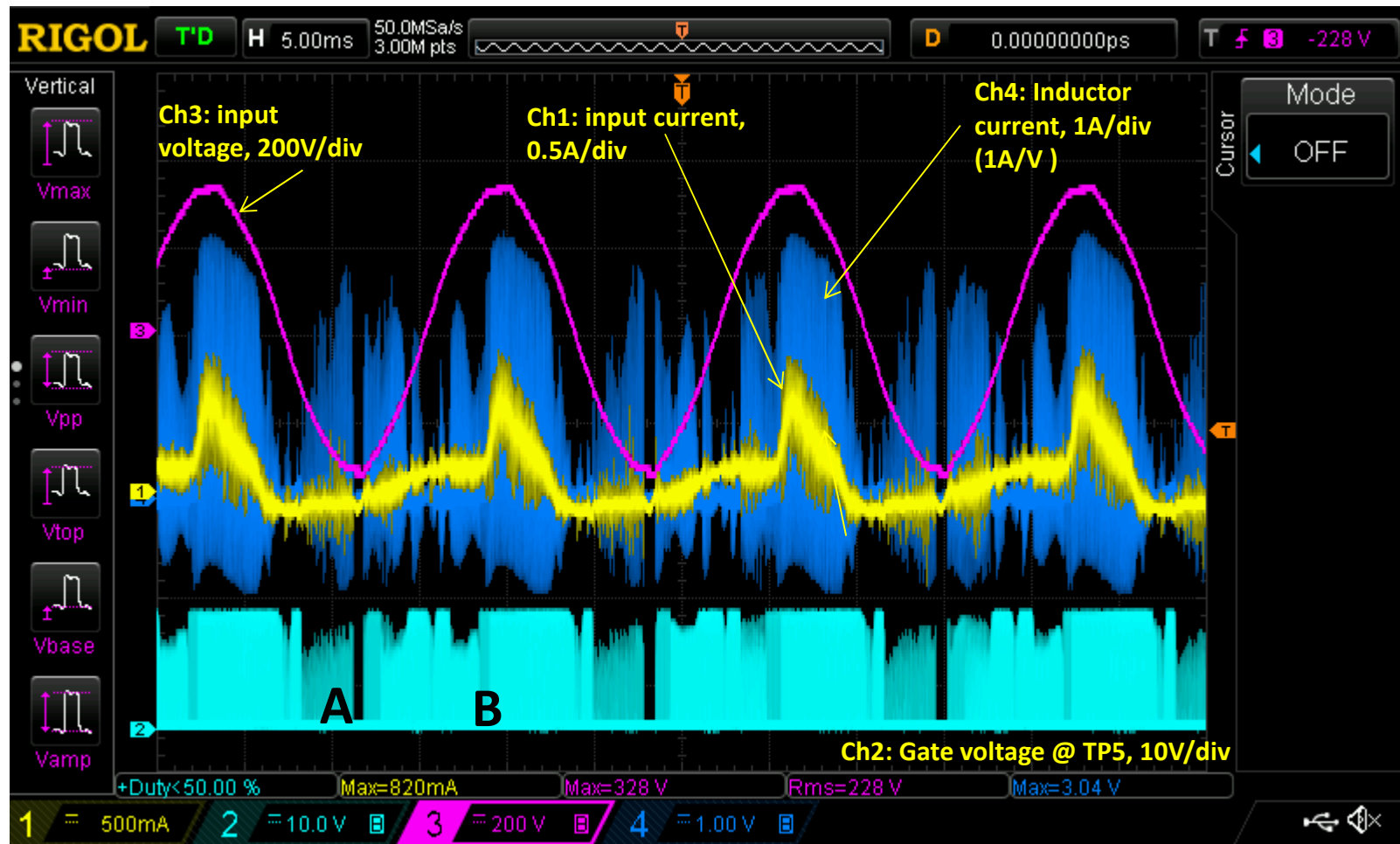
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

At 230Vac input, 25W external load, the view of locations A (negative peak) and location B (positive peak)



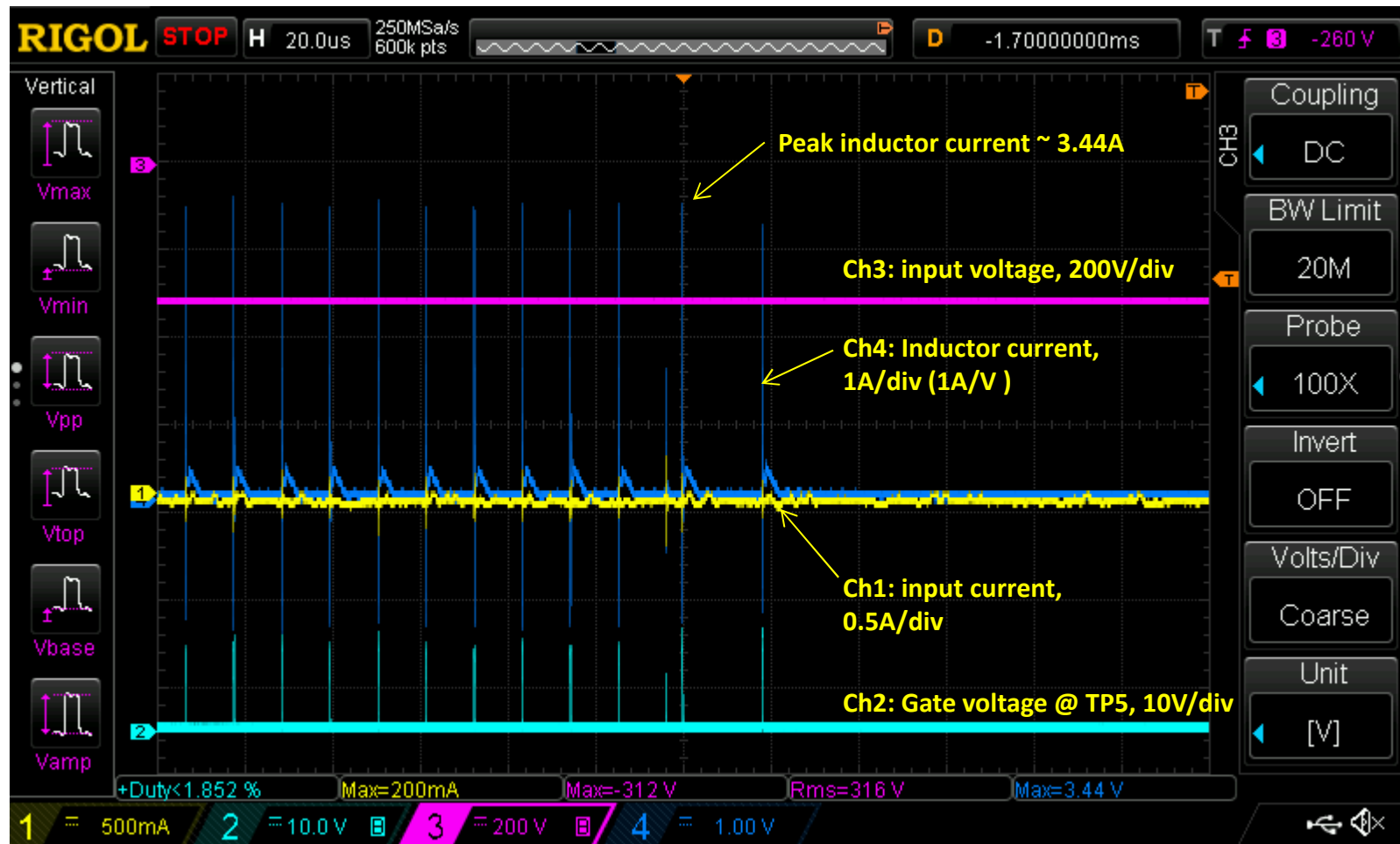
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Location A: Current probe on Ch1 has low BW and is used to measure input current only.



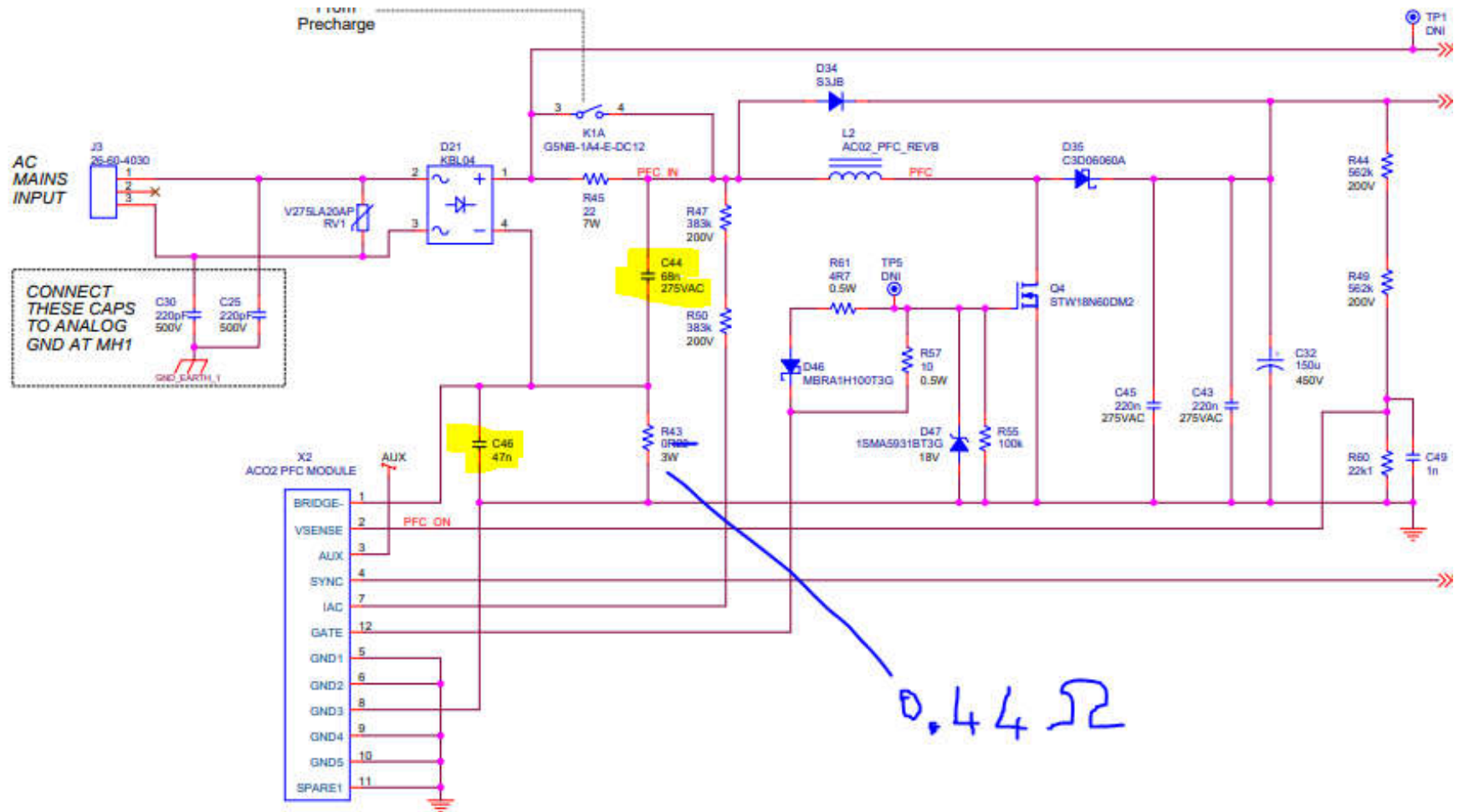
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

There is a 68nF cap after the rectifier. It is increased to $2 \times 68 = 136\text{nF}$, did not observe any improvements



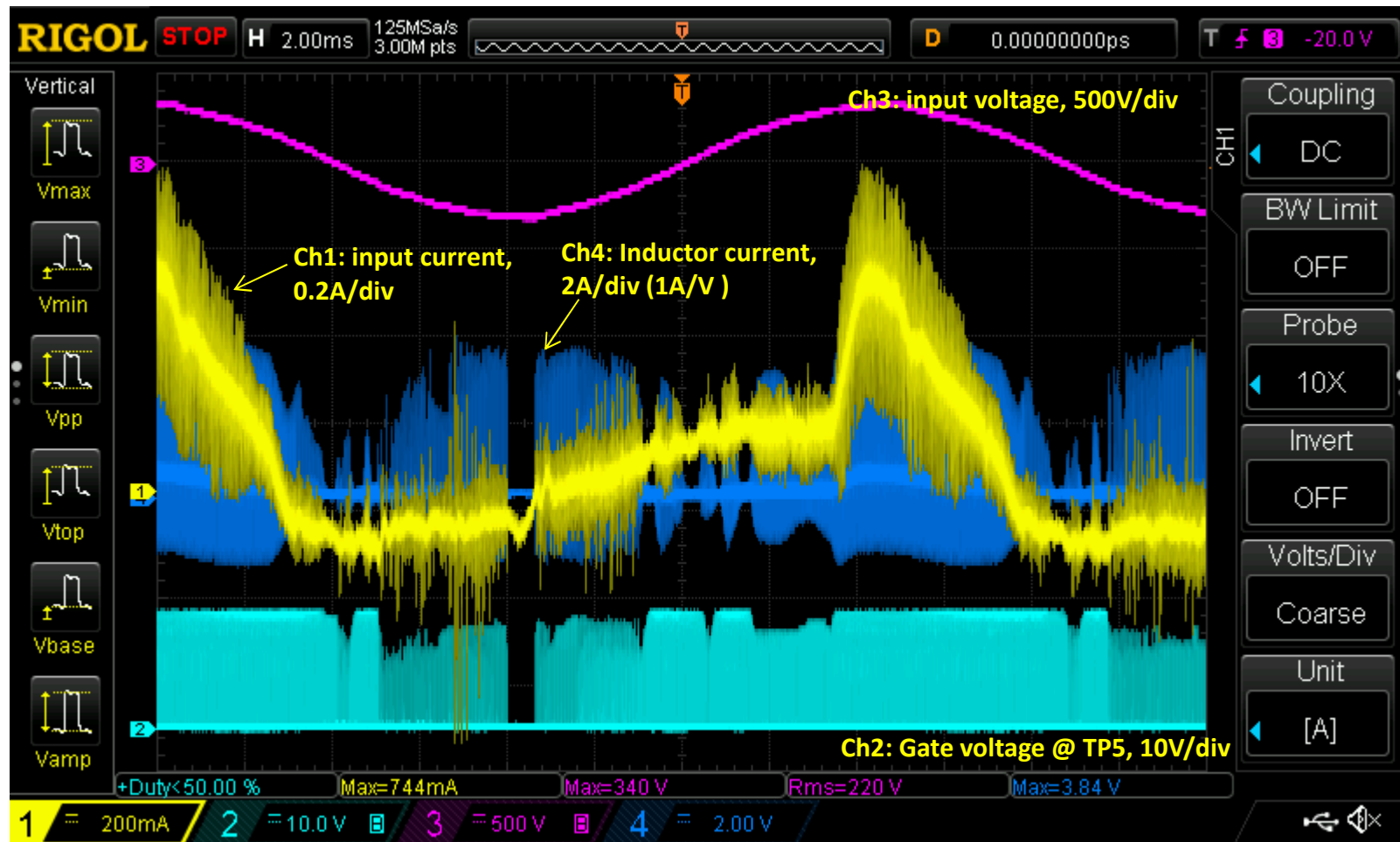
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

There is a 68nF cap after the rectifier. It is increased to $2 \times 68 = 136\text{nF}$, did not observe any improvements



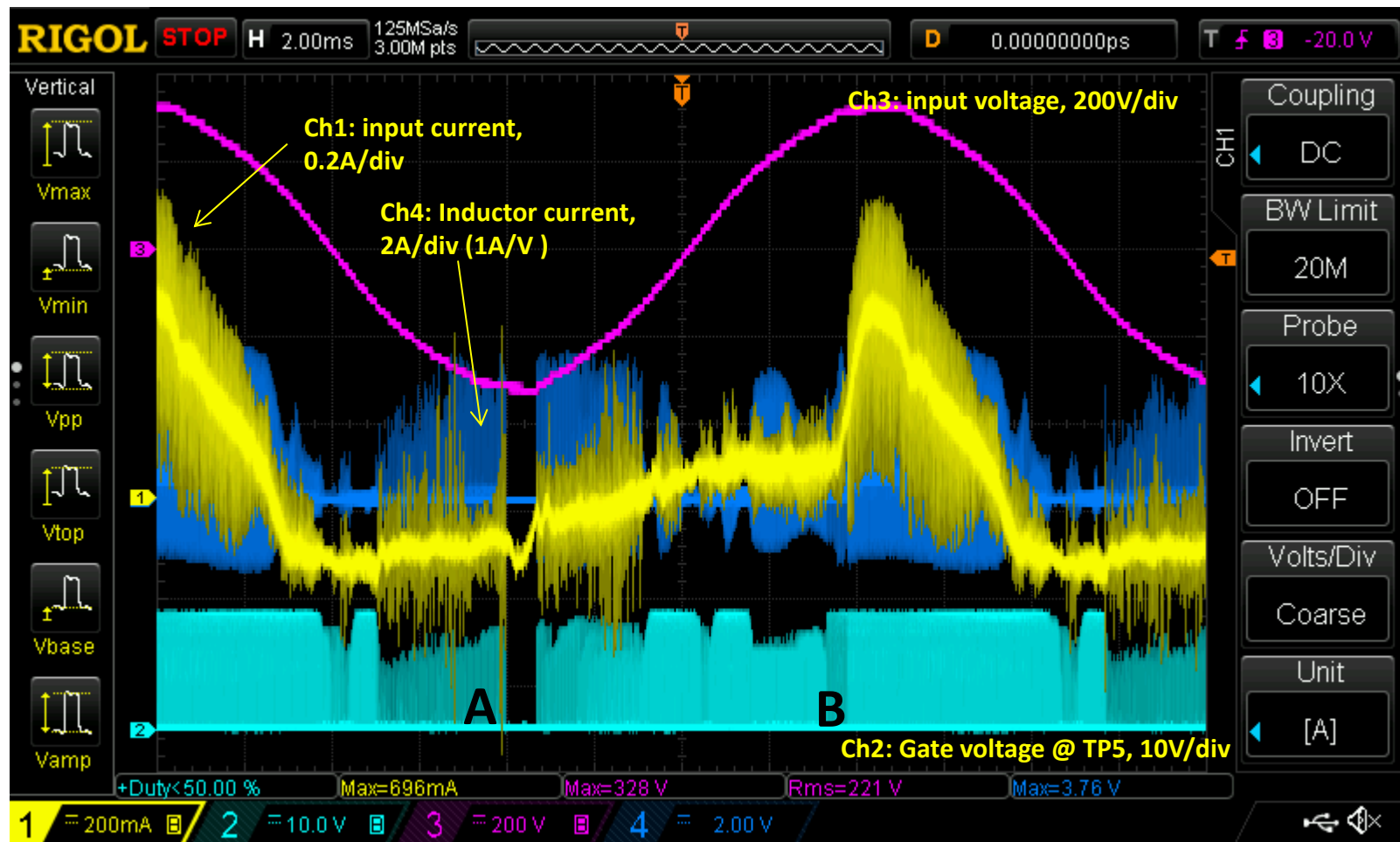
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Added 100pF across the R5 (R7 on data sheet), did not observe any improvements



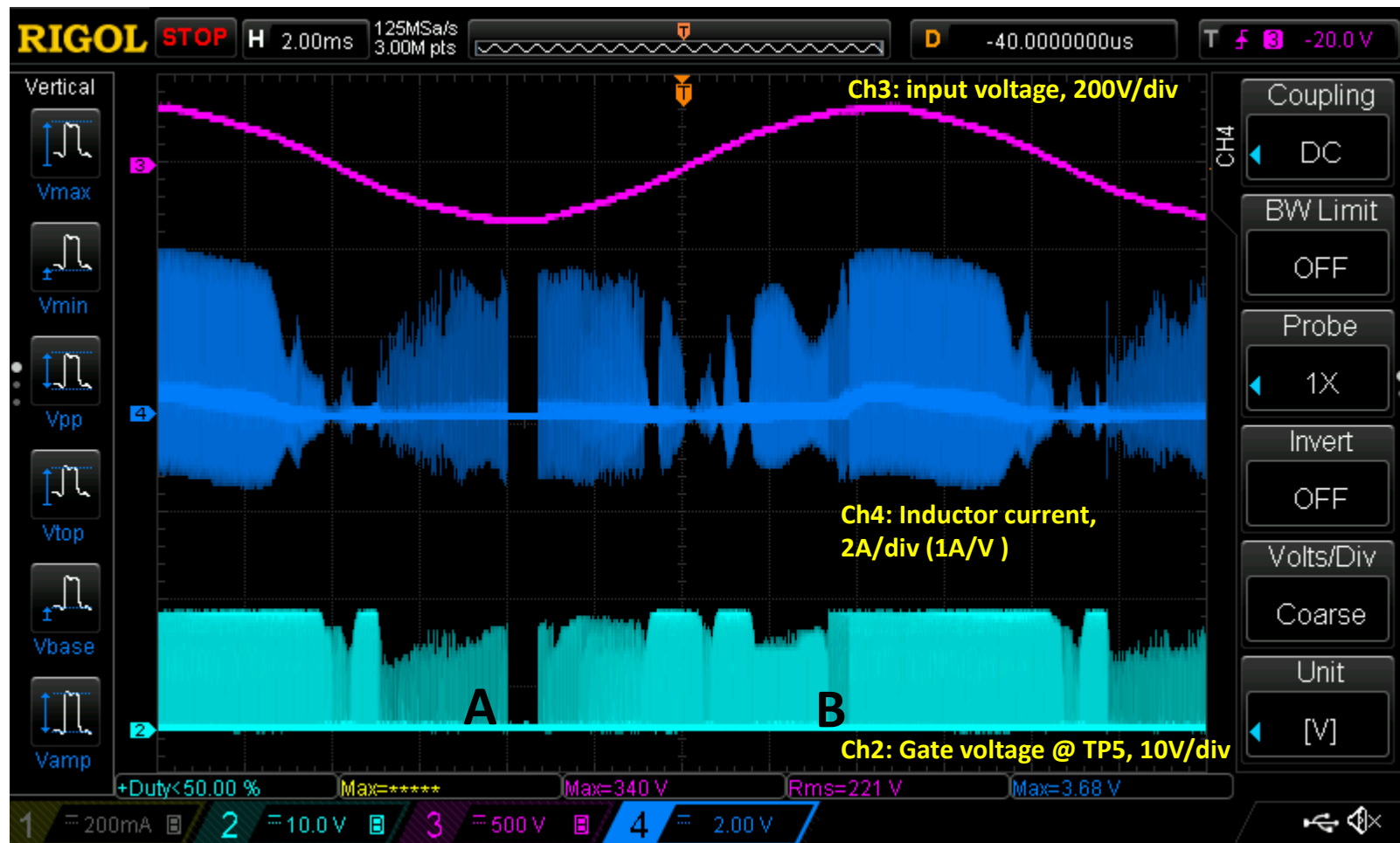
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Added 100pF across the R5 (R7 on data sheet), did not observe any improvements



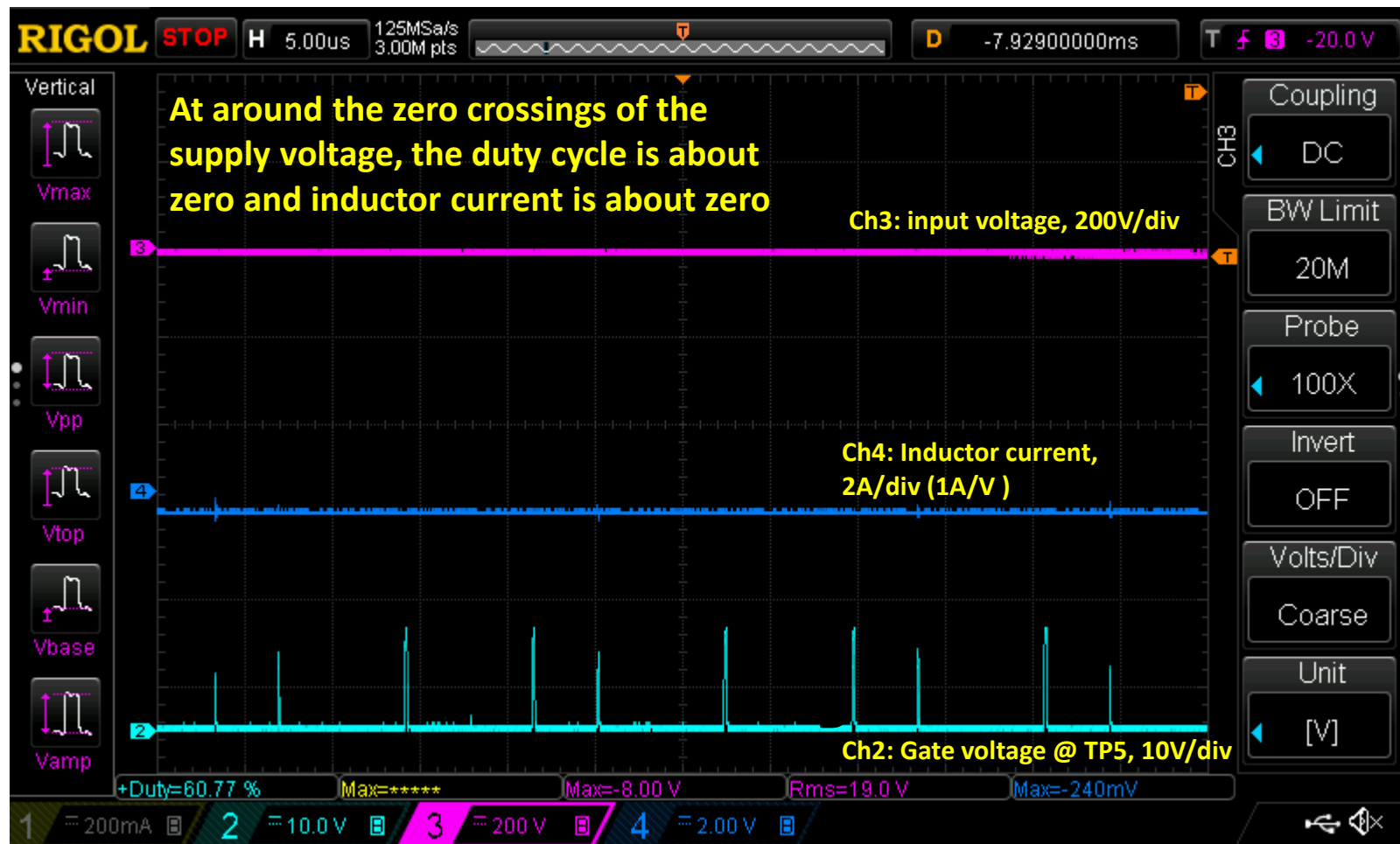
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Added 100pF across the R5 (R7 on data sheet), did not observe any improvements



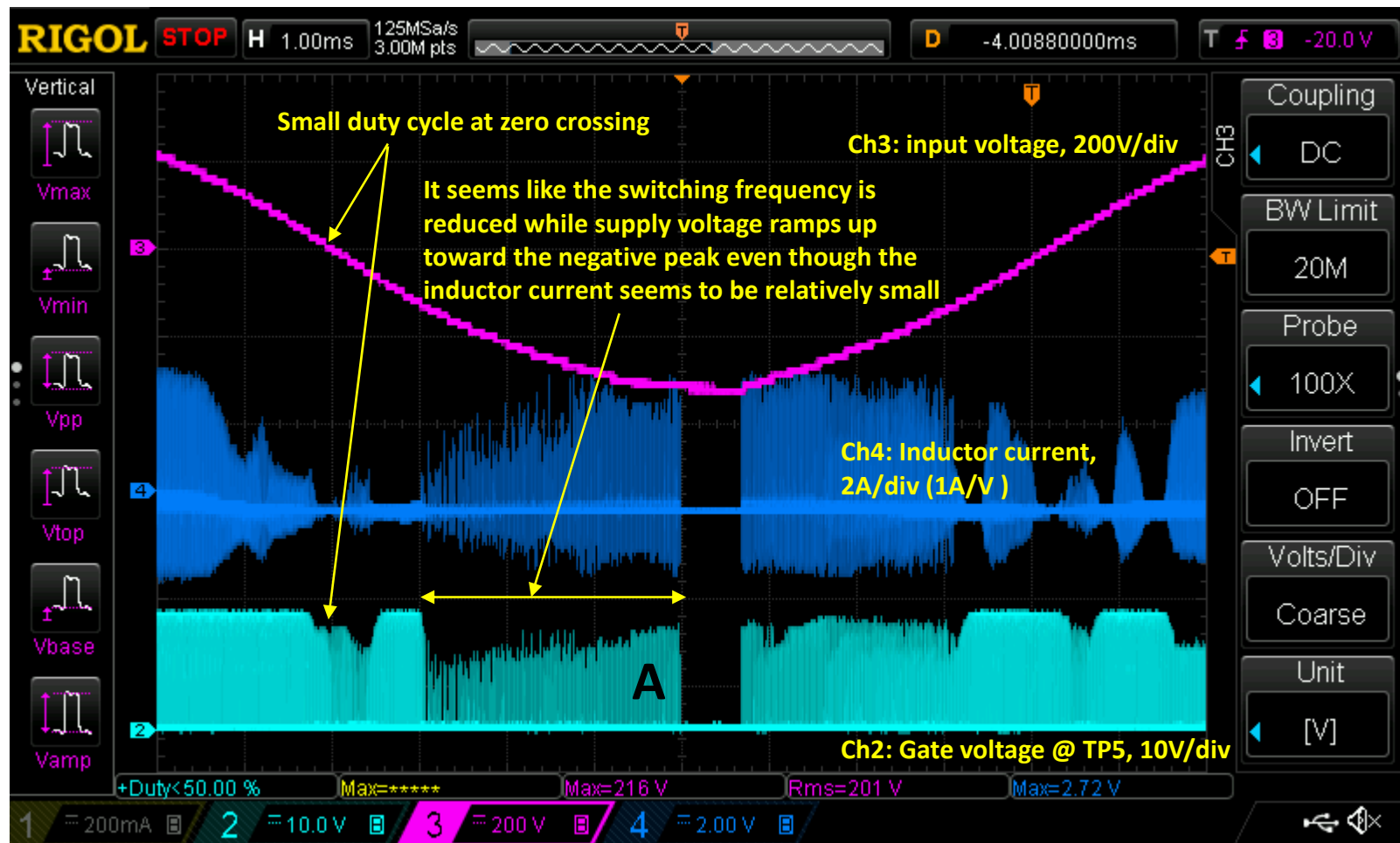
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Added 100pF across the R5 (R7 on data sheet), did not observe any improvements



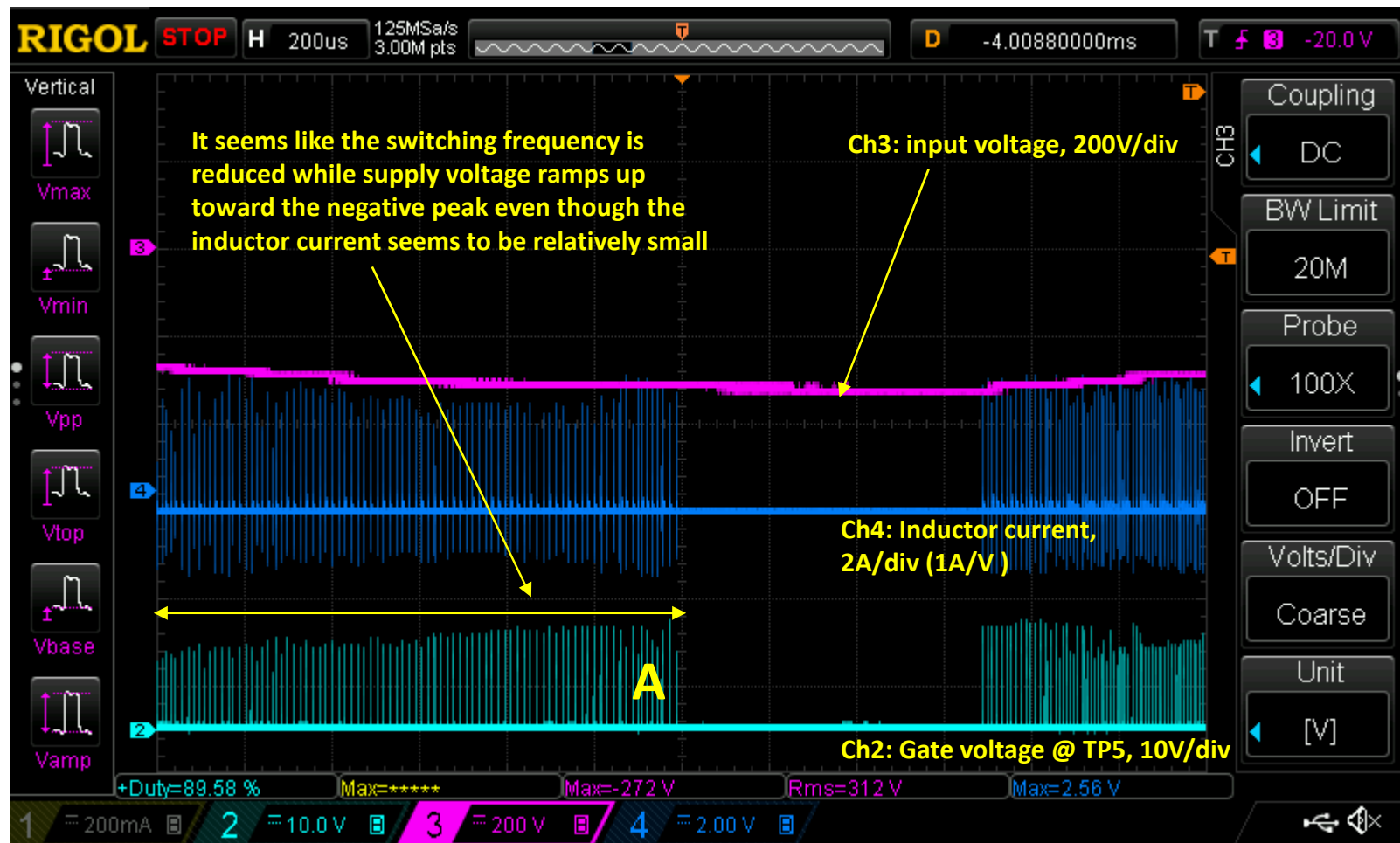
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Added 100pF across the R5 (R7 on data sheet), did not observe any improvements



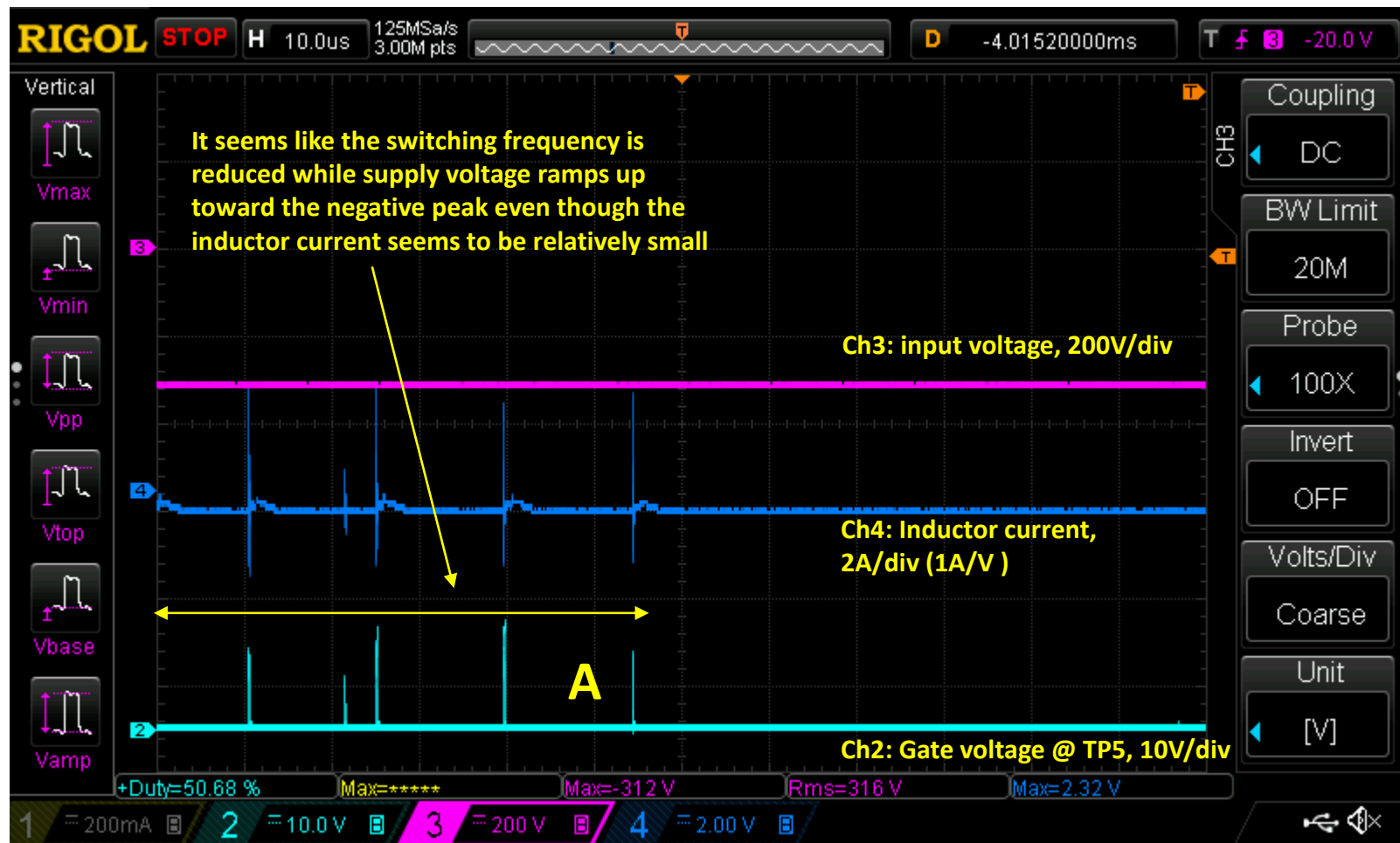
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Added 100pF across the R5 (R7 on data sheet), did not observe any improvements



Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

At ~230Vac input voltage and ~25W external load, distortions observed.

Observations:

- 1) Location A: While the supply voltage ramps up toward the negative peak, the switching frequency is reduced significantly and then MOSFET shuts down for a while at the negative peak. It seems like the inductor current is relatively small and this may not be current limit initiated turn off.
- 2) Location B: While the supply voltage ramps up toward the positive peak, the switching frequency is reduced by about 50%. It is observed this corresponds to “flat top” on supply current. At around the peak of supply voltage, the switching frequency suddenly snaps back to normal frequency and this corresponds to surge in peak of the supply voltage. It is not clear if this phenomenon is caused by activation of current limit.
- 3) Adding an other 68nF cap after rectifier did not improve the issue
- 4) Adding 100nF cap across R5 (R7 on data sheet) did not improve the issue

Measuring inductor current vs gate voltages, 12/25

PCB	RefDes on datasheet	RefDes in design	12/11/2022 Fci=44kHz, Gci=5.6	12/25/2022 Fci=11kHz, Gci=1.6
PSB-04	R1	R47, R50	383K	383K
	R2	R43	0.44	0.44
	L1	L2	660uH	660uH
PFC-03	R8, R12	R4, R9	2.74K	2.74K
	R13	R10	15.8K	4.7K
	C6	C6	233pF (200p//33p)	3.3n
	C7	C8	33pF	150pF
	R15	R11	30.1K	30.1K
	C8	C9	4.7uF	4.7uF
	C10	C1	1.5uF	1.5uF
	C11	C2	150nF	150nF
	R21	R1	48.7K	48.7K
	R14	R13	10K	10K
	R7	R5	1.50K (3.3K//2.74K) +100pF	1.50K (3.3K//2.74K) + 100pF
	C9	C4	1uF	1uF

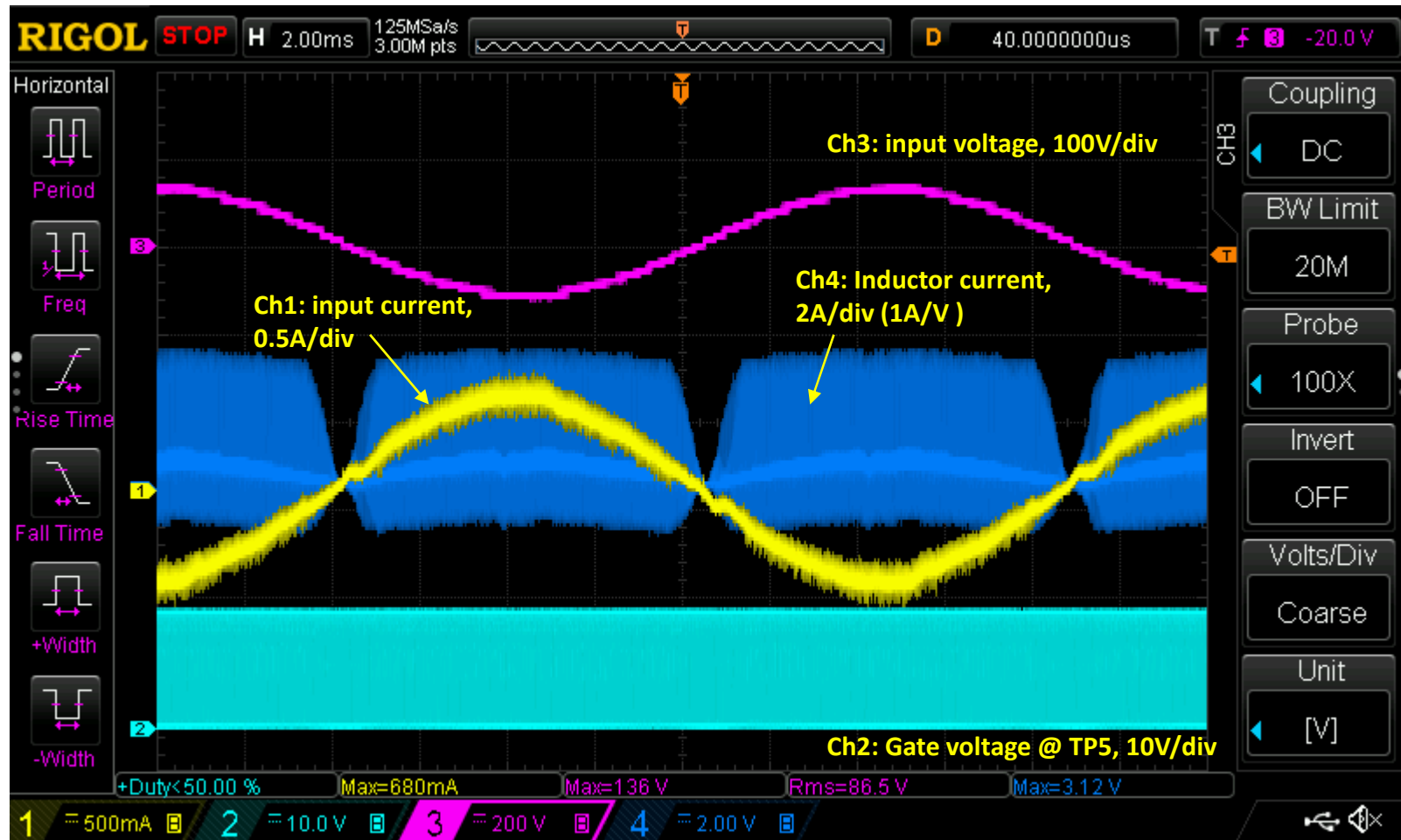
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~85Vac input voltage and ~25W external load, no major distortions observed



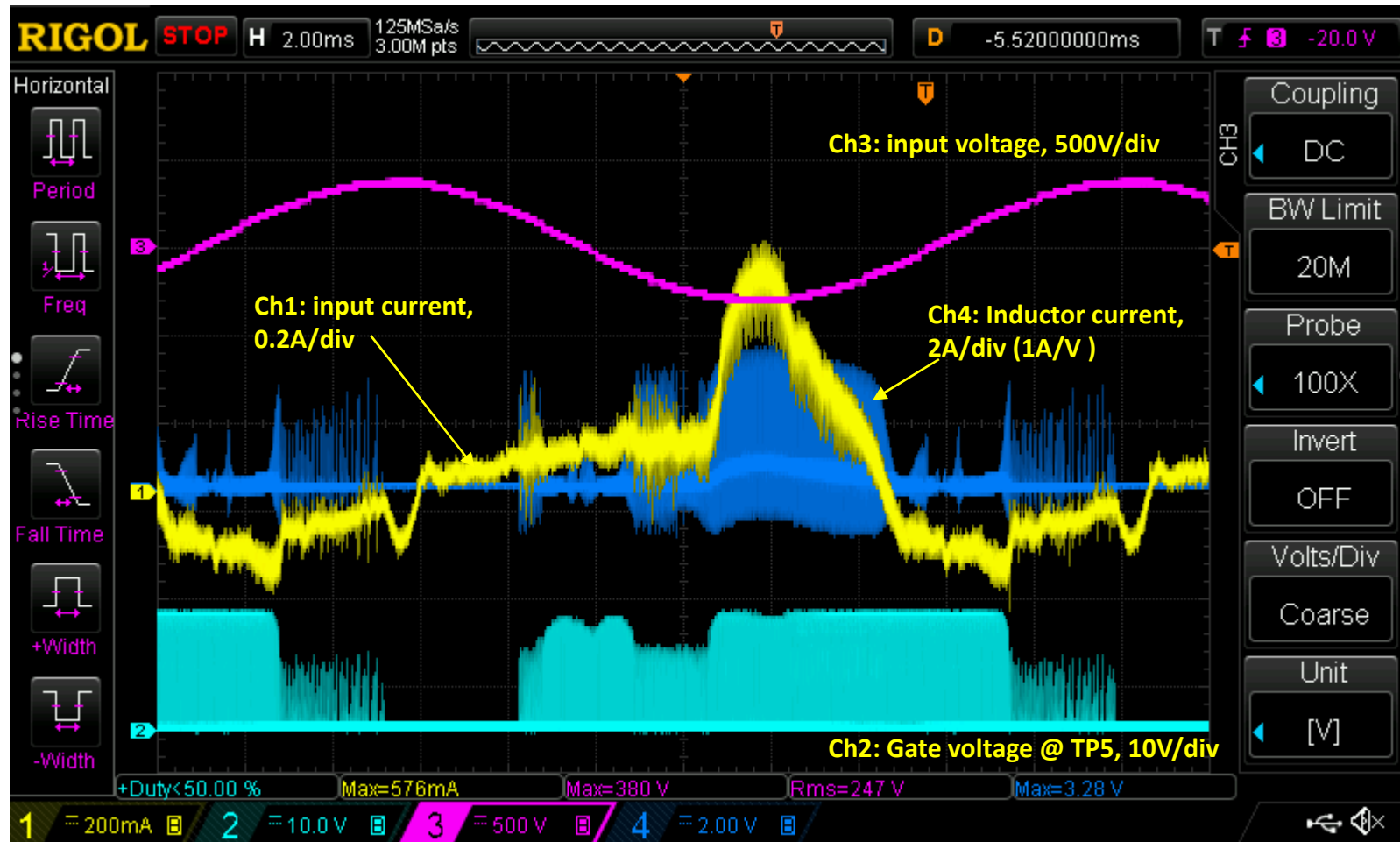
Measuring inductor current vs gate voltages, 12/25

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~230Vac input voltage and ~25W external load, major distortions observed



Measuring inductor current vs gate voltages, 12/28

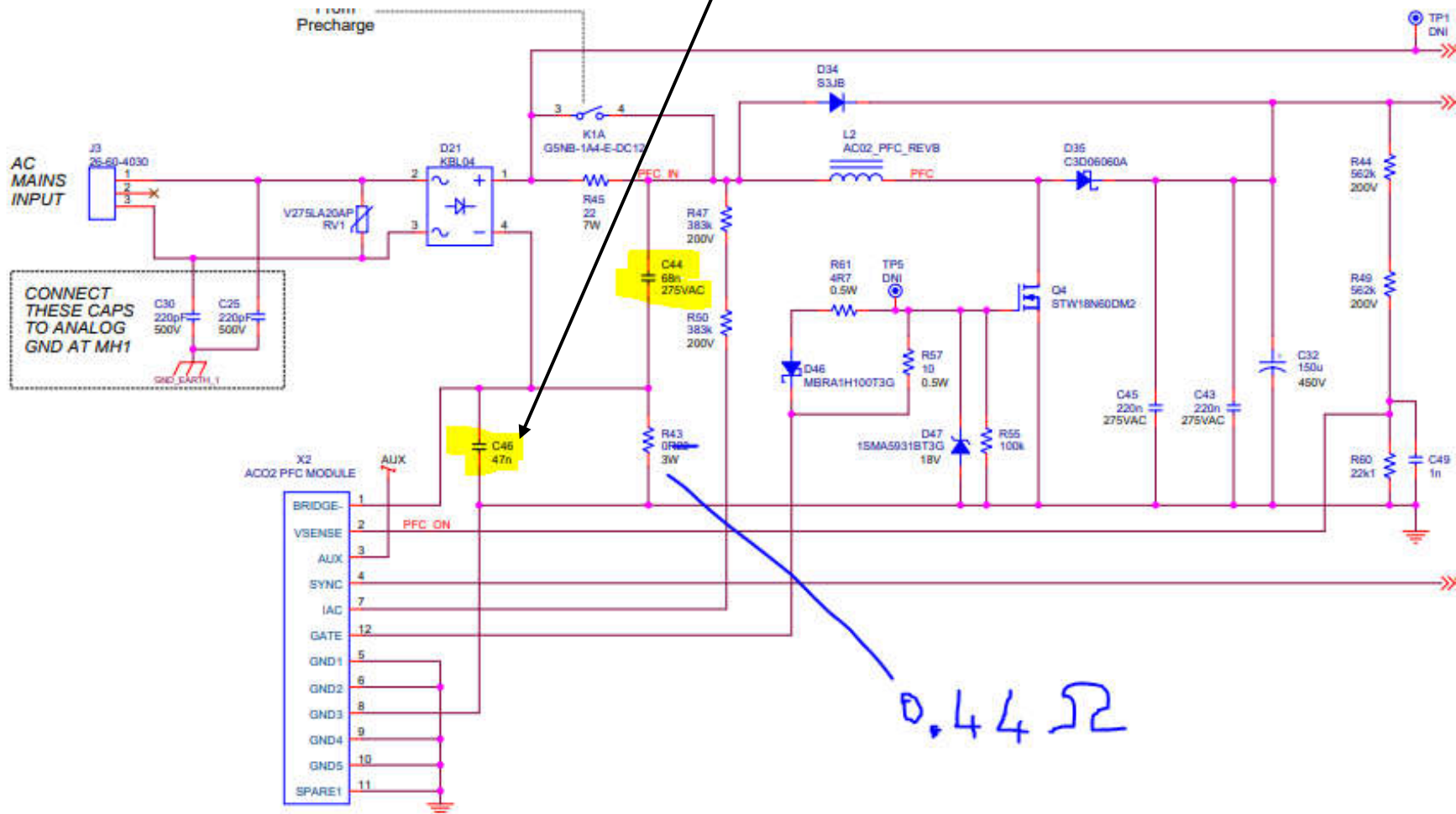
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~230Vac input voltage and ~25W external load, major distortions observed

Investigating the effect of C46 on input current distortions



Measuring inductor current vs gate voltages, 12/28

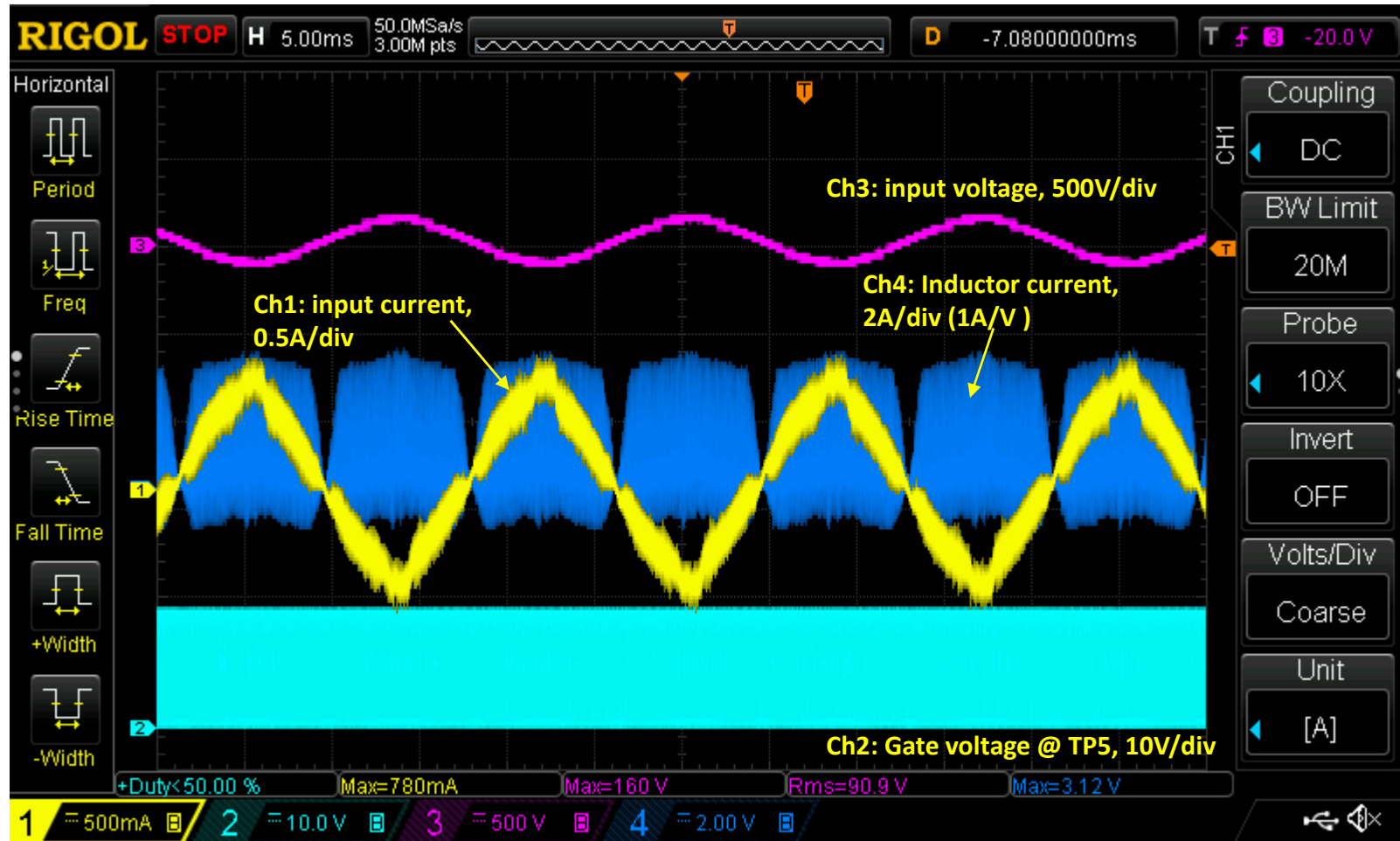
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At 90Vac input voltage, no C46, no major distortions observed

Ch4: Tek TCPA300, Ch1: Fluke 80i-110S



Measuring inductor current vs gate voltages, 12/28

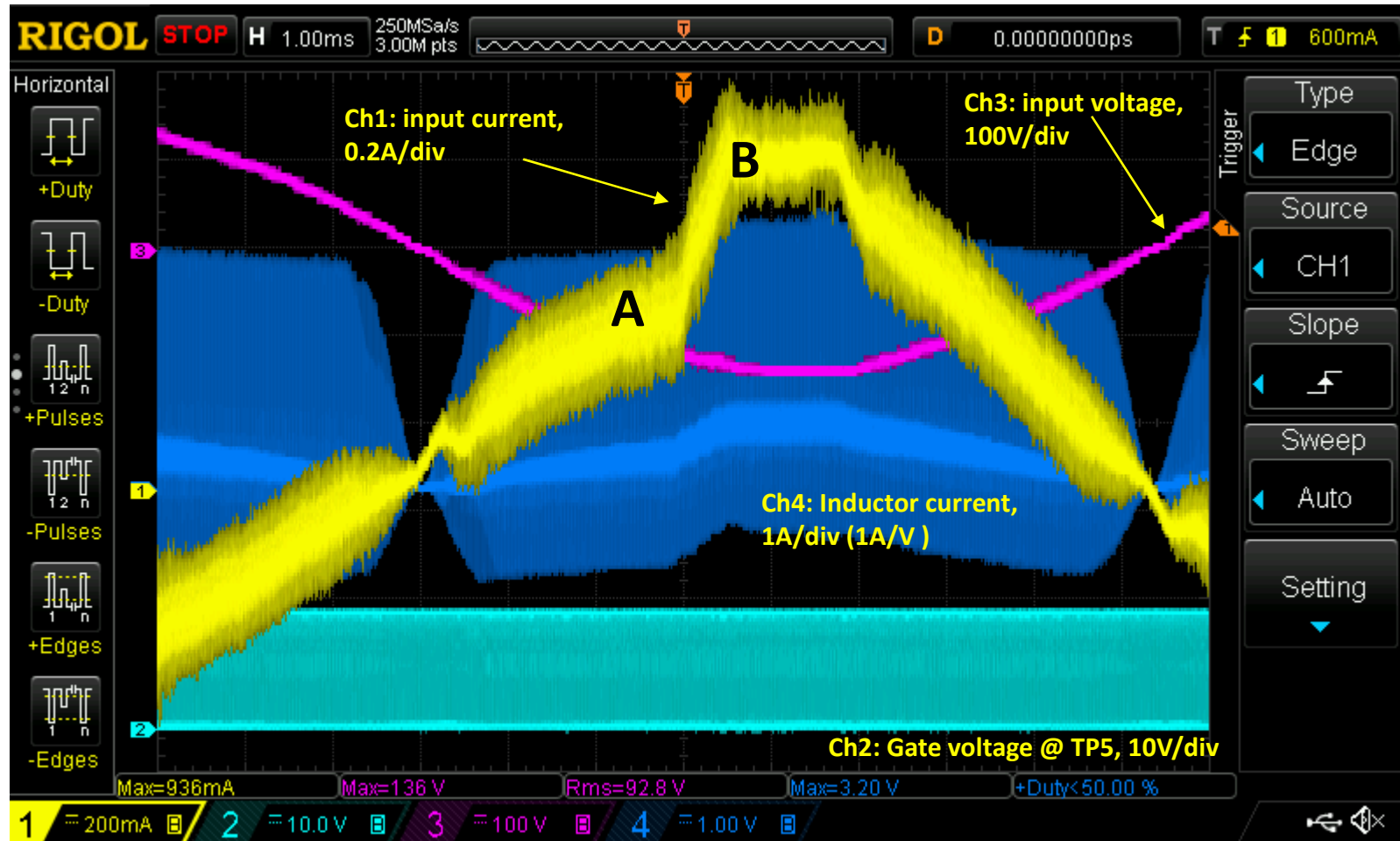
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~100Vac input voltage, no C46, distortions started, notice the current surge from location A to B

Ch4: Tek TCPA300, Ch1: Fluke 80i-110S



Measuring inductor current vs gate voltages, 12/28

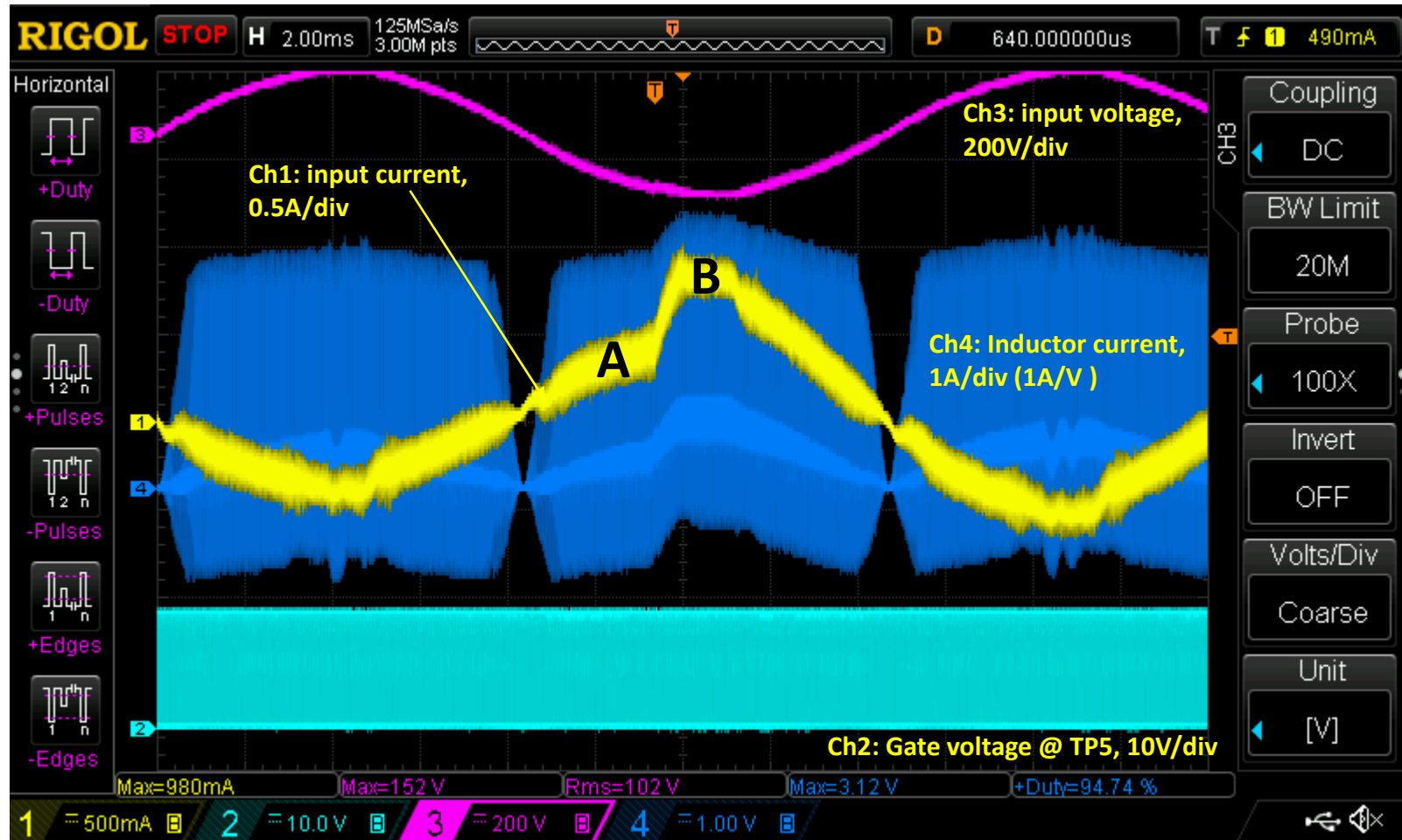
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~100Vac input voltage, no C46, distortions started

Ch4: Tek TCPA300, Ch1: Fluke 80i-110S



Measuring inductor current vs gate voltages, 12/28

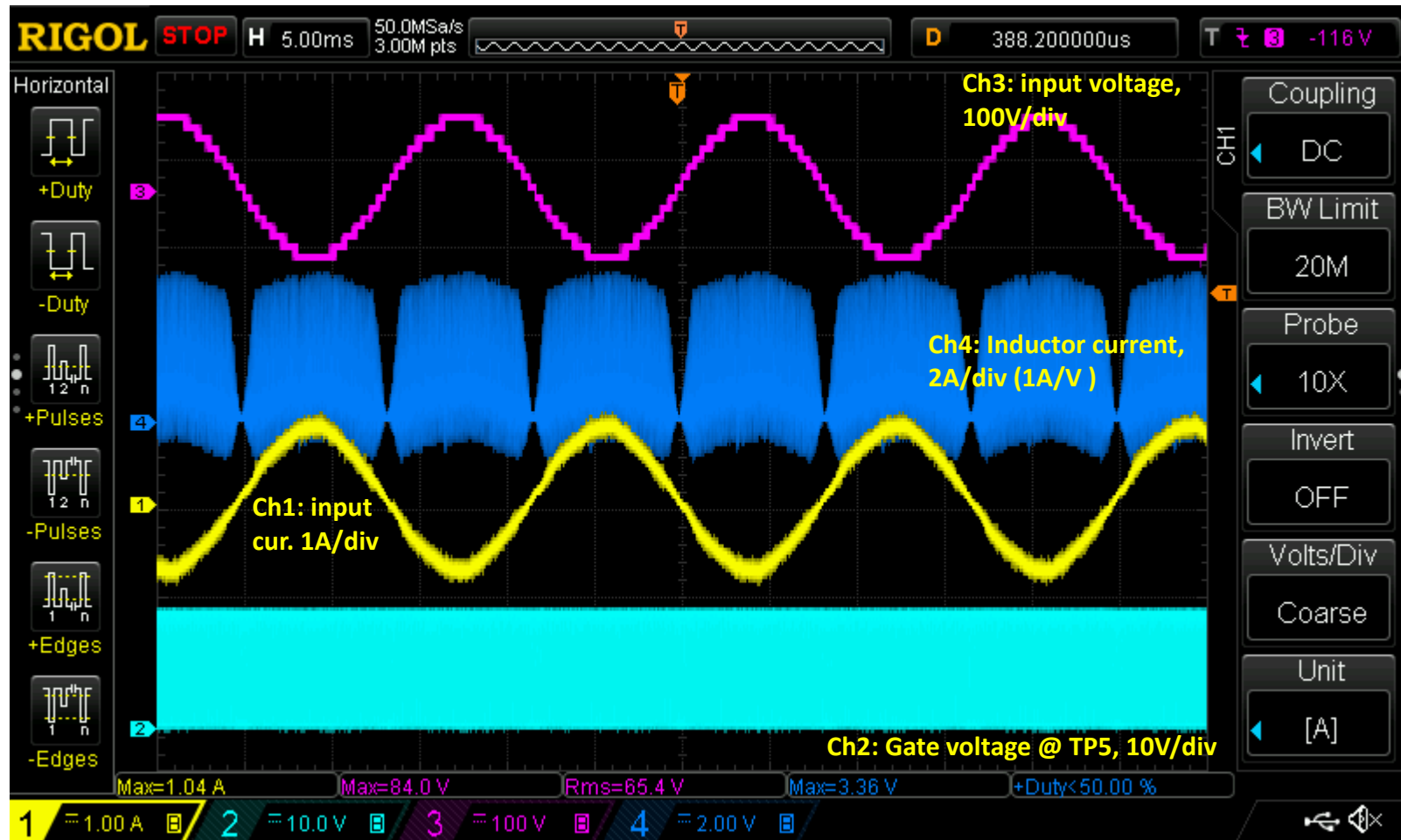
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~85Vac input voltage, $C_{46}=100\text{nF}$, no major distortions

Ch4: Tek TCPA300, Ch1: Fluke 80i-110S



Measuring inductor current vs gate voltages, 12/28

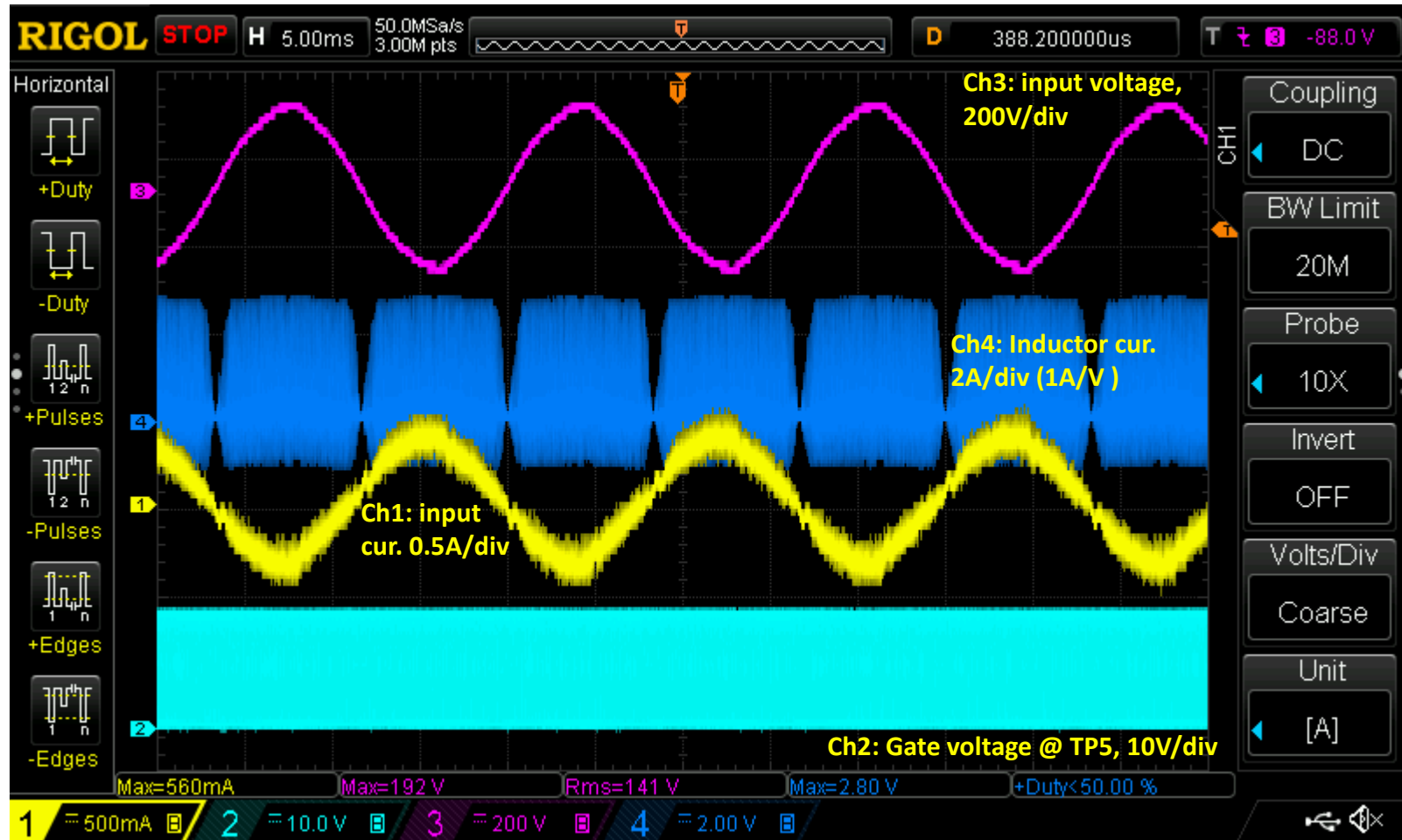
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~140Vac input voltage, $C_{46}=100\text{nF}$, no major distortions

Ch4: Tek TCPA300, Ch1: Fluke 80i-110S



Measuring inductor current vs gate voltages, 12/28

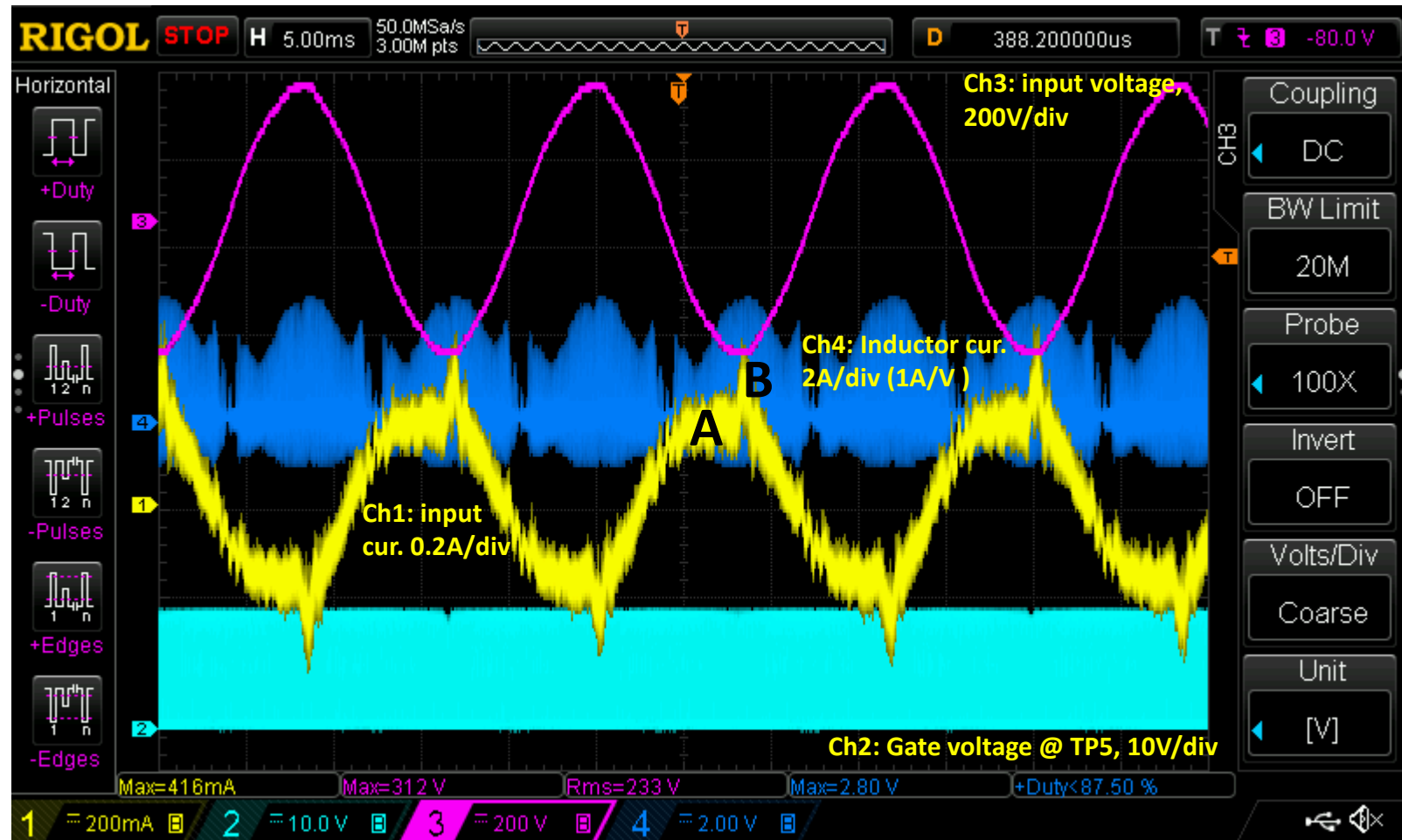
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~230Vac input voltage, C46=100nF, symmetric distortions show up on peaks of voltage waveform

Ch4: Tek TCPA300, Ch1: Fluke 80i-110S



Measuring inductor current vs gate voltages, 12/28

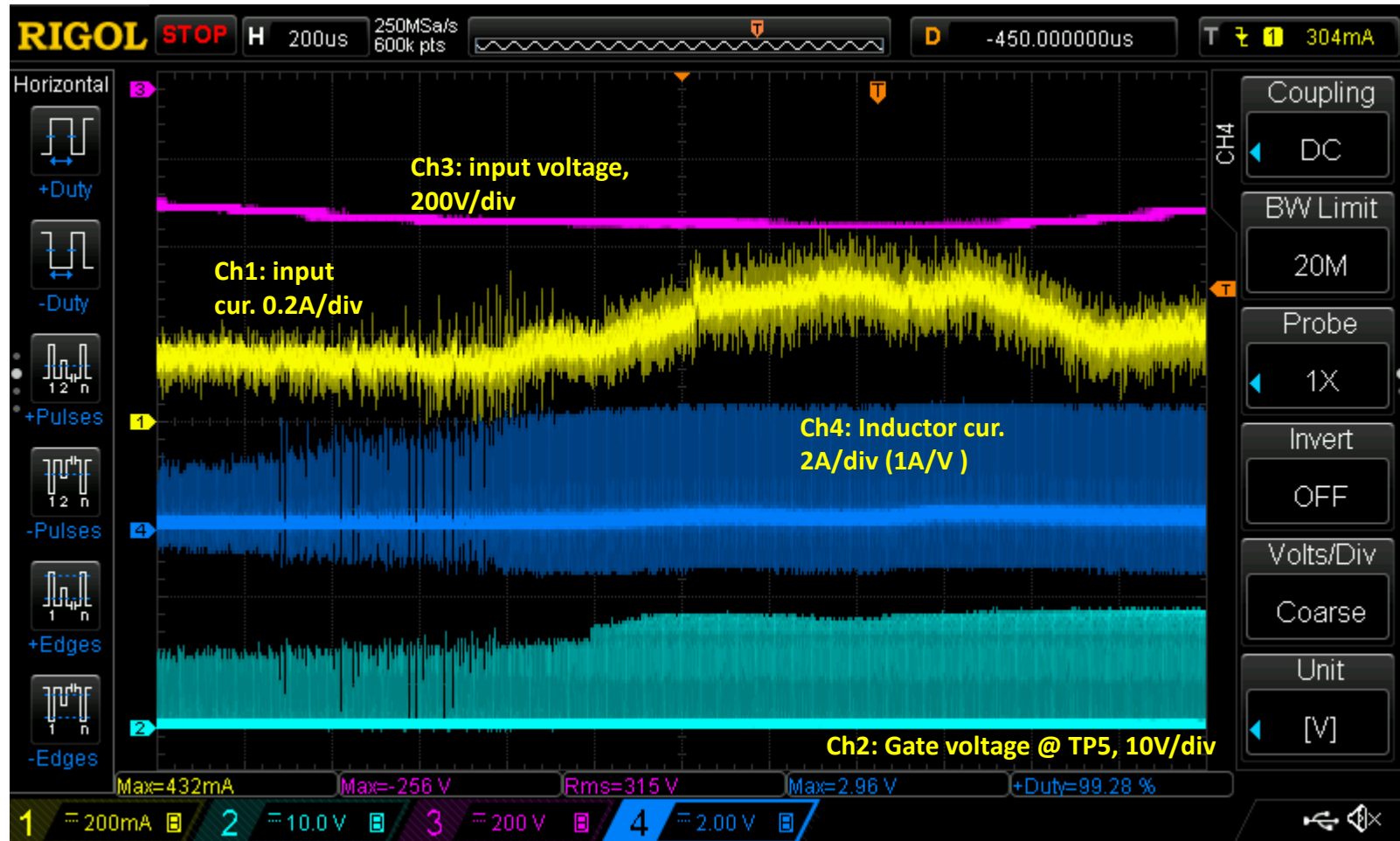
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~230Vac input voltage, C46=100nF, location A; ON times gets very small & pulses start to drop

Ch4: Tek TCPA300, Ch1: Fluke 80i-110S



Measuring inductor current vs gate voltages, 12/28

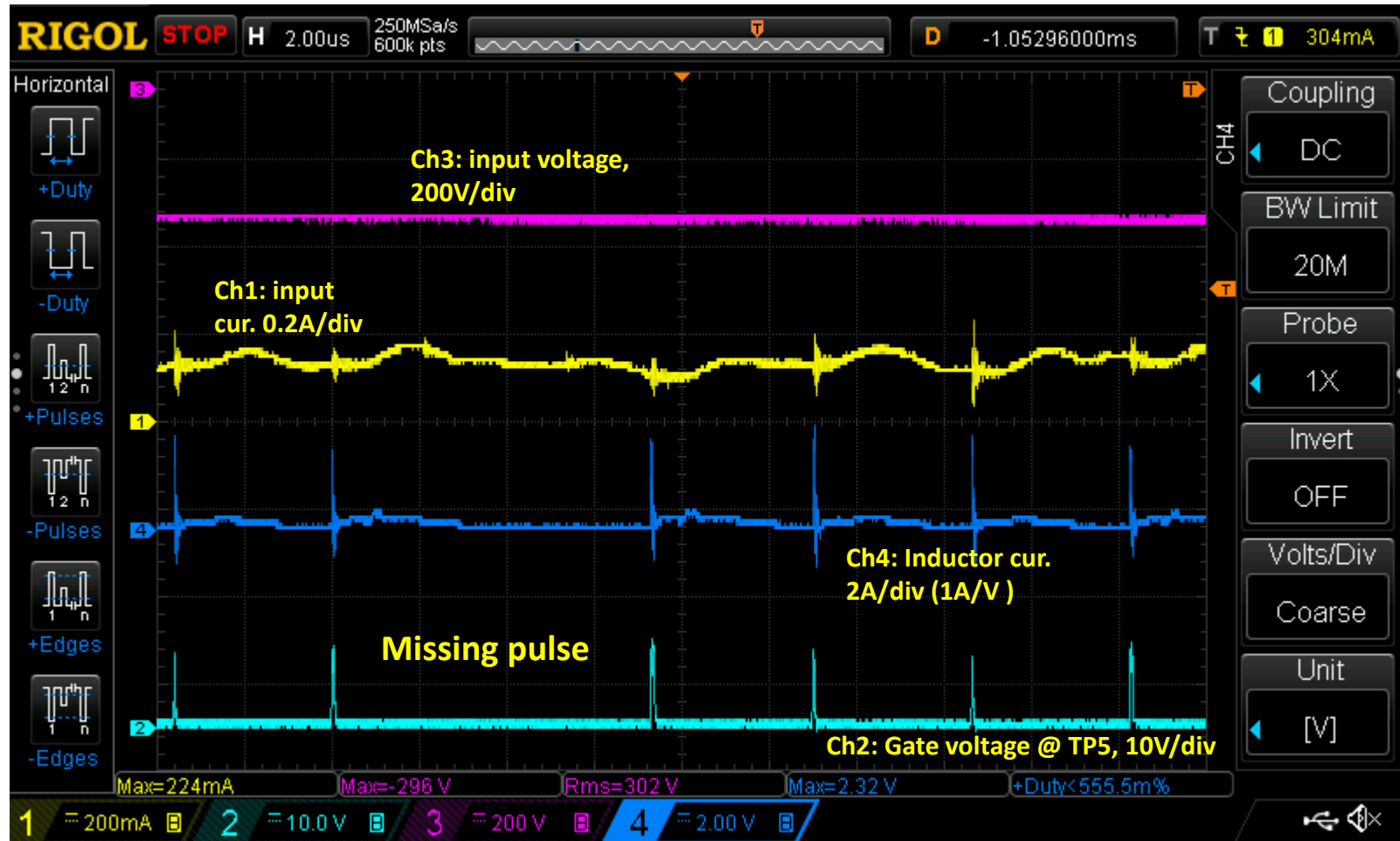
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

At ~230Vac input voltage, C46=100nF, location A; ON times gets very small & pulses start to drop

Ch4: Tek TCPA300, Ch1: Fluke 80i-110S



Measuring inductor current vs gate voltages, 12/28

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55

Conclusions on 12/28:

- 1) Removing the 47nF cap across the sense resistor made the issue worse that the current became more unstable and distortions started even at low voltages of $\sim 100\text{Vac}$
- 2) Replacing the capacitor across the sense resistor (C46) with 100nF, did not improve the problem that the current waveform was stable up to $\sim 230\text{Vac}$ and distortions started above this voltage, as it was the case with C46=47nF
- 3) Referring to location A; it appears that the duty cycle gets very small and even some pulses were dropped even though the current through the inductor does not appear to be the max. This suggests that the event is not initiated by peak current limit
- 4) Referring to location B; at this point no pulse dropping is observed. On the contrary, the duty-cycle increases which results in surge in inductor current.

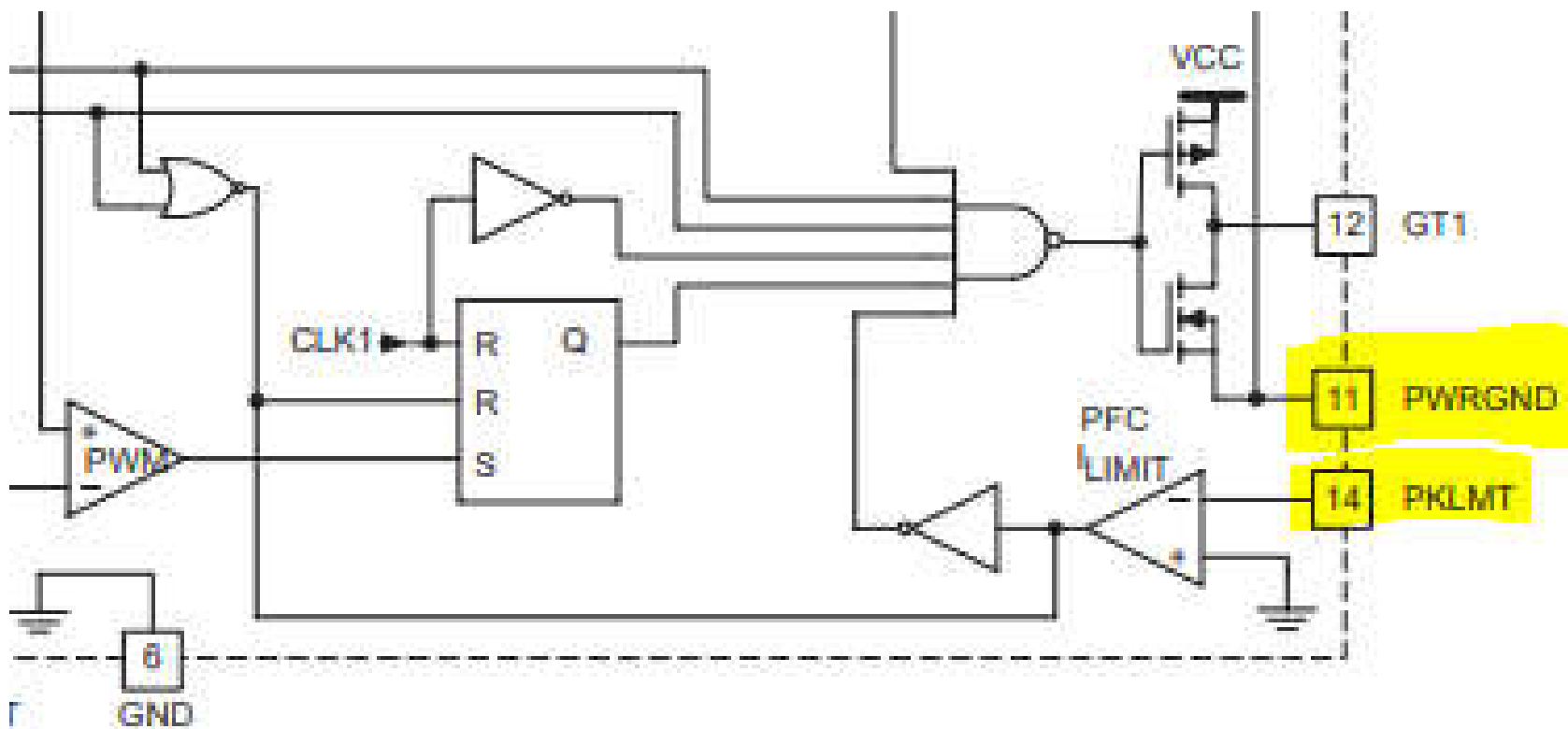
Measuring inductor current vs gate voltages, 1/8/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

100pF leaded ceramic capacitor was connected from pin14 (PKLMT) to pin11 (PWRGND). No improve observed
After that, a 1nF capacitor was added from pin14 to pin11, and again no improvements were observed



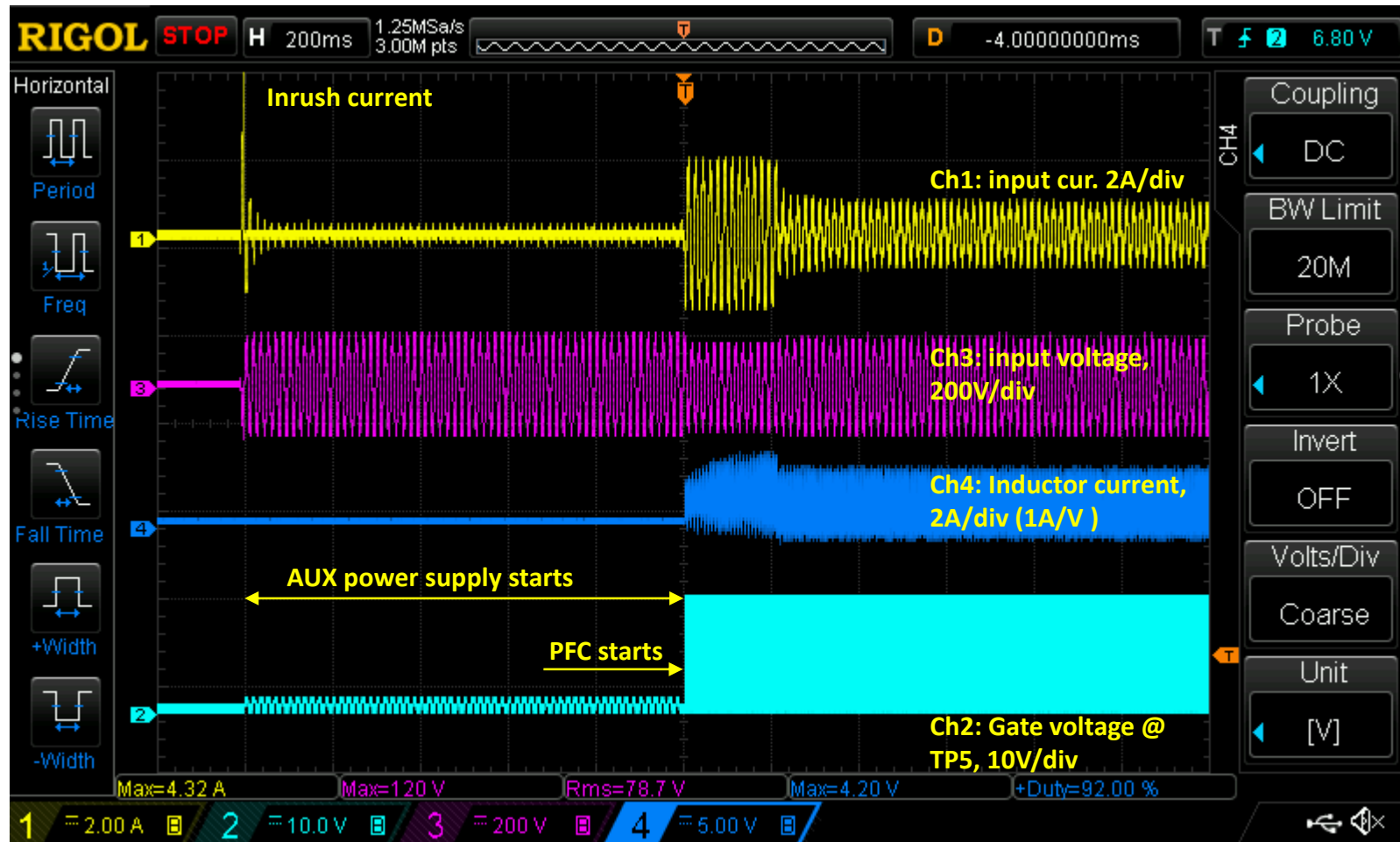
Measuring inductor current vs gate voltages, 1/8/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Start-up timing of PFC stage



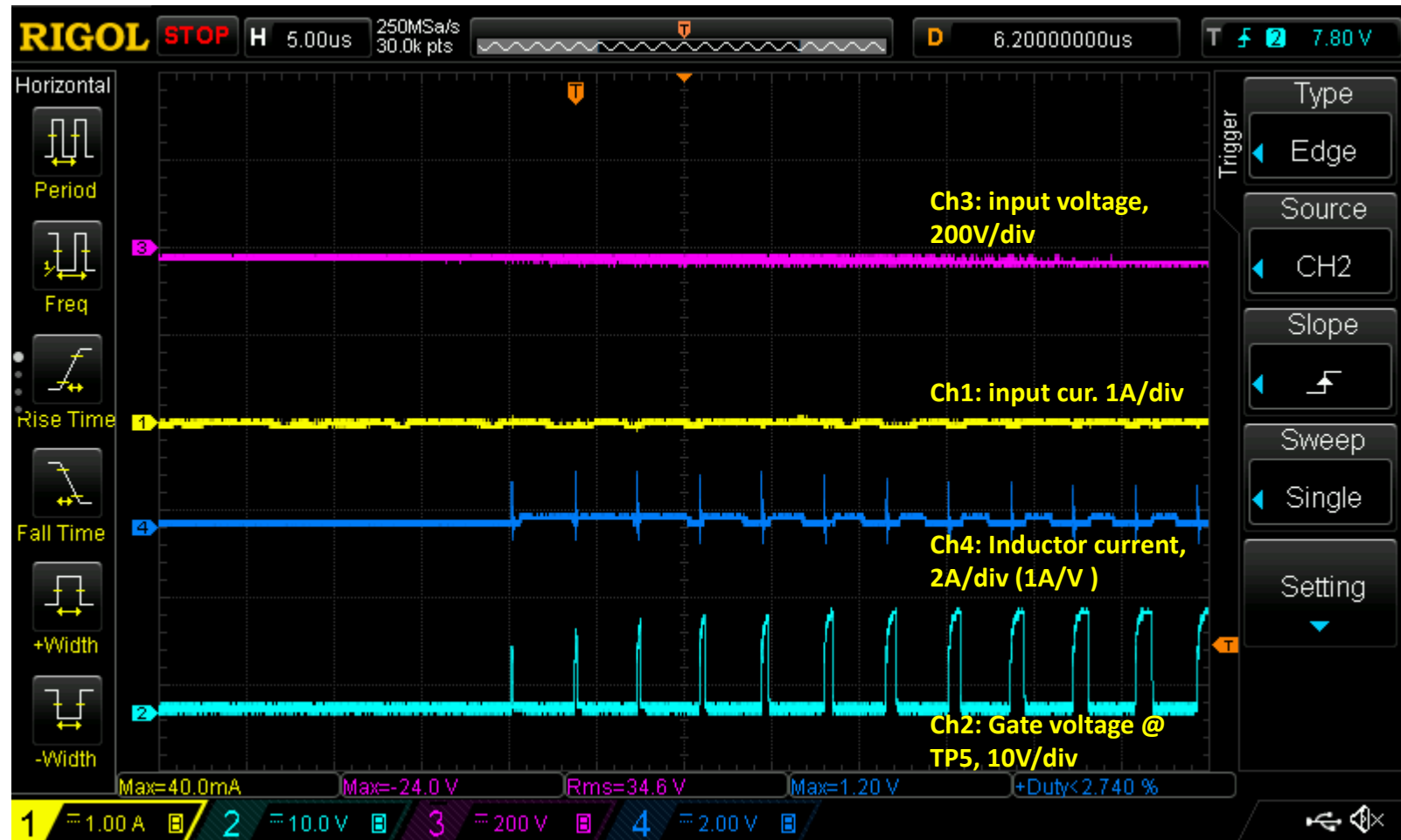
Measuring inductor current vs gate voltages, 1/8/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

At the time when PFC stage starts operating, it seems like the ON time pulse-width starts from zero and increases slowly



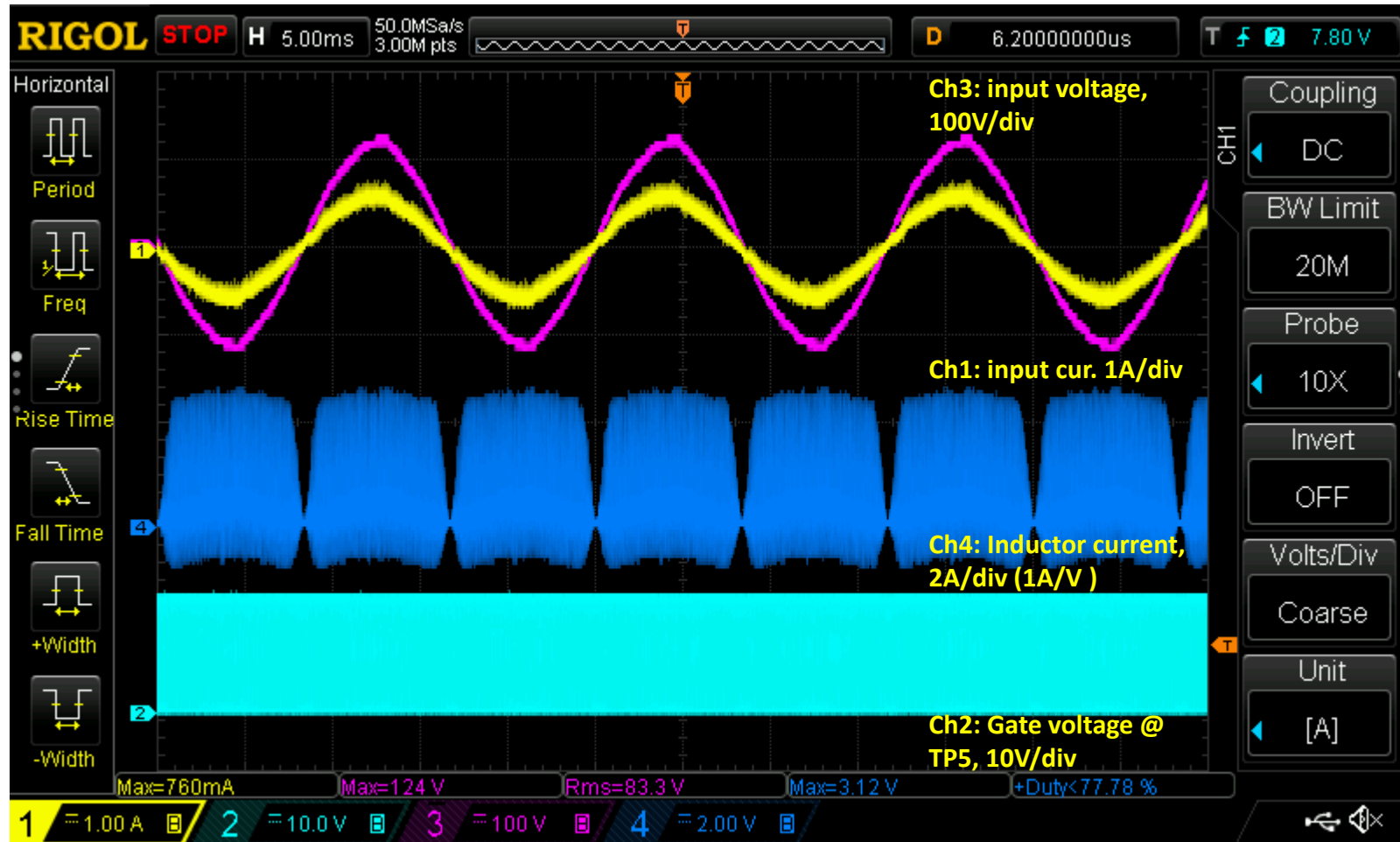
Measuring inductor current vs gate voltages, 1/8/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

With 1nF from PKLMT (pin14) to GND (pin11), no major distortions at 85Vac



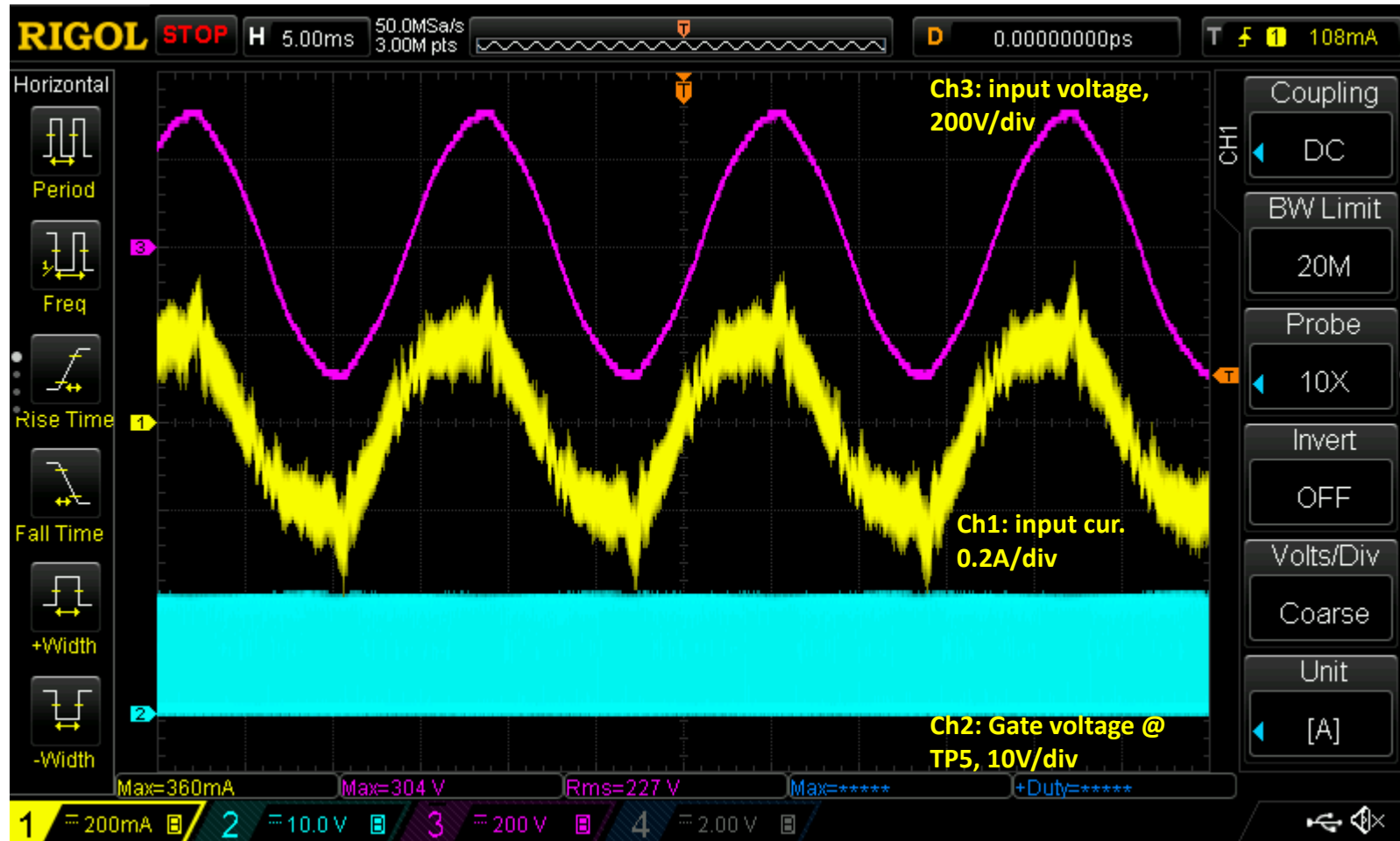
Measuring inductor current vs gate voltages, 1/8/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

**With 1nF from PKLMT (pin14) to GND (pin11), distortions start at around 220Vac
(also tried 100pF from pin14 to pin11, no improvements were observed)**

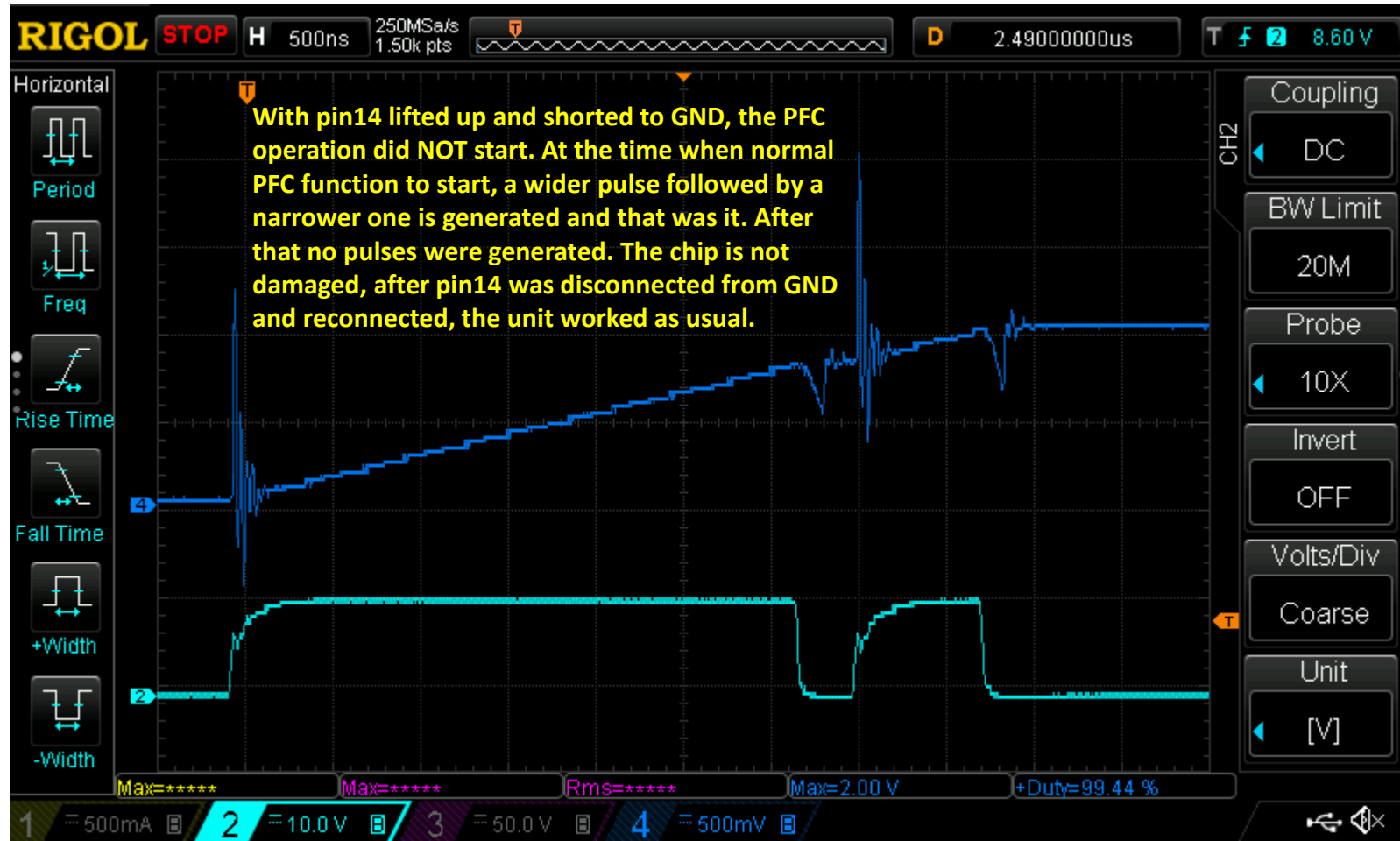


Measuring inductor current vs gate voltages, 1/8/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5



Measuring inductor current vs gate voltages, 1/8/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Conclusions on 1/8:

- 1) Connecting 100pF and then 1nF capacitor from pin14 (PKLMT) and pin11 (GND), right on the control chip, did not result in any improvements and that input current started to get distorted at around 230Vac
- 2) When PFC chip starts to operate, after AUX power supply is established, it seems like the ON time of the MOSFET starts from zero and is increased slowly. This makes sense, as doing so would reduce the chance of triggering PKLMT while DC-link cap is charged.
- 3) When pin14 is lifted up and shorted to pin11, at the time when it normally starts, the PFC chip generated a wider single pulse followed by a narrower one. The PFC chip did not go through the normal ramp up sequence. It is not clear why shorting the PKLMT to GND disables the ramp up function. And, since PKLMT is shorted to GND, it is presumed that the interruption of PFC function is not due to activation of PKLMT function.

SS2 (pin 13): A capacitor between SS2 and GND programs the softstart duration of the PWM stage gate drive. When the UVLO2 comparator enables the PWM stage, an internal 10.5-μA current source charges the external capacitor at SS2 to 3 V to ramp the voltage at VERR during startup. This allows the GT2 duty cycle to increase from 0% to the maximum clamped by the duty cycle comparator over a controlled time delay t_{SS} given by:

$$C_{SS2} = \frac{t_{SS} \times 10.5 \times 10^{-6} \times \text{Amp}}{3 \text{ V}}$$

C_{SS2} is in Farads

In the event of a disable command or a UVLO2 dropout, SS2 quickly discharges to ground to disable the PWM stage gate drive.

CAOUT (Pin 15): This is the output of a wide-bandwidth operational amplifier that senses line current and commands the PFC stage PWM comparator to force the correct duty cycle. This output can swing close to GND to command maximum duty cycle, and above the PFC ramp peak voltage to force zero duty cycle when necessary. Connect current loop compensation components between CAOUT and MOUT.

Measuring inductor current vs gate voltages, 1/15/2023

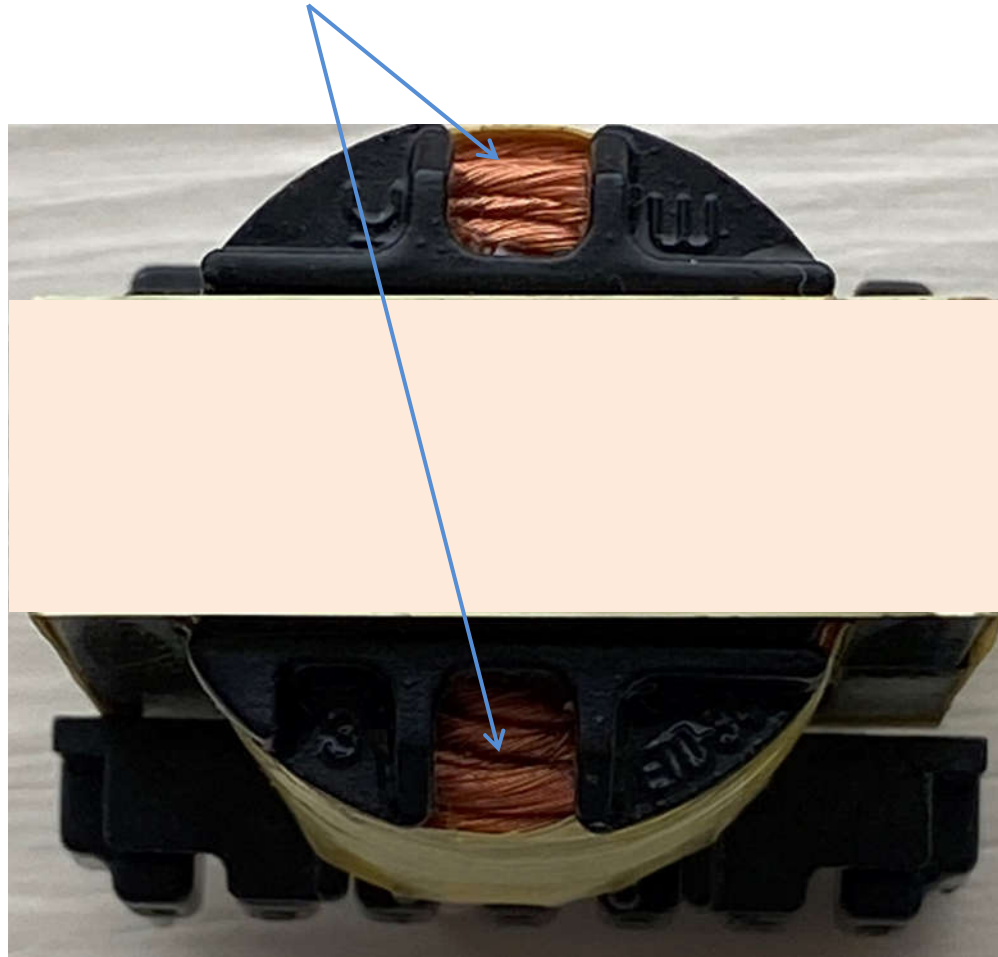
Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings

Original PFC inductor has overlapping layers of winding



Measuring inductor current vs gate voltages, 1/15/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

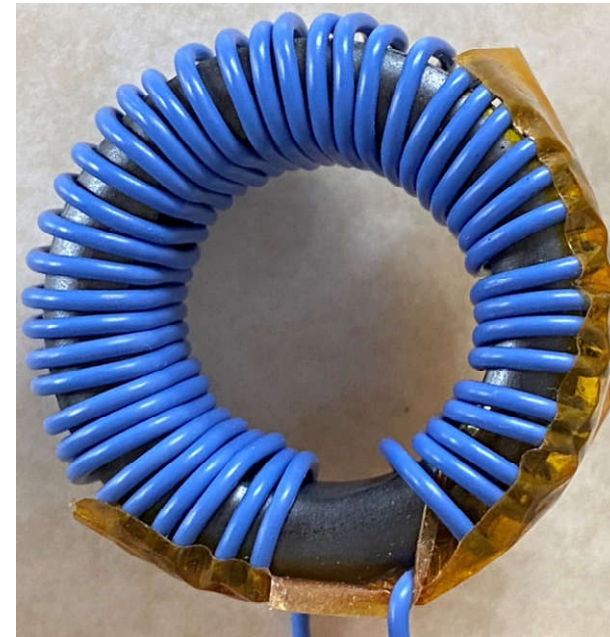
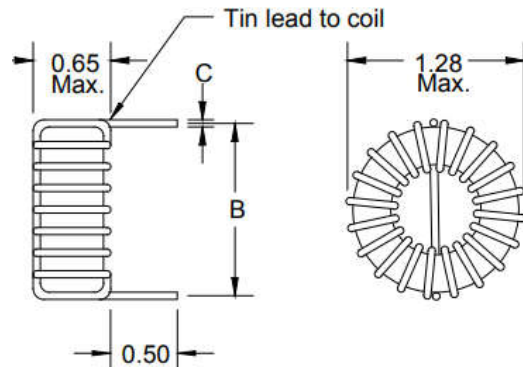
Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



Inductor #1: Bourns PN: 2300LL-681-H-RC, 680uH
Built by using toroid core, single layer winding.
However, some amount of winding overlapping is visible.



Inductor #2: Homemade by using toroid core
Würth PN: 7427015 and 43 turns of single layer
winding.
Inductance measured at 1.39mH @ 300kHz.

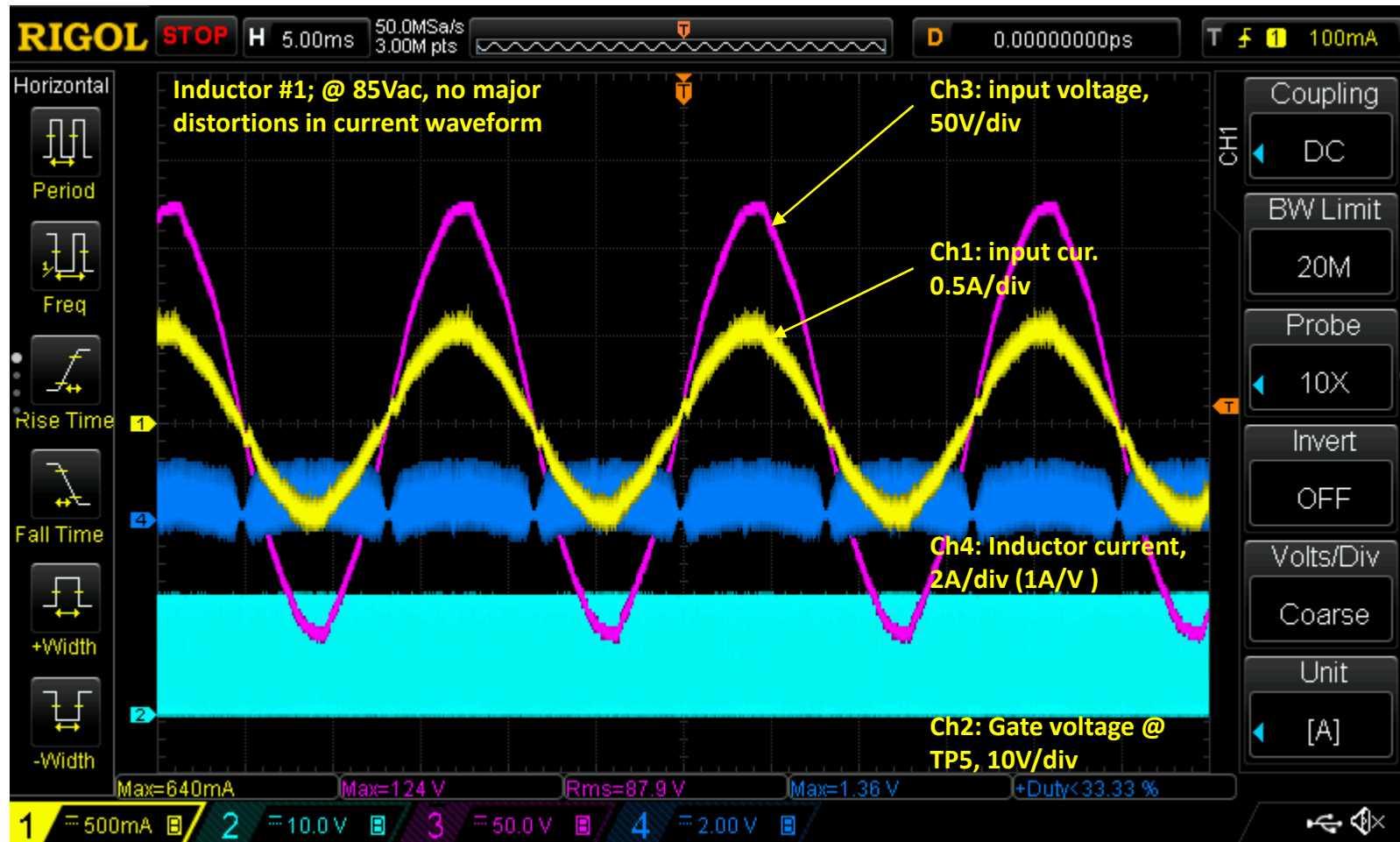
Measuring inductor current vs gate voltages, 1/15/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



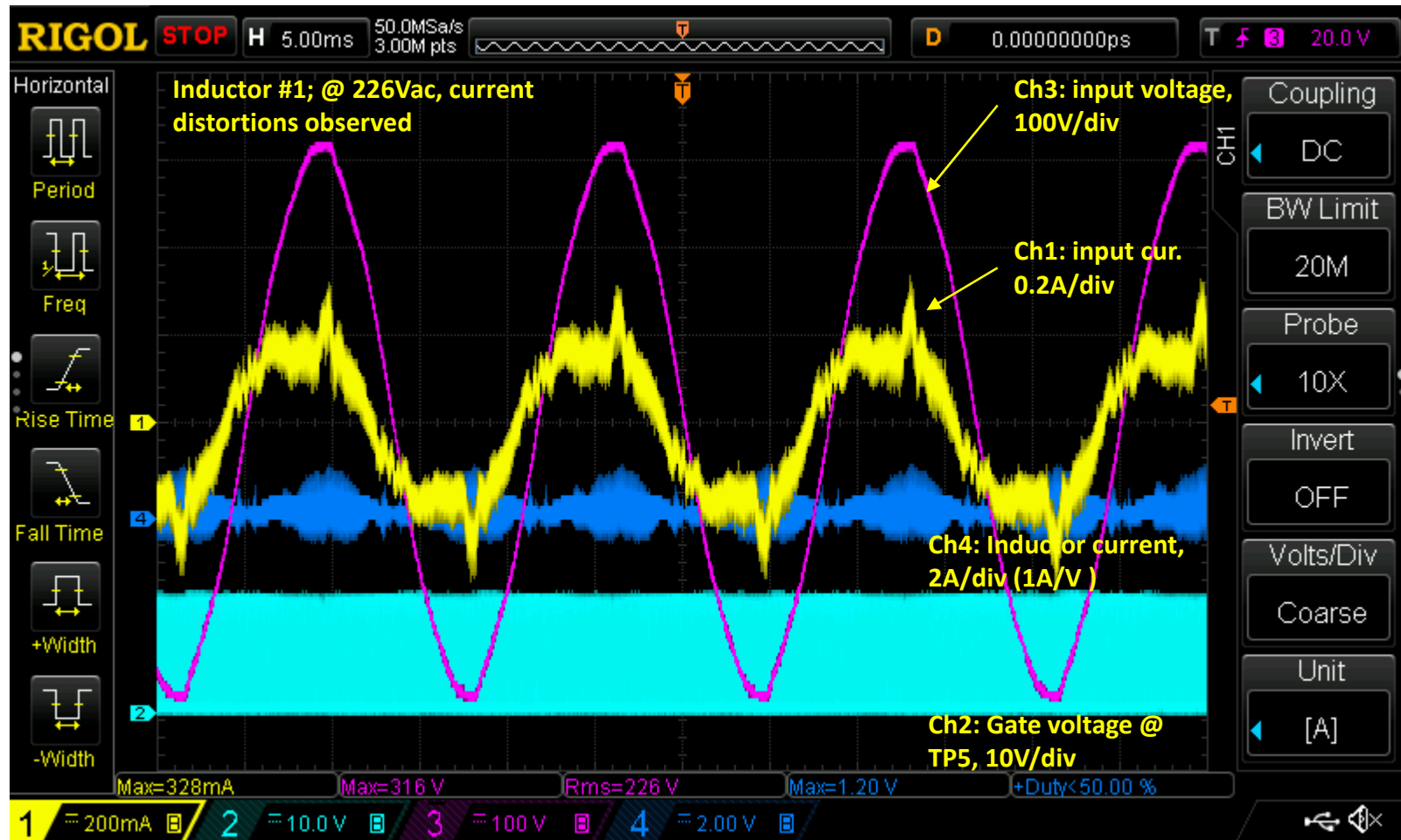
Measuring inductor current vs gate voltages, 1/15/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



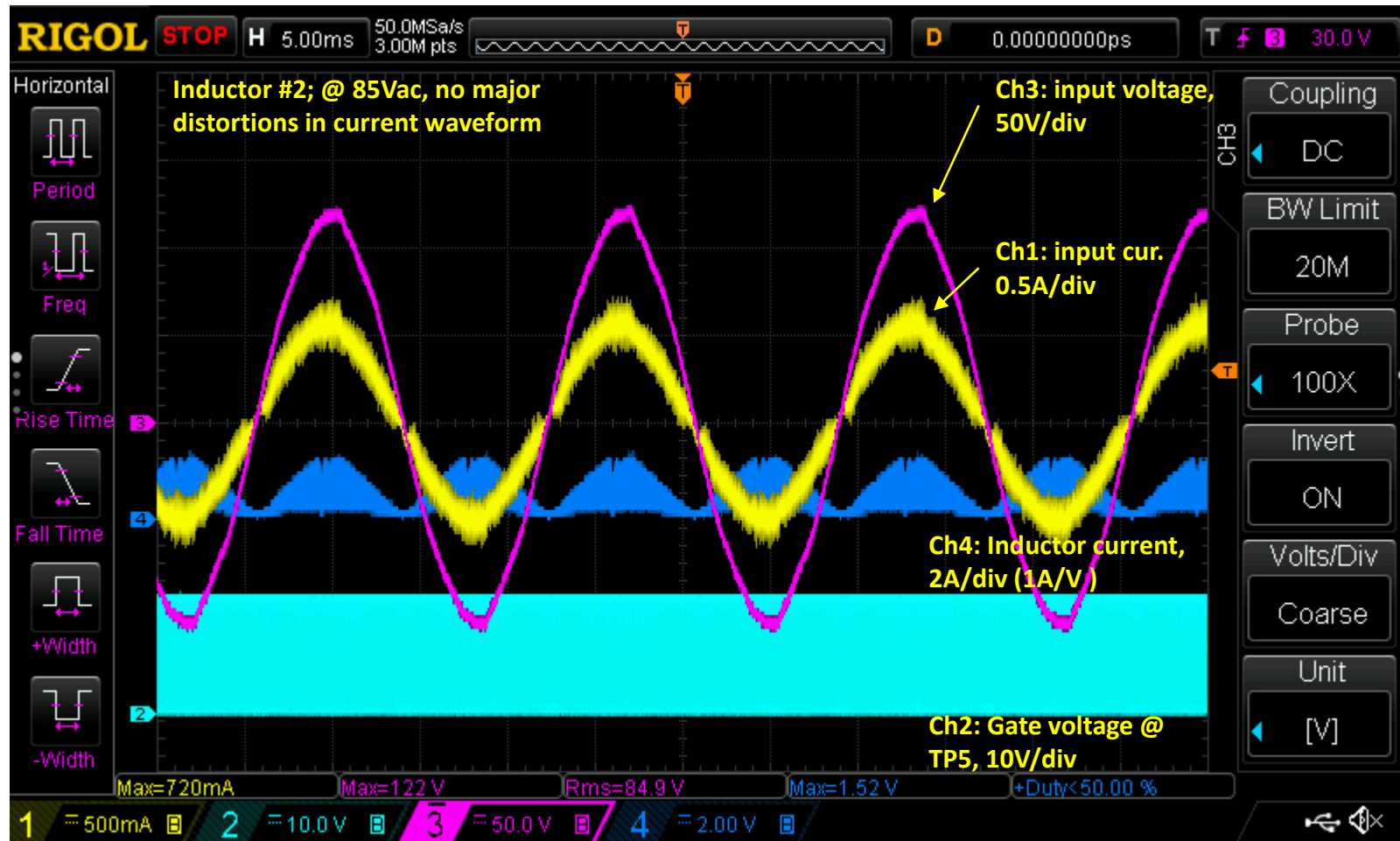
Measuring inductor current vs gate voltages, 1/15/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



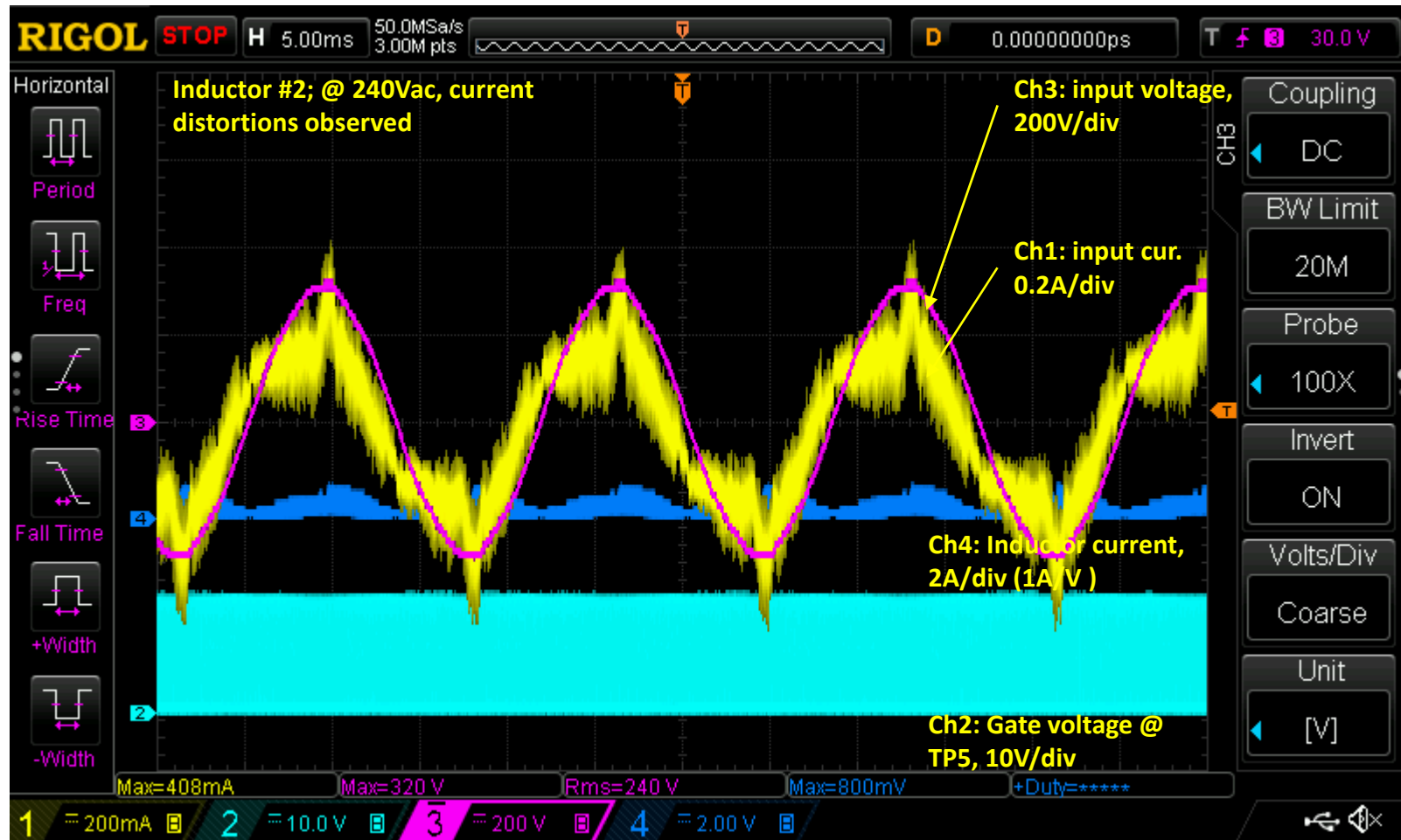
Measuring inductor current vs gate voltages, 1/15/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



Measuring inductor current vs gate voltages, 1/15/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings

Conclusions on 1/15:

- 1) Two different inductors with no or minimal winding overlapping are tried out. Current distortions at high voltages were observe in both case.
- 2) Original inductor has overlapping windings and new ones do not. Even though the winding to winding capacitance is not measured, it is assumed that a single layer toroid inductor would result in lowest inter-winding capacitance.
- 3) Is there any practical way to measure the inter-winding capacitance?
- 4) Is there any rule of thumb as to how much inter-winding capacitance would be too much?

Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



Inductor #2: Homemade inductor by using toroid core Wurth PN: 7427015 and 43 turns of single layer winding.

Inductance measured at 1.39mH @ 300kHz.

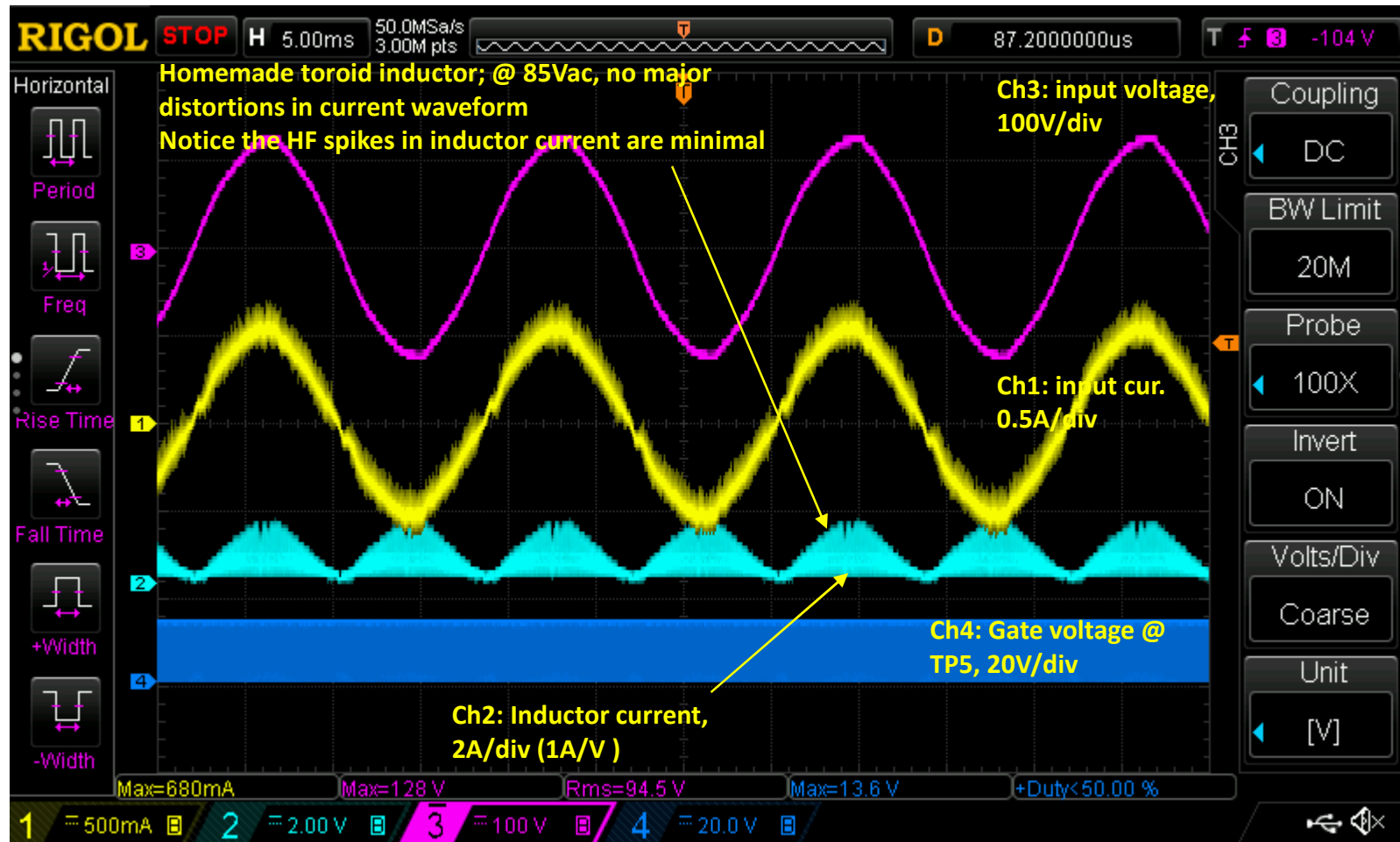
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



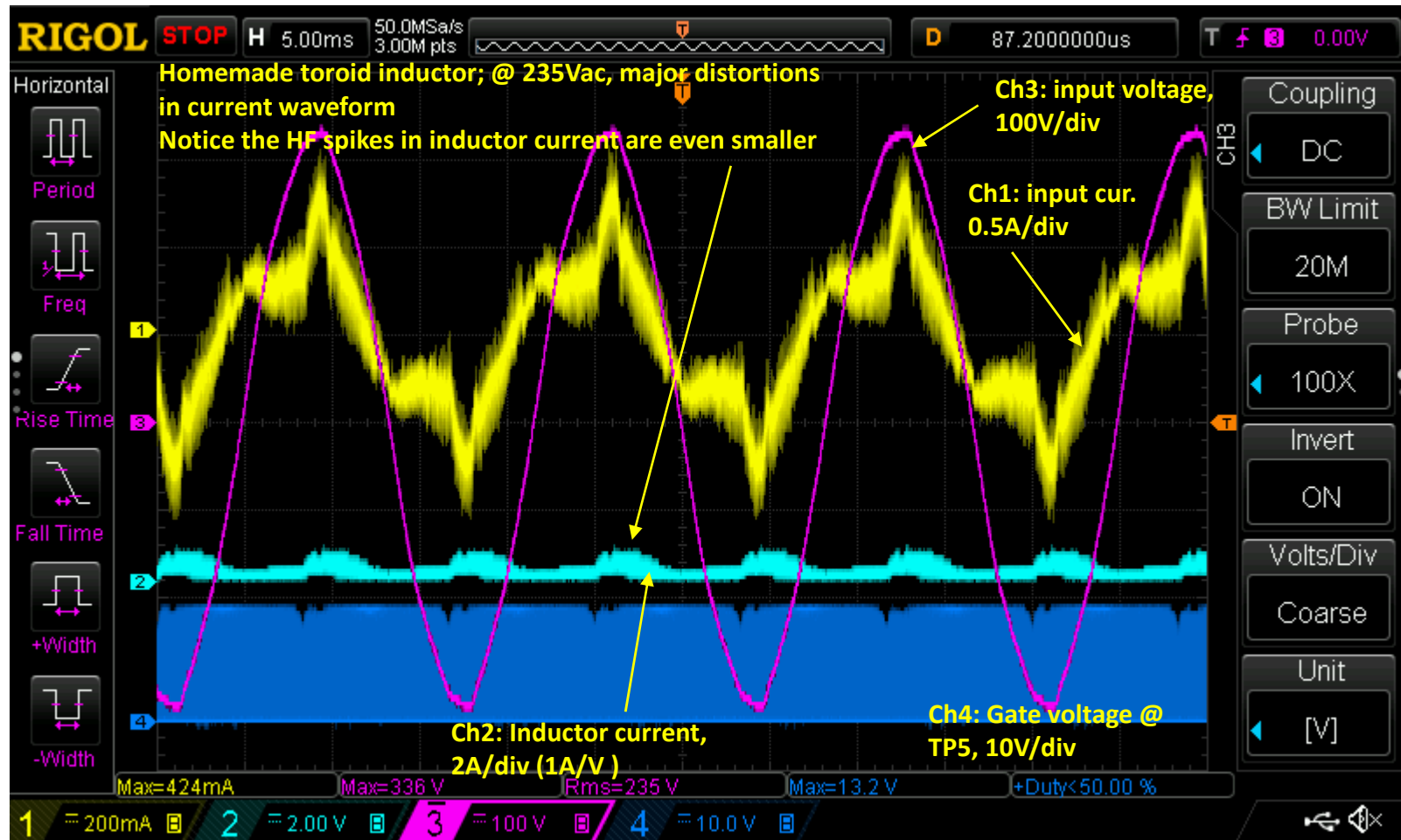
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



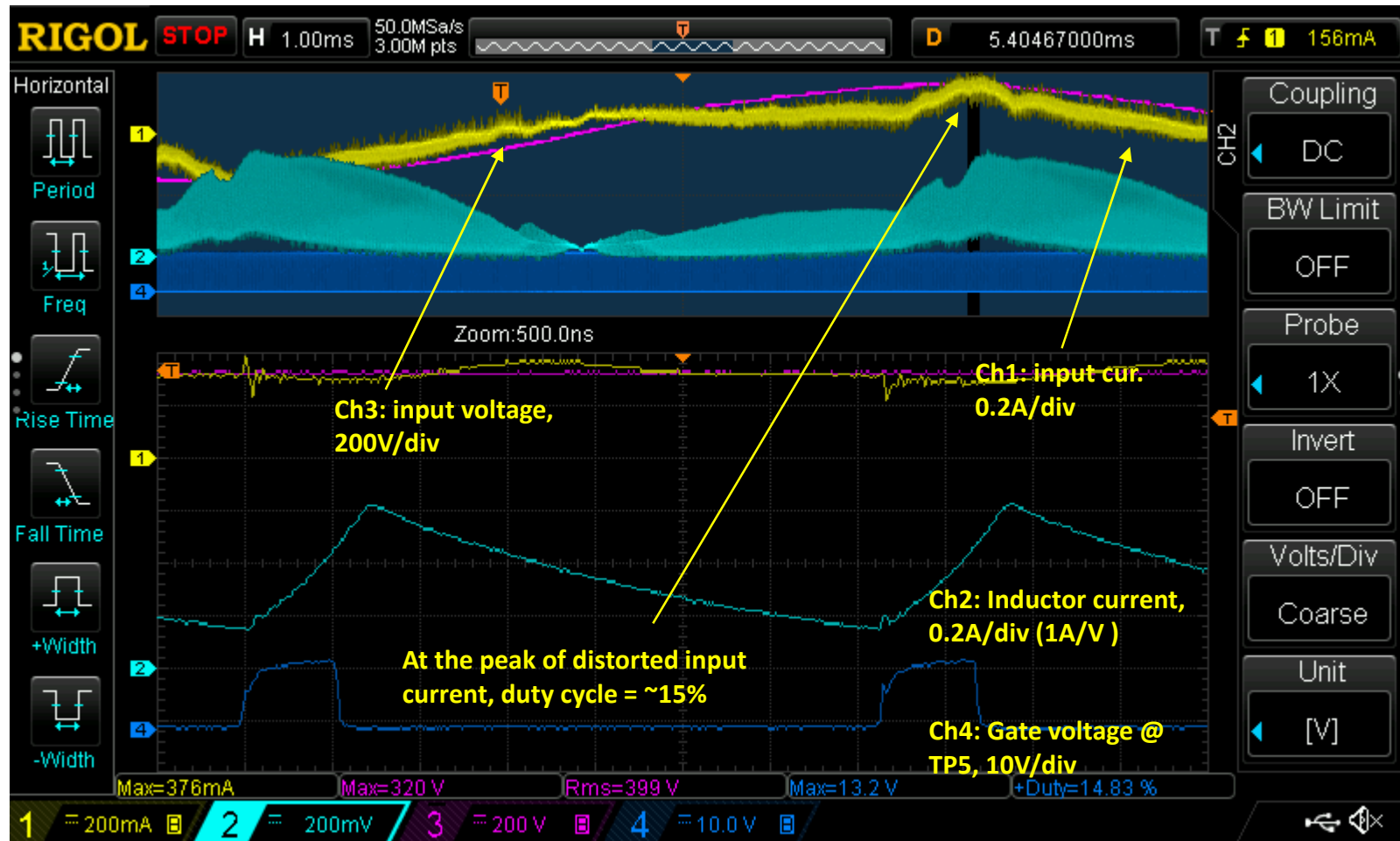
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



Homemade toroid inductor; @ 235Vac, major distortions in current waveform. Notice that there are no turn-on spikes in inductor current waveform

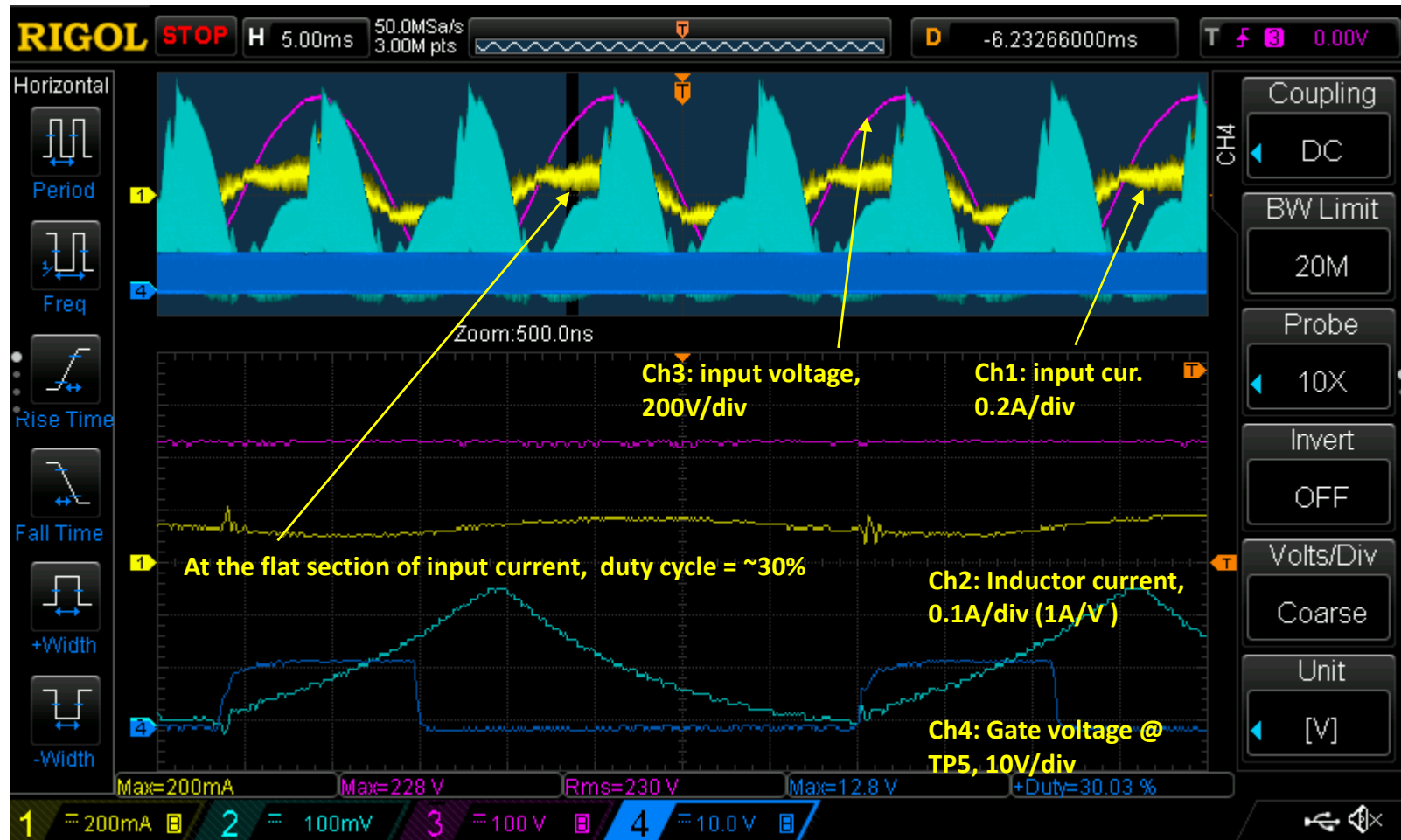
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



Homemade toroid inductor; @ 235Vac, major distortions in current waveform. Notice that there are no turn-on spikes in inductor current waveform

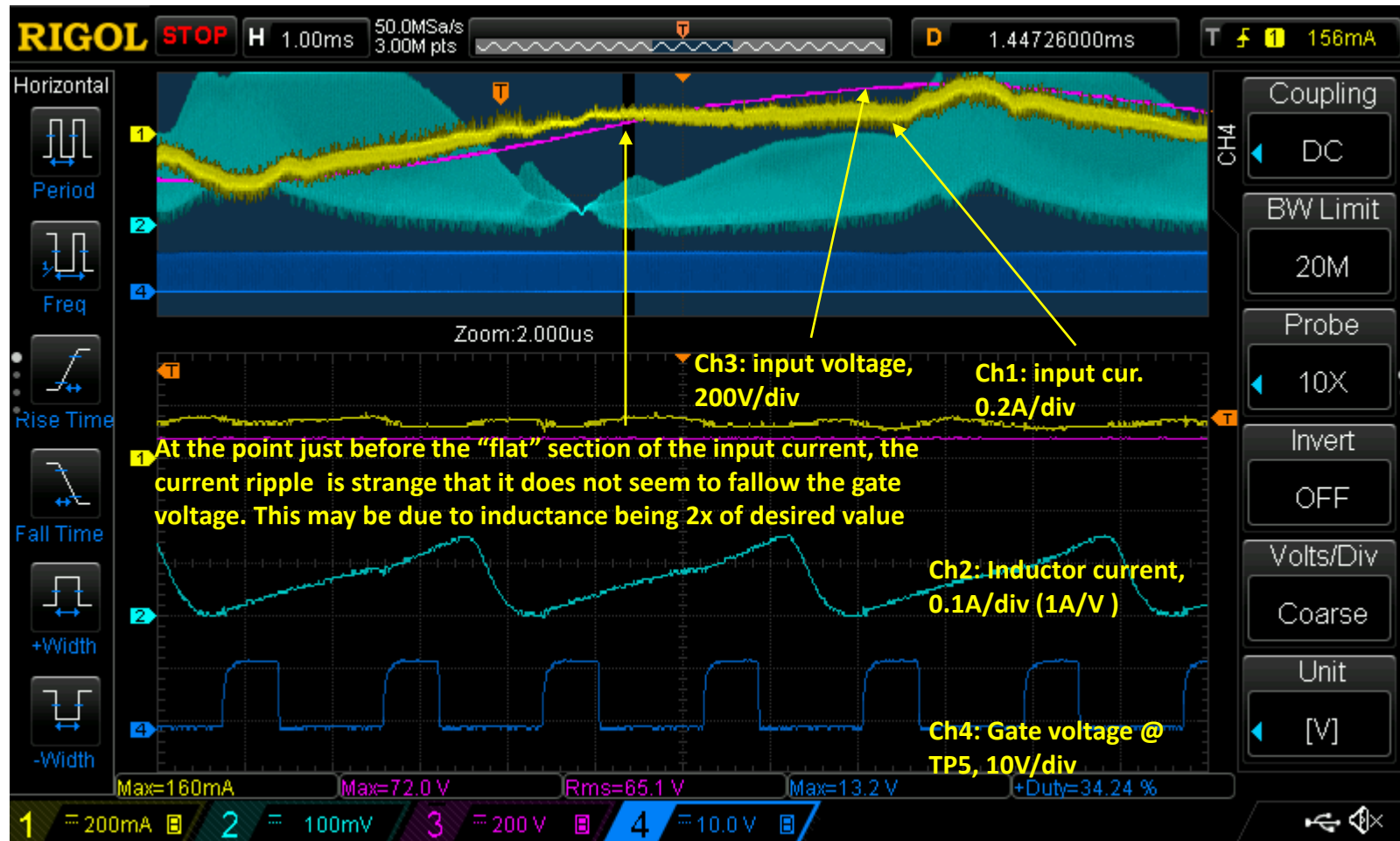
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Trying out with PFC boost inductors that are built by using toroid cores with no overlapping windings



Homemade toroid inductor; @ 235Vac, major distortions in current waveform. Notice that there are no turn-on spikes in inductor current waveform

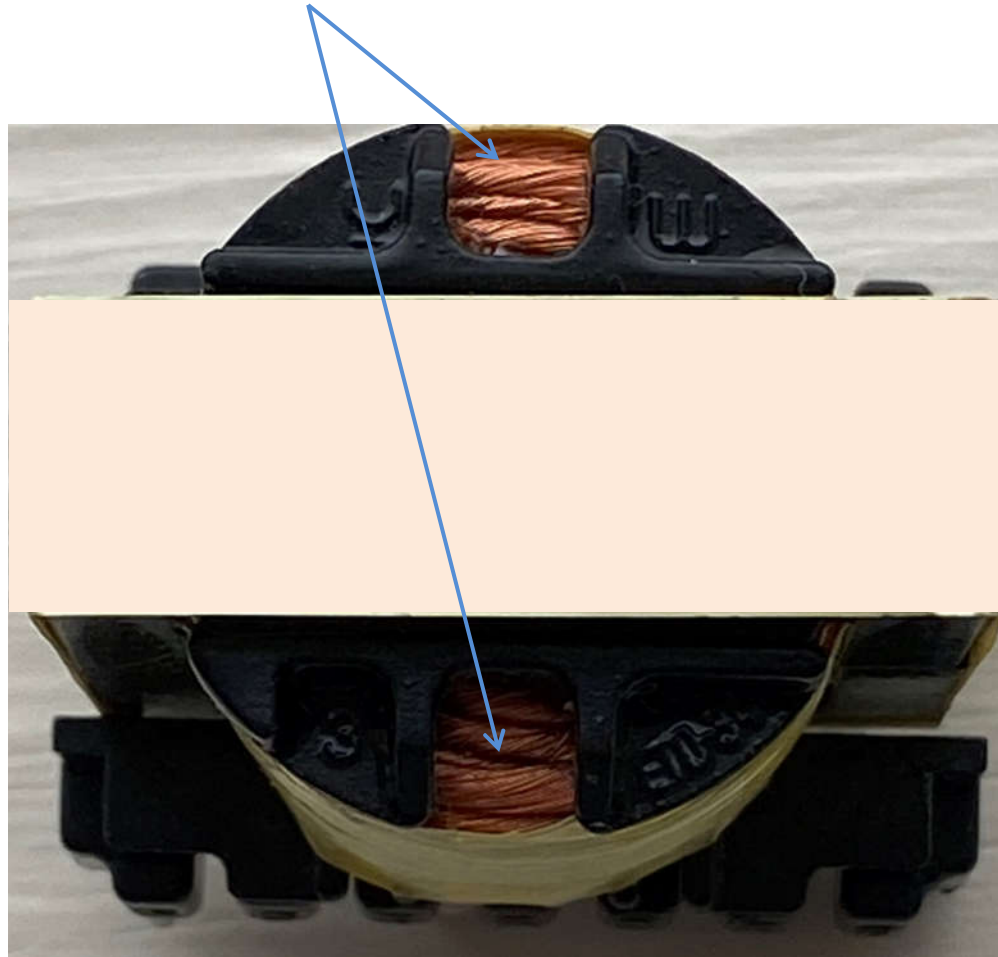
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Original PFC inductor (680uH) has overlapping layers of winding



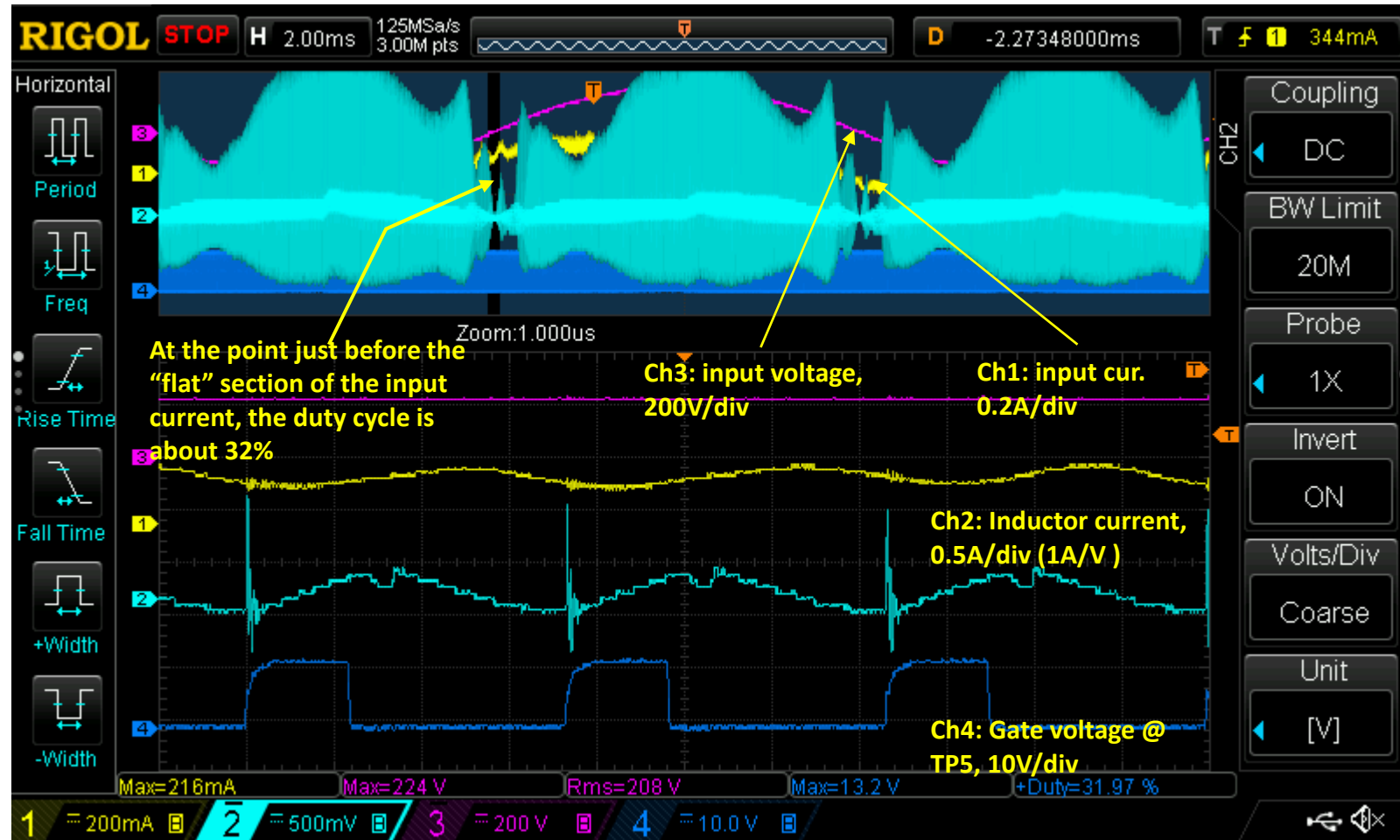
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Original inductor @ 235Vac, major distortions in current waveform. Notice that there are significant current spikes at the time when MOSFET turns on.



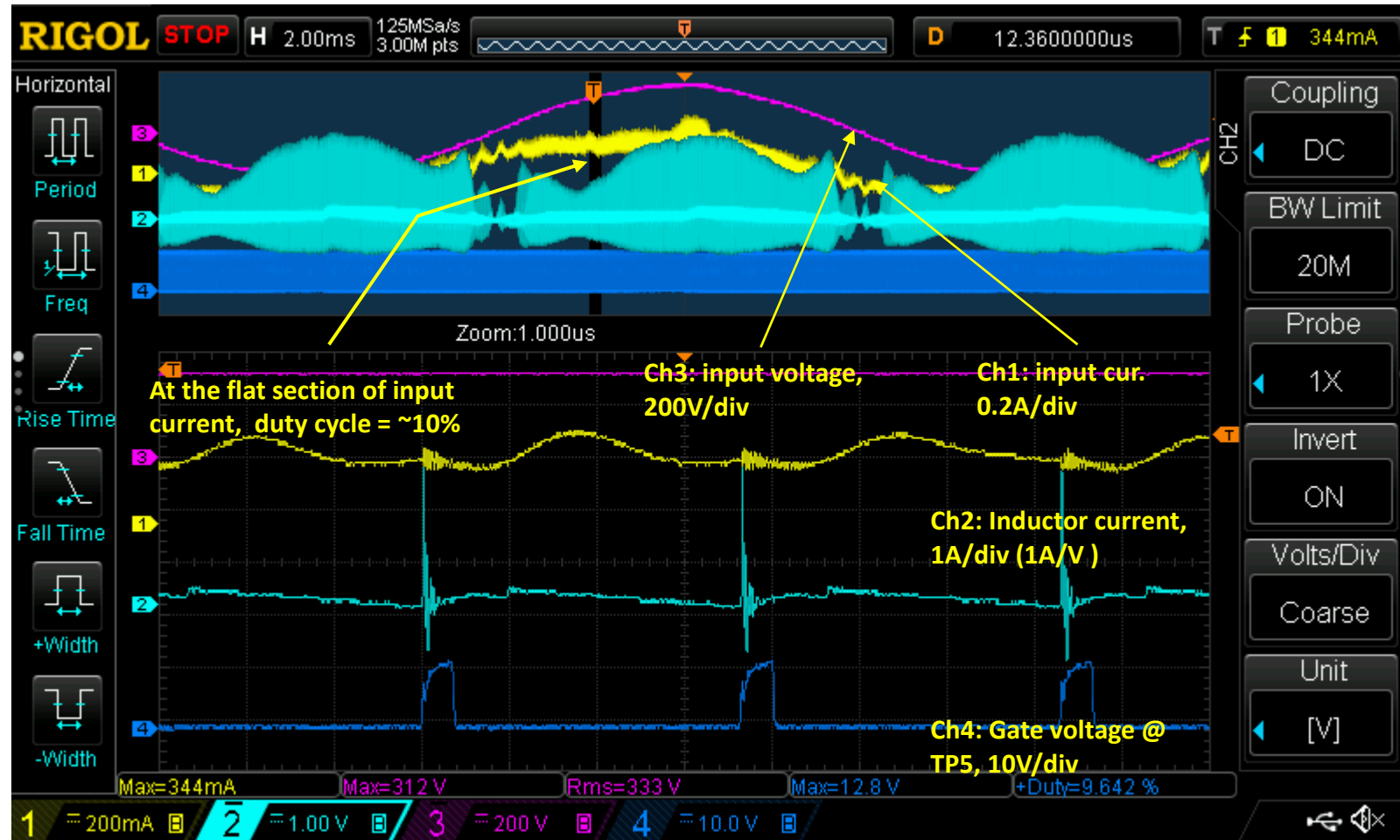
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Original inductor @ 235Vac, major distortions in current waveform. Notice that there are significant current spikes at the time when MOSFET turns on.



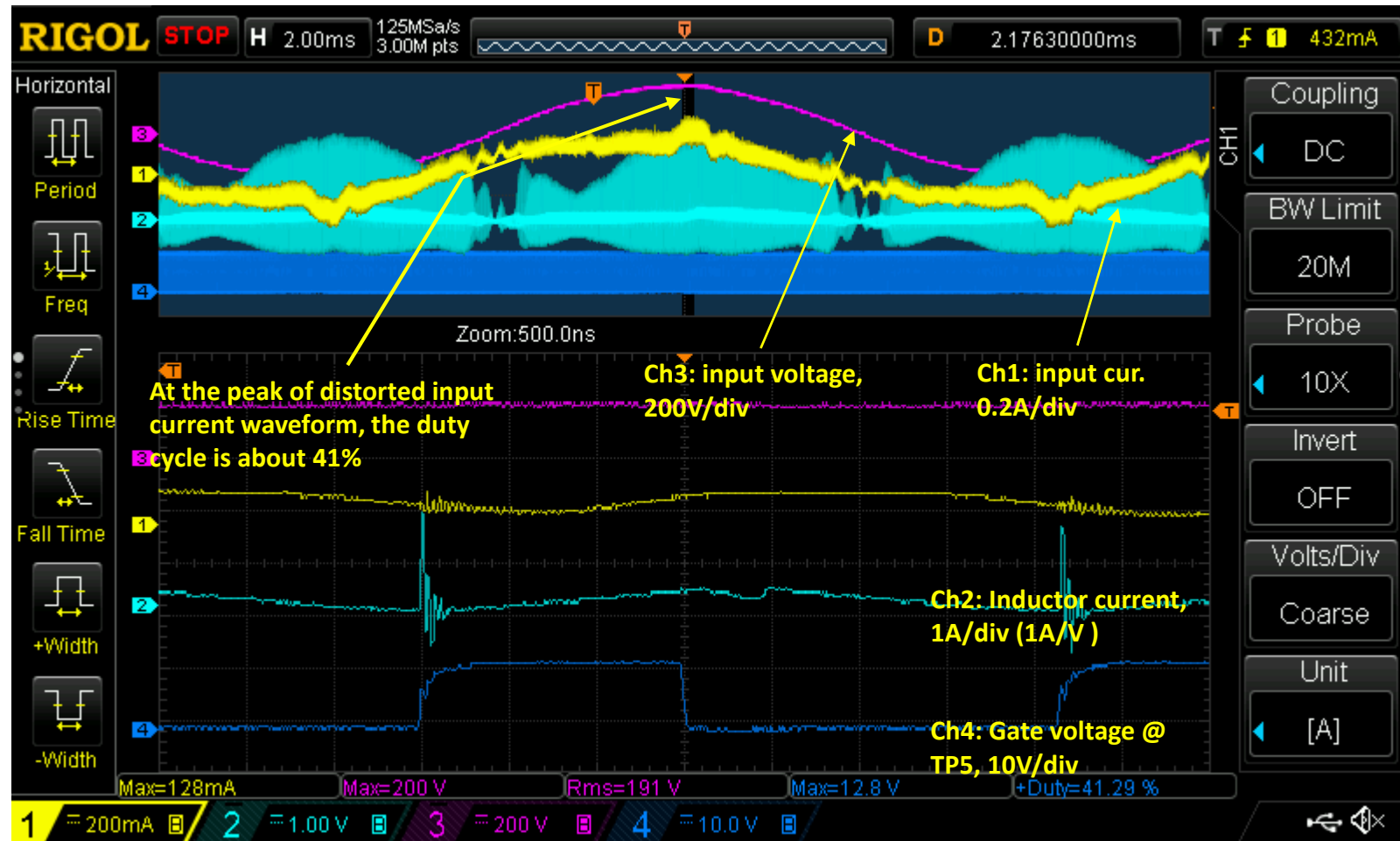
Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

Original inductor @ 235Vac, major distortions in current waveform. Notice that there are significant current spikes at the time when MOSFET turns on.



Measuring inductor current vs gate voltages, 1/22/2023

Ch1: Fluke 80i-110S, which has limited bandwidth

Ch4: Tek TCPA300 w/ TCP312 Current probe

Per changes listed on page 55, 100pF cap removed from R5, 10nF from UCC28513 pin14-pin11

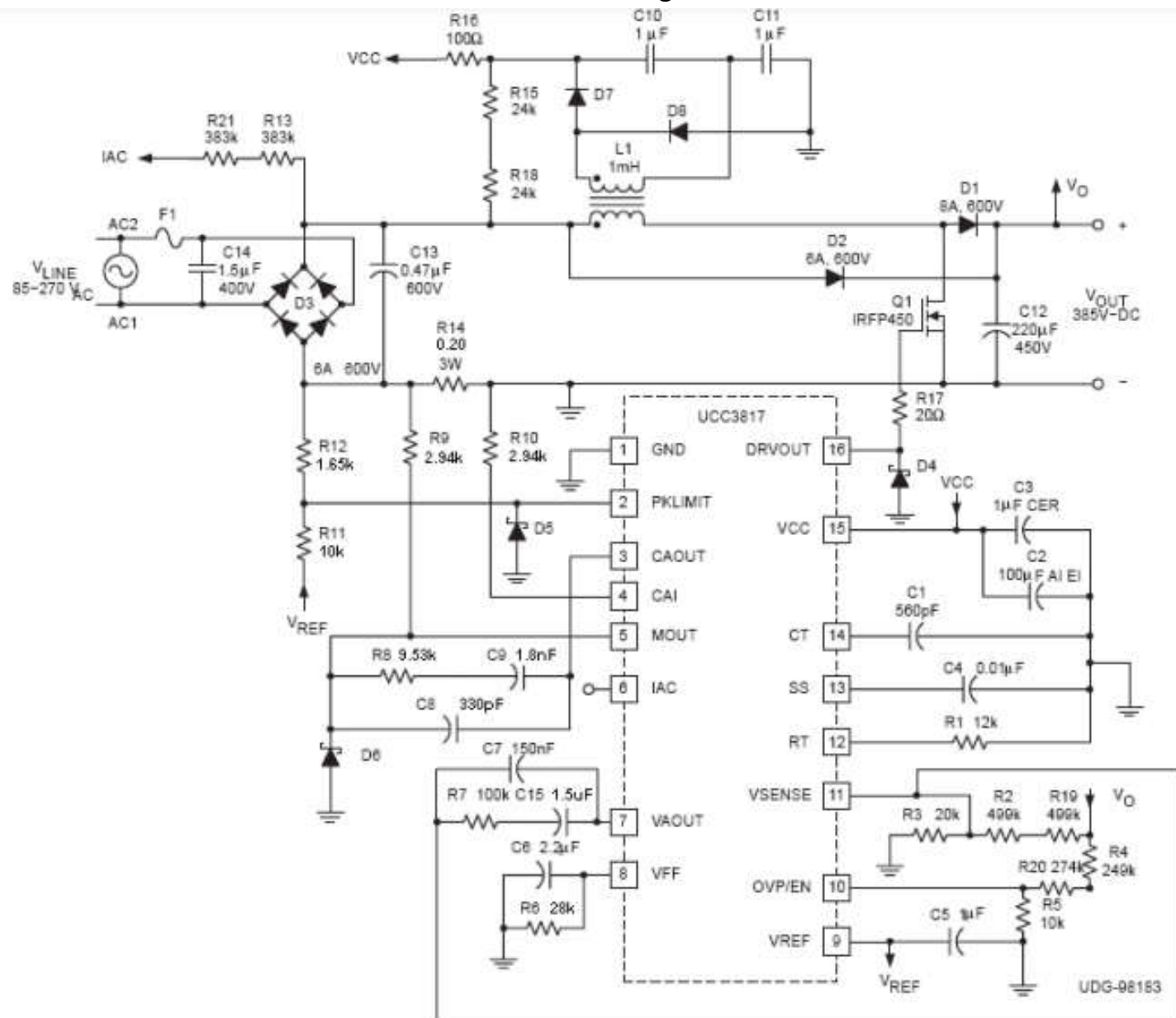
Conclusions on 1/22:

1. It seems like there is significant inter-winding capacitance in original inductor (TSD-3222, 680uH) which creates significant current spikes at the time when MOSFET turns on. Additional measurements conducted with 10nF cap on PKLMT pin14 to GND do not show any pulse droppings. However, the low frequency distortions in input current waveform are still visible.
2. The home-made toroid inductor (1.39mH @ 300kHz) was also tried out with 10nF on PKLMT pin. No pulse droppings observed. However, the low frequency distortions in input current waveforms were still visible. There was some unexpected results at the zero crossing of the voltage. This may be due to inductance being larger than the expected value. Repeat the same test with smaller toroid inductor with 680uH inductance.

Even though the original inductor has some inter-winding capacitance and even though it should be addressed, it seems that the current distortion may not be caused by the inter-winding capacitance since:

1. The current distortions are visible even when a toroid inductor with minimum inter-winding capacitance is used. There were no turn-on spikes and no pulse dropping observed with 10nF capacitance on PKLMT.
2. When the original inductor was used, there were significant current spikes at the turn on. Even though no pulse droppings observed with 10nF on PKLMT pin, the input current waveform was still distorted.

UCC3817 based design values



Measuring inductor current vs gate voltages, 2/5/2023

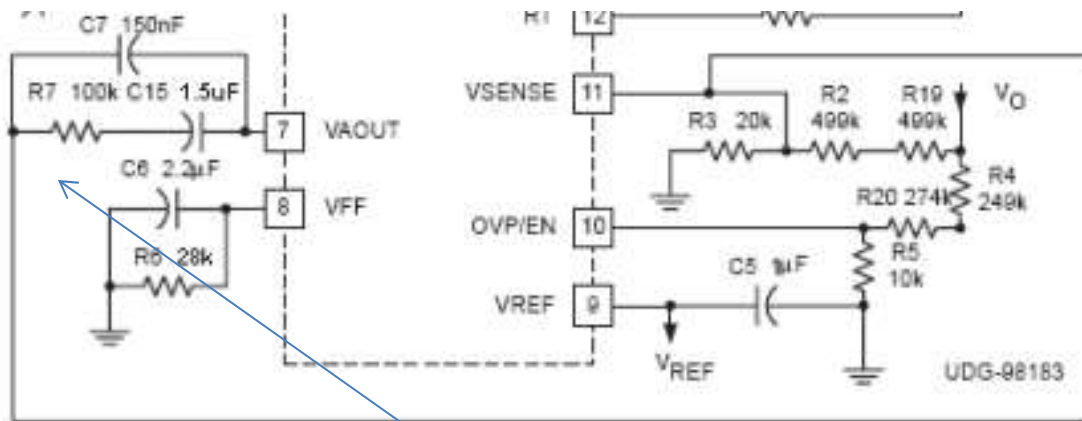
UCC3817 based design values

RefDes on UCC3817	RefDes on UCC28513	RefDes in Listen	PCB	Existing values	New values
R14=0.2	R2	R43	PSB-04	0.44	0.22
R3=20K	-	R60		22.1K	20K
R2/R19=499K	-	R44/R49		562K	499K
R21/R13	-	R47/R50		383K	No change
R11=10K	R14	R13	PFC-03	10K	No change
R12=1.65k	R7	R5		1.5K	1.62K
R9=2.94K	R12	R4		2.74K	2.94K (3.16k//39k)
C8=330p	C7	C8		150pF	330p
C9=1.8nF	C6	C6		3.3n	1.6nF
R8=9.53K	R13	R10		4.7K	9.53K (20k//18.2k)
R10=2.94K	R8	R9		2.74K	2.94K (3.16k//39k)
R6=28K	R15	R11		30.1K	28K (56k//56k)
C6=2.2uF	C8	C9		4.7uF	2uF (1uF//1uF)
C7=150n	C11	C2		150n	No change
C15=1.5uF	C10	C1		1.5uF	No change
R7=100k	R21	R1		48.7k	100k

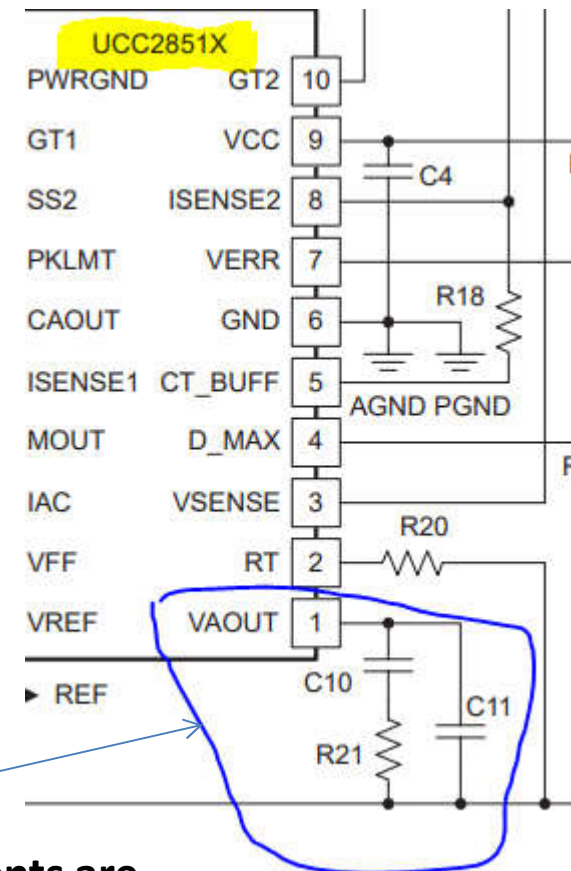
Measuring inductor current vs gate voltages, 2/5/2023

UCC3817 based design values

UCC3817 voltage error amp.



UCC28513 voltage error amp.



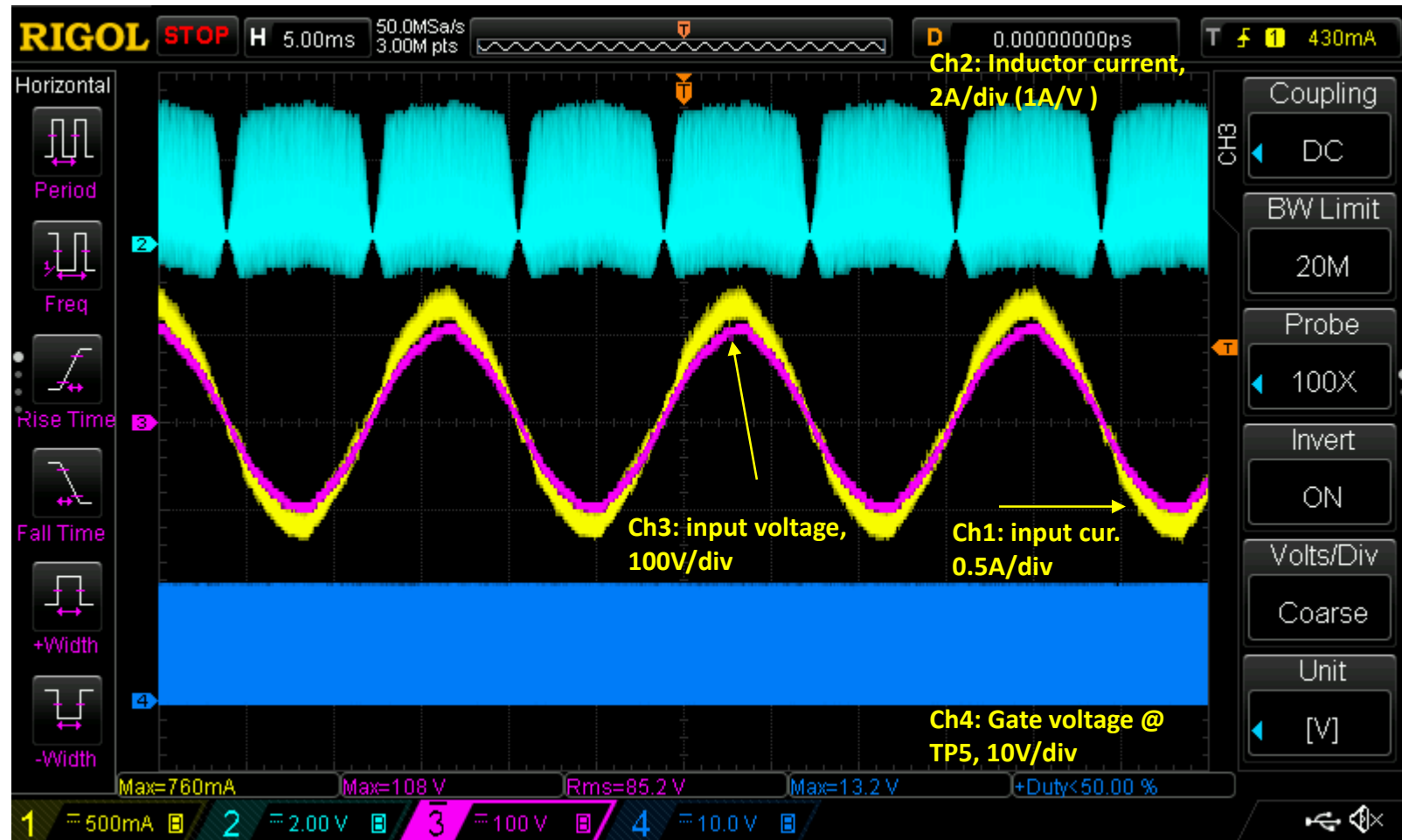
As part of this exercise, voltage error amp components are disconnected from GND and connected to pin3 (Vsense)

Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5
Original boost inductor (680uH), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

No major distortions at 85Vac input voltage

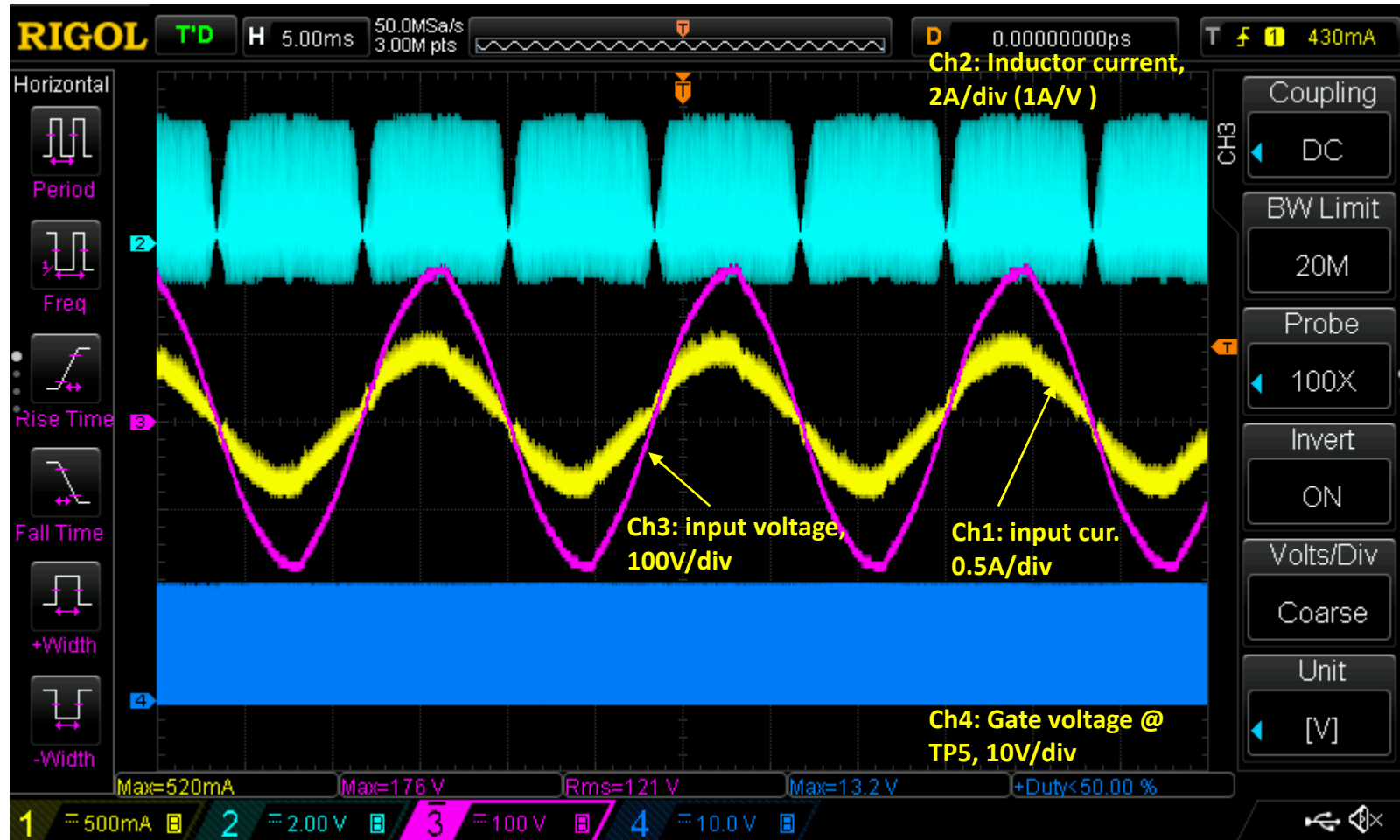


Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5
Original boost inductor (680uH), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

No major distortions at 120Vac input voltage

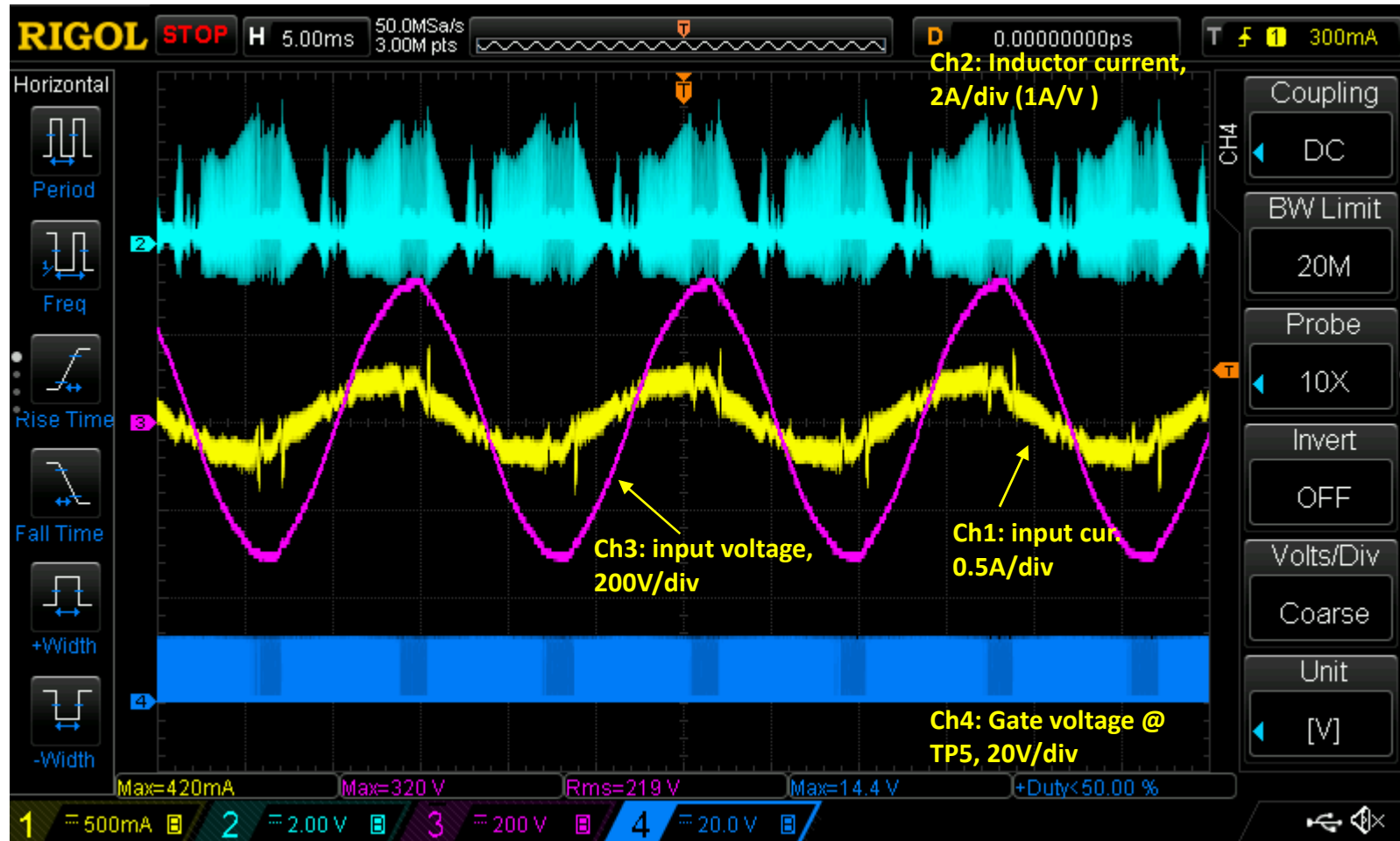


Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5
Original boost inductor (680uH), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

Distortions start at around 220Vac



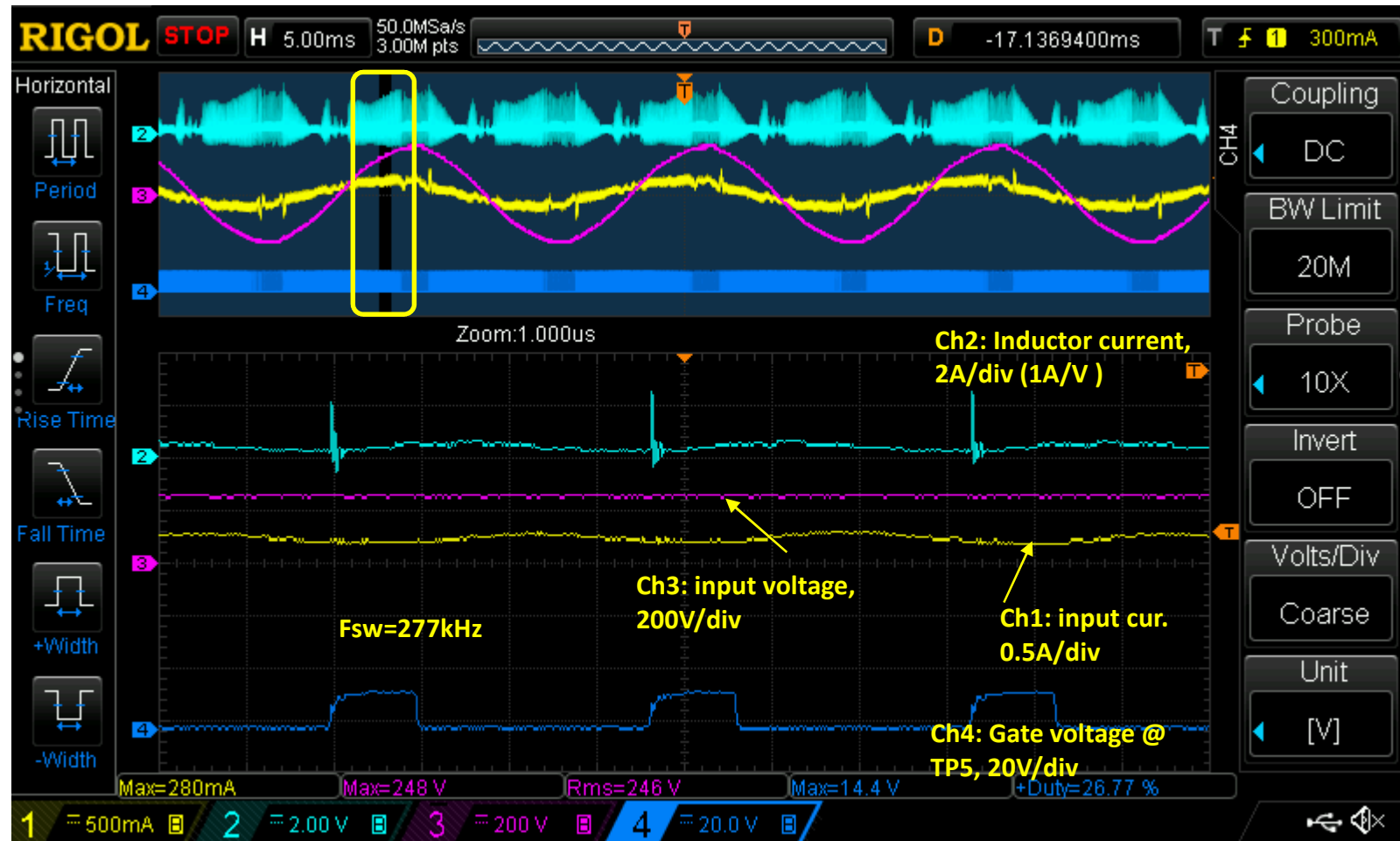
Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5

Original boost inductor (680uH), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

Distortions start at around 220Vac; zooming into time just before input voltage reaches its peak region

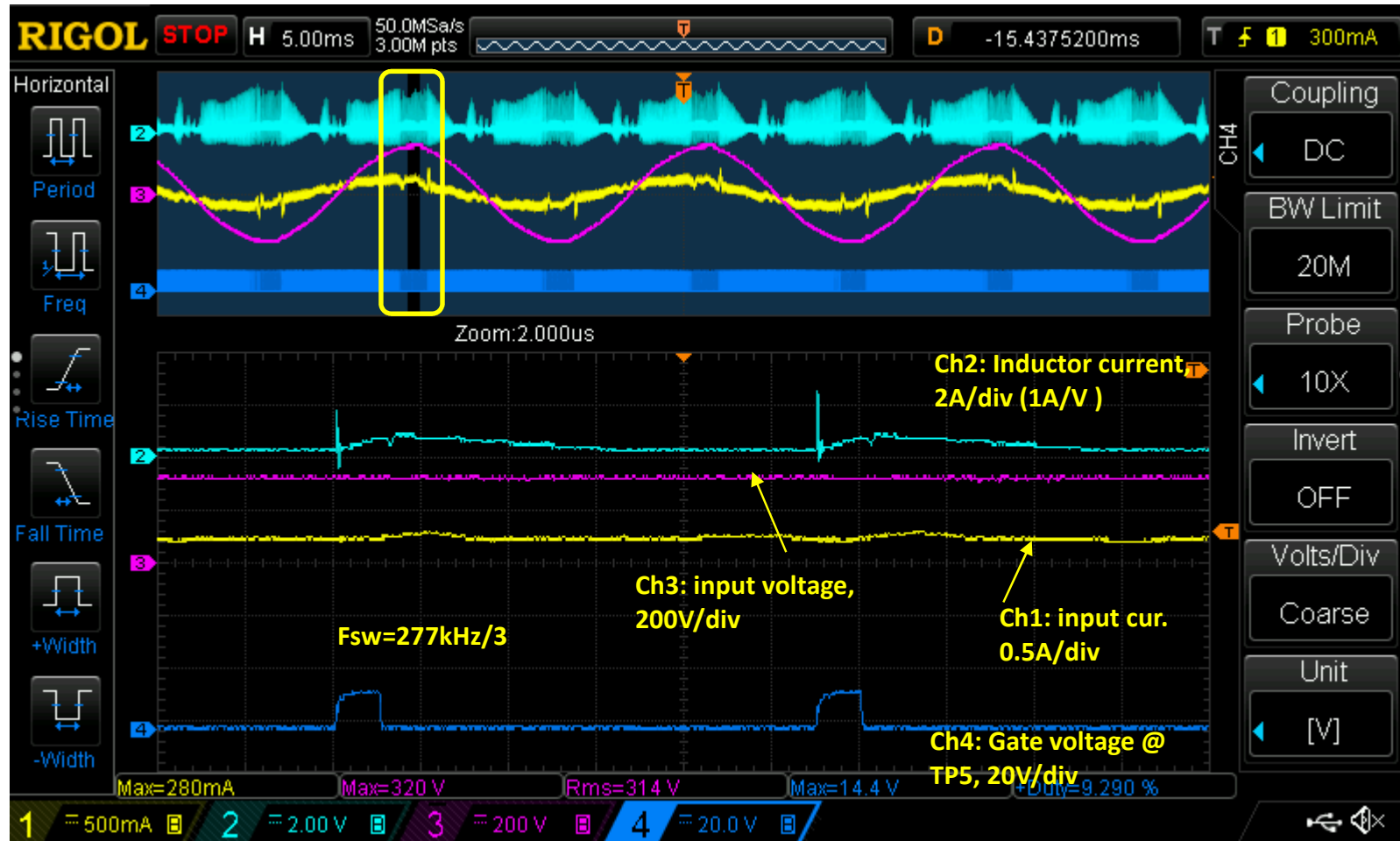


Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5
Original boost inductor (680uH), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

Distortions start at around 220Vac; zooming into the region where input voltage is at its peak
Notice that the switching frequency is reduced to 1/3rd



Measuring inductor current vs gate voltages, 2/5/2023

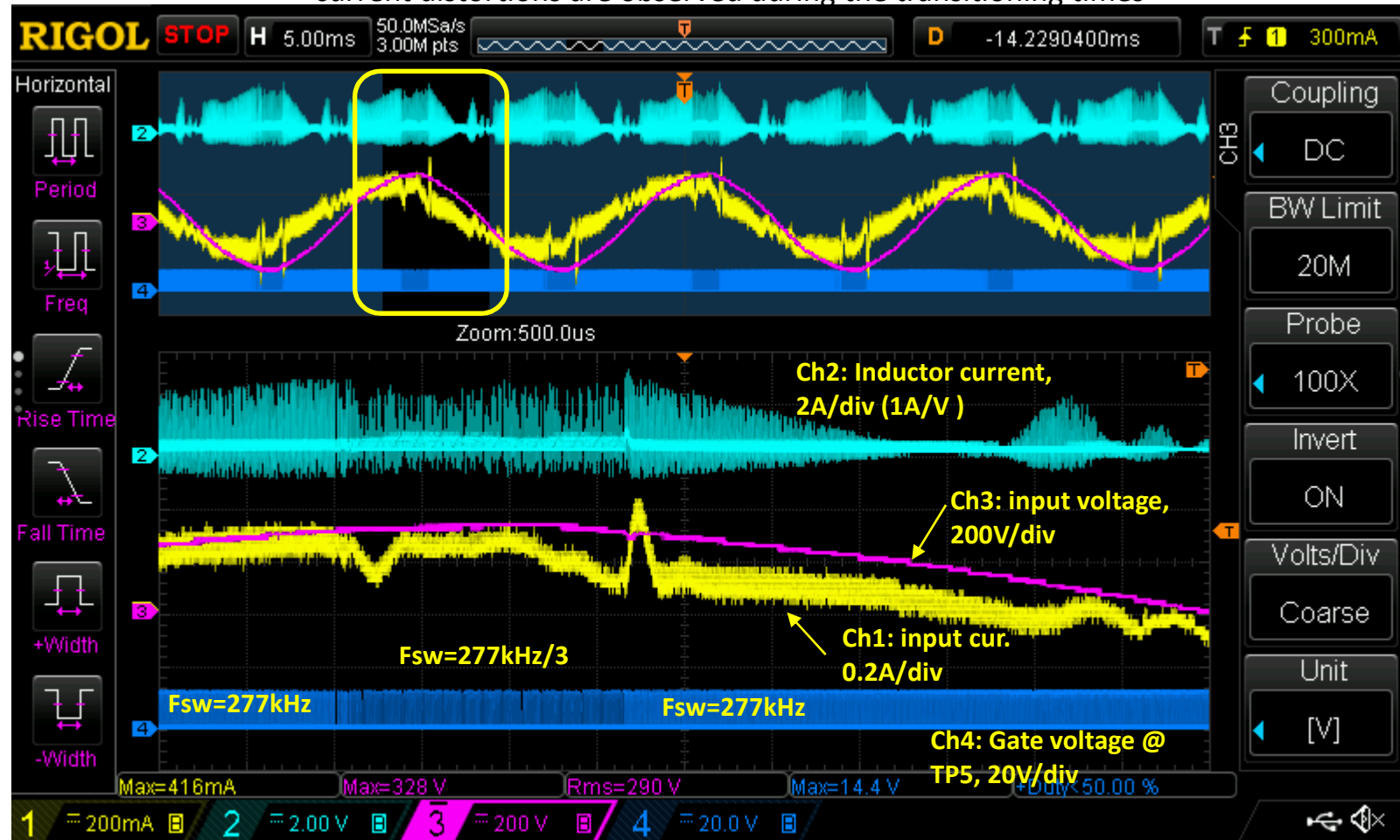
PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5

Original boost inductor (680uH), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

Distortions start at around 220Vac; zooming into the region where input voltage is at its peak

Notice that the switching frequency is reduced to $1/3^{\text{rd}}$ at the peak of input voltage and then changes back to F_{sw} and current distortions are observed during the transitioning times



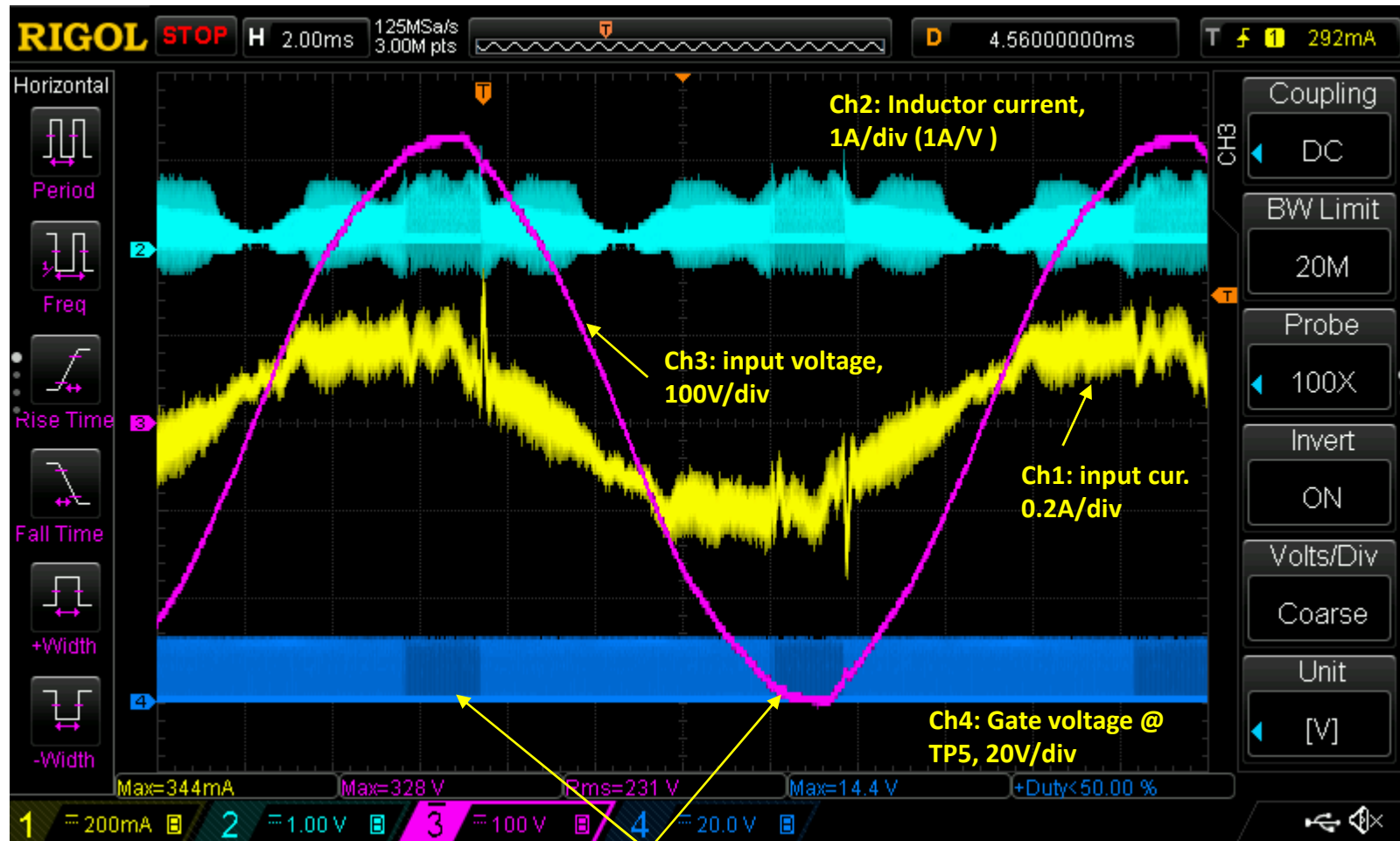
Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5

560uH toroid (2300LL-561-H-RC), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

Distortions start at around 220Vac; notice less HF cur. through inductor probably due to less inter-winding capacitance



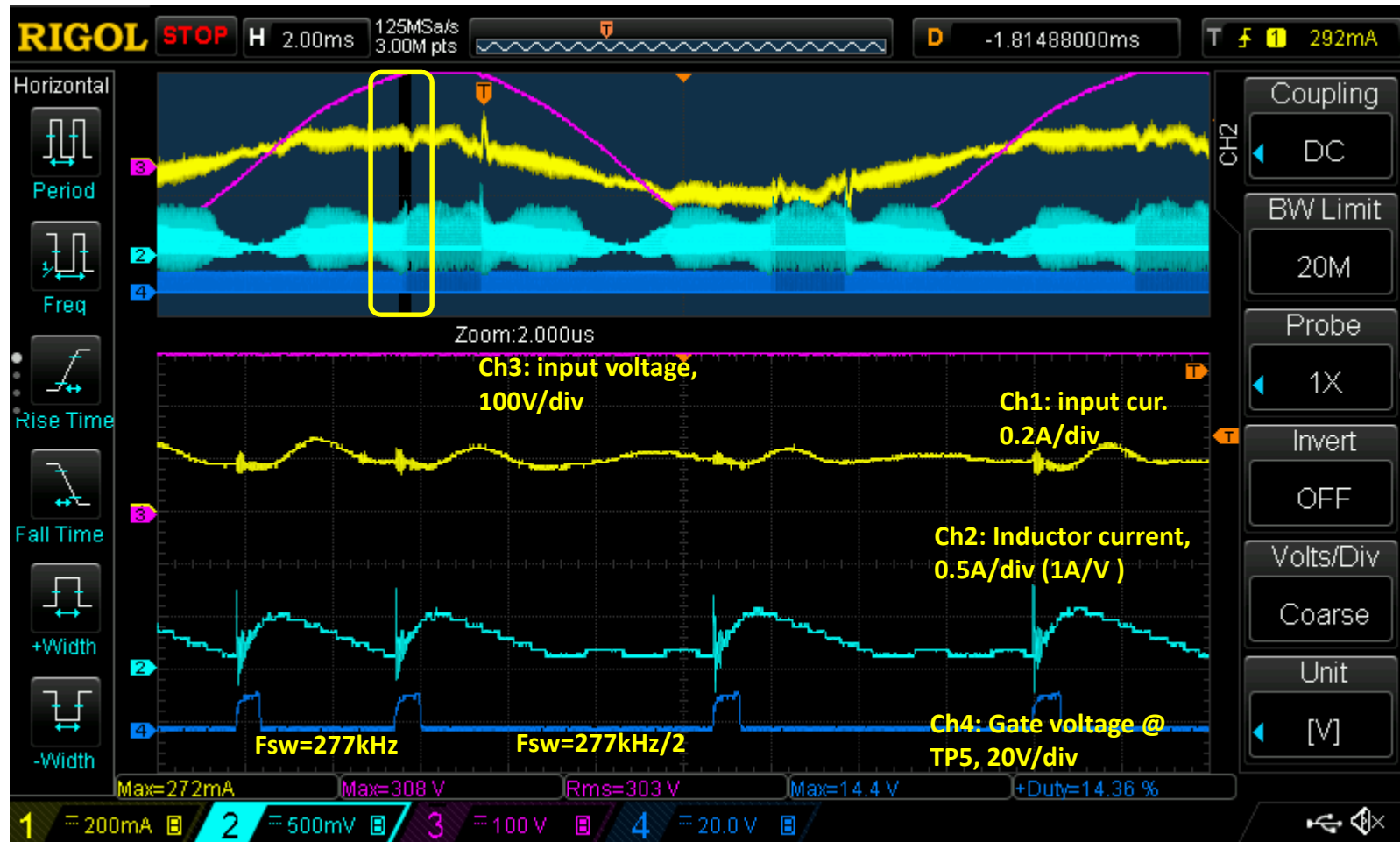
Areas of interest

Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5
560uH toroid (2300LL-561-H-RC), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

Distortions start at around 220Vac; as input voltage reaches its peak, switching frequency is reduced by half

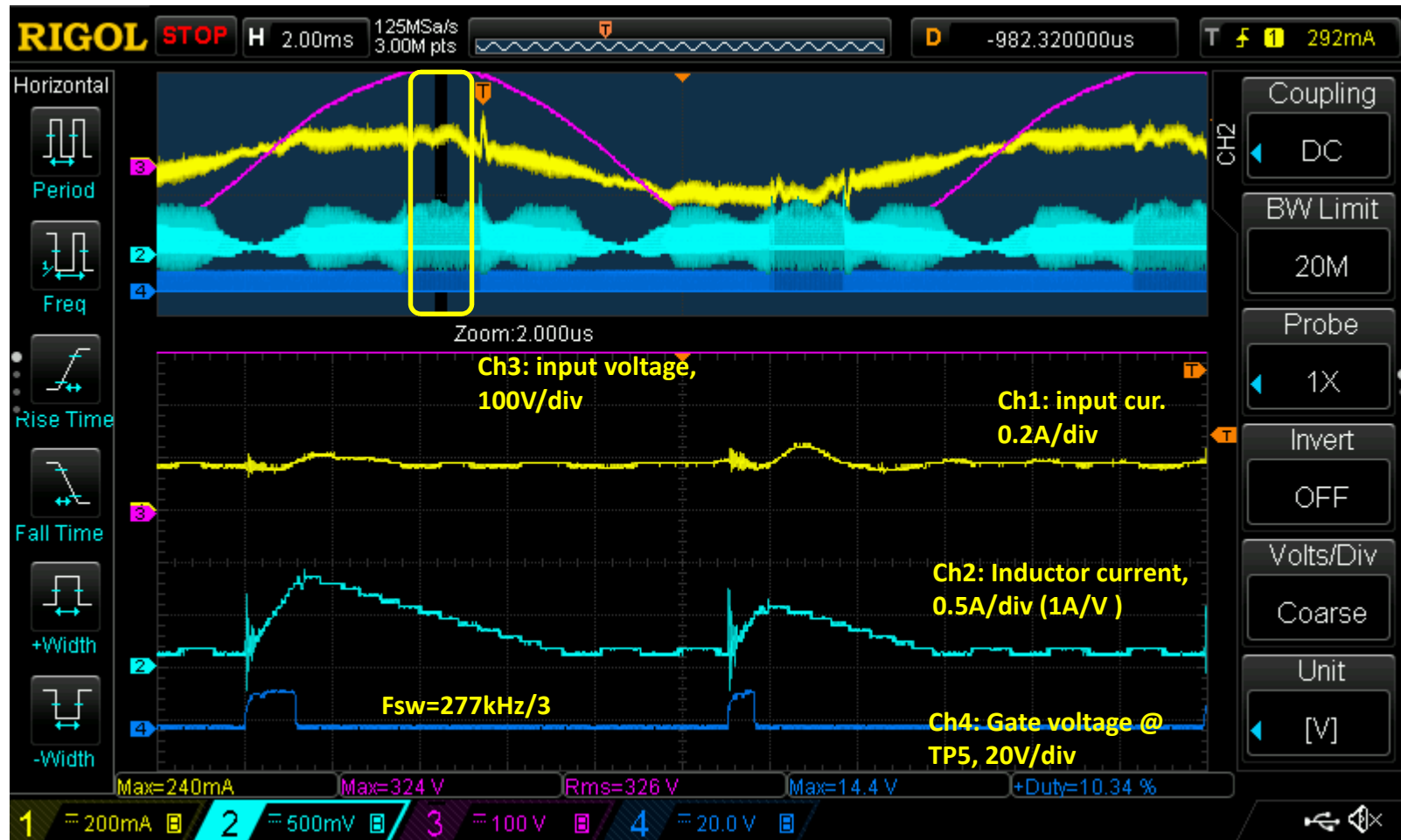


Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5
560uH toroid (2300LL-561-H-RC), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

Distortions start at around 220Vac; at the peak of input voltage , switching frequency is reduced to 1/3 rd

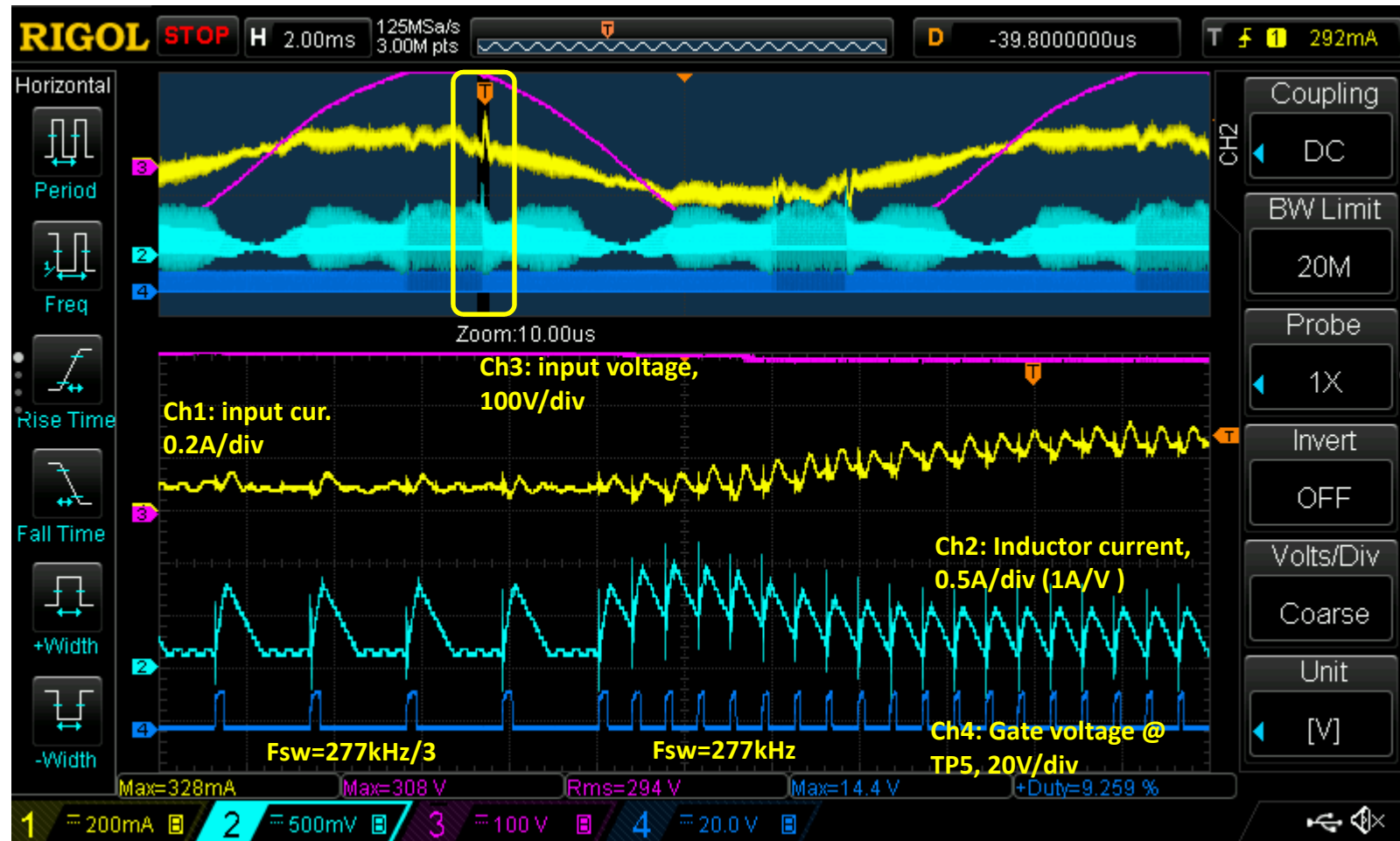


Measuring inductor current vs gate voltages, 2/5/2023

PSB-04, PFC-03, CONT A w/ PFC board ,SN: AG155544221261437

L1, Q5, Q6 on PSB removed, 25W external load across the DC-link, no capacitor across R5
560uH toroid (2300LL-561-H-RC), 10nF cap on PKLMT pin14 to GND, see slide 94 for modifications

Distortions start at around 220Vac; current distortions observed as the switching frequency changes back to 277kHz



Measuring inductor current vs gate voltages, 2/5/2023

Conclusions:

- Referencing UCC3817 design, component values are updated
- Voltage feedback circuit is disconnected from GND and connected to pin3
- Two inductors were tried: original boost inductor (probably with some inter-winding capacitance) and an other toroid inductor, presumably with less inter-winding inductance. Both resulted in similar current distortions at high supply voltages.
- In both cases, the switching frequency was reduced at the time when supply voltage approached to its peak and switching frequency increased back as supply voltage starts to decrease. Current distortions were observed during the times when switching frequency changes were observed.
- There is no specific indication that PKLMT was initiated. All tests were conducted with 10nF cap from pin14 (PKLMT) to pin11 (GND) on UCC28513