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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# **HAT2195R**

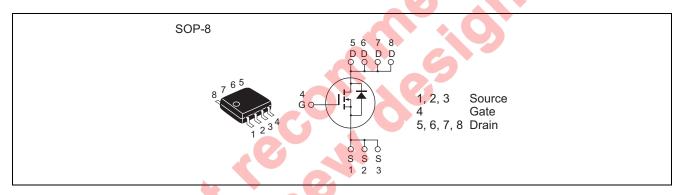
# Silicon N Channel Power MOS FET Power Switching

REJ03G0060-0300Z Rev.3.00 Apr.01.2004

#### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{DS(on)}\!=4.6~\text{m}\Omega~\text{typ.}~(\text{at}~V_{GS}=10~\text{V})$

#### **Outline**



#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	$I_D$	18	A
Drain peak current	I <sub>D(pulse)</sub> Note1	144	A
Body-drain diode reverse drain current	$I_{DR}$	18	A
Avalanche current	I <sub>AP</sub> Note 2	18	A
Avalanche energy	E <sub>AR</sub> Note 2	32.4	mJ
Channel dissipation	Pch Note3	2.5	W
Channel to ambient thermal impedance	θch-a <sup>Note3</sup>	50	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10s

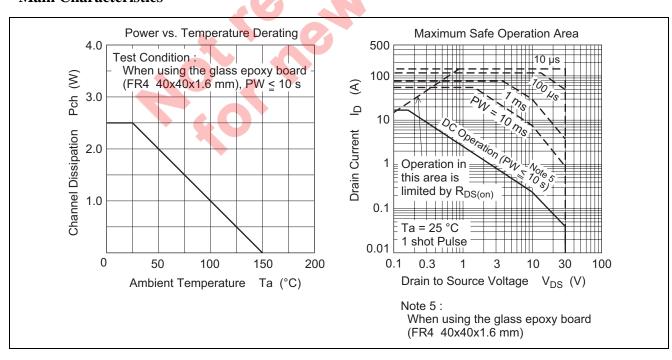
#### **Electrical Characteristics**

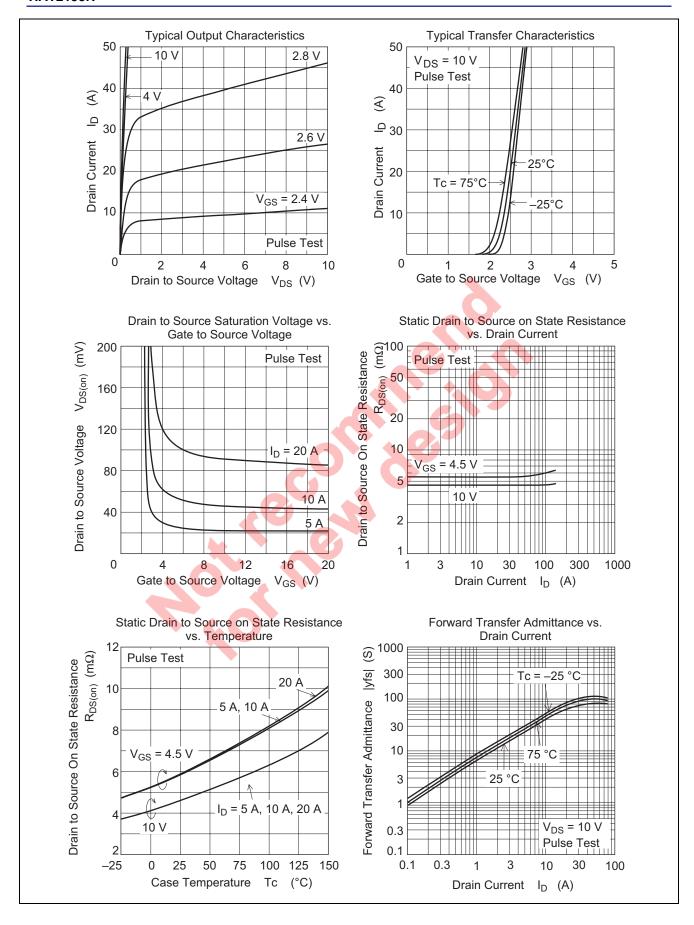
 $(Ta = 25^{\circ}C)$ 

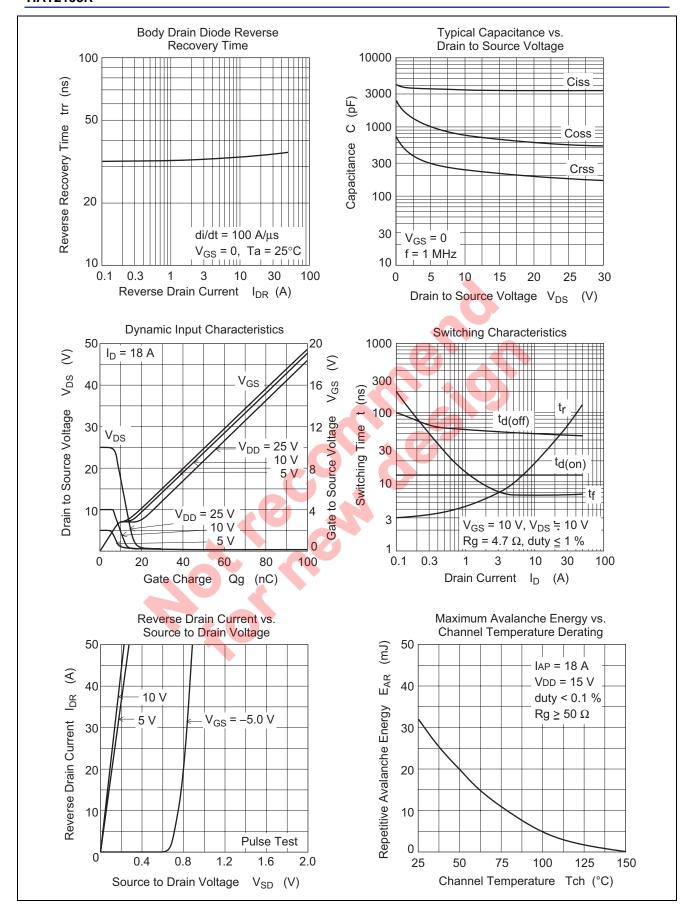
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	± 0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	4.6	5.8	mΩ	$I_D = 9 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	5.8	8.4	mΩ	$I_D = 9 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	25	42	_	S	$I_D = 9 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	3400	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	785	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	250	_	pF	f = 1 MHz
Gate Resistance	Rg	_	1.0	_	Ω	
Total gate charge	Qg	_	23	_	nC	V <sub>DD</sub> = 10 V
Gate to source charge	Qgs	_	10	_	nC	$V_{GS} = 4.5 \text{ V}$
Gate to drain charge	Qgd	_	5.5	_	nC	I <sub>D</sub> = 18 A
Turn-on delay time	t <sub>d(on)</sub>	_	12		ns	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 9 A
Rise time	t <sub>r</sub>	_	16	-73	ns	V <sub>DD</sub> ≅ 10 V
Turn-off delay time	t <sub>d(off)</sub>	_	50	A	ns	$R_L = 1.11 \Omega$
Fall time	t <sub>f</sub>	_	6.5		ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	$V_{DF}$	_	0.80	1.04	V	IF = 18 A, V <sub>GS</sub> = 0 Note4
Body-drain diode reverse recovery	t <sub>rr</sub>		32		ns	IF = 18 A, V <sub>GS</sub> = 0
time						diF/ dt = 100 A/ μs

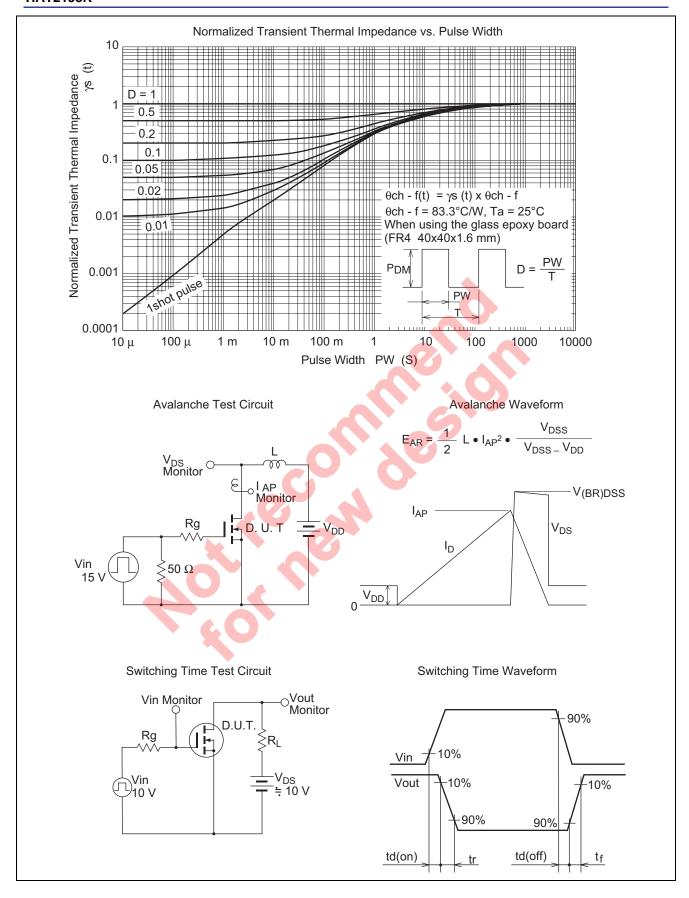
Notes: 4. Pulse test

#### **Main Characteristics**

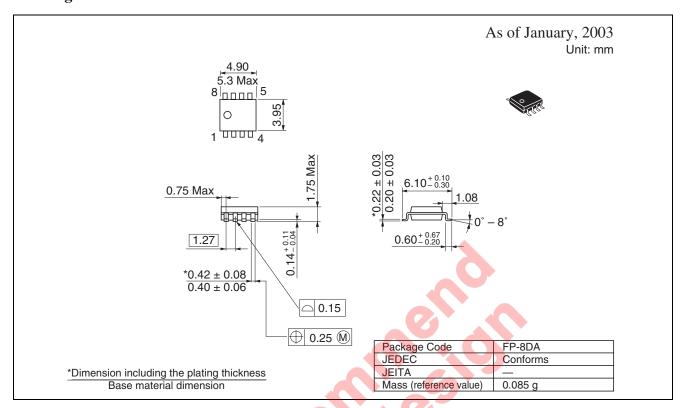








#### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2195R-EL-E	2500pcs	Taping

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