UCC28070 Controller Setup Tool	11/20/2018		
Preliminary			
Notes:			
This design tool is designed to work with the UCC28070 Appli	cation Note SLUA	479	
This design tool is optimized for a Universal Input and can be			
used for designs were the input voltage is anywhere between			
85V to 265V RMS.			
Please enter design parameters into the	shaded	cells;	
Calculated results will be in	GRAY		
Design Parameters:	Variable Names		Units
Minimum RMS Input Voltage	$V_{IN\_MIN}$	85	V
Maximum RMS Input Voltage	$V_{IN\_MAX}$	120	V
Minimum Line Frequency	f <sub>LINE</sub>	55	Hz
Maximum Line Frequency		65	Hz
Maximum Output Power	P <sub>OUT</sub>	5000	W
Full Load Efficiency (Must be less than 0.99)	η	0.95	
Switching Frequency	f <sub>S</sub>	1.00E+05	Hz
Output Voltage	V <sub>OUT</sub>	390	V
Maximum Duty Cycle	D <sub>MAX</sub>	0.94	
Soft Start Time	t <sub>SS</sub>	0.50	S
Optional Frequency Dither Magnitude (Total ∆fs)	f <sub>DM</sub>	3.00E+04	Hz
Optional Frequency Dither Rate	$f_{DR}$	1.00E+04	Hz
VCC	VCC	12	V
Component Selection, Trip Points and Calculated Values			
from the UCC28070 Design Example	D	0.69	
Duty Cycle at the Peak the Peak of Low Line Input	D <sub>PLL</sub>		
Inductor Ripple Current Cancellation at the Peak of Low Line	K(D <sub>PLL</sub> )	0.55	
Inductor Ripple Current	$\Delta I_{L1}$	47.38	
Calculated Boost Inductors L <sub>1_MIN</sub> , L <sub>2_MIN</sub>	L <sub>1_MIN</sub> , L <sub>2_MIN</sub>	1.76E-05	Н
Enter the Lowest Inductance Value of the Real Inductor	L <sub>1_MIN</sub> , L <sub>2_MIN</sub>	1.80E-05	Н
Enter the Highest Inductance Value of the Real Inductor	$L_{1\_MAX}$ , $L_{2\_MAX}$	2.00E-05	Н
Average Inductance Value	L <sub>1_AVG</sub> , L <sub>2_AVG</sub>	1.90E-05	Н
Inductor RMS Current	I <sub>L1_RMS</sub> , I <sub>L2_RMS</sub>	32.563	Α
Output Capacitance Calculated Based on Holdup Time	C <sub>OUT</sub>	2.73E-03	F
Output Capacitance Selected	C <sub>OUT</sub>	3.28E-03	F
Output Ripple Voltage	$V_{RIPPLE}$	11.9	V
Low Frequency Output Capacitor RMS Current	I <sub>COUT_LF</sub>	9.543	Α
High Frequency Output Capacitor RMS Current	I <sub>COUT_HF</sub>	25.660	А
Peak Diode and FET Current	I <sub>PEAK</sub>	80.969	А
FET RMS Current (Q1 and Q2)	I <sub>DS</sub>	26.603	Α
Diode Average Current (D1 and D2)	I <sub>D</sub>	6.410	Α
Calculated Current Sense Transformer Turns Ratio	$N_{CT}=N_S/N_P$	810	
Enter Current Sense Transformer Turns Ratio	N <sub>CT</sub> =N <sub>S</sub> /N <sub>P</sub>	200	

Minimum Magnetizing Inductance of the Current Sense Transfo	rm L <sub>M</sub>	3.16E-03	Н
Select Current Sense Peak Voltage	Vs	3.00	V
Calculated Current Sense Resistor	$R_{SA} = R_{SB}$	6.7	ohm
Select Standard Current Sense Resistor	$R_{SA} = R_{SB}$	6.8	ohm
CalculatedReset Resistor	$R_R$	1.E+02	ohm
Select a Standard Value	$R_R$	1.00E+02	ohm
Calculated Maximum DR Reverse Voltage	$V_R$	40	V
Current Sense Offset Desired	$V_{OFF}$	0.20	V
Program Offset Bias Current to be added to R <sub>S</sub>	R <sub>OA</sub> =R <sub>OB</sub>	4.01E+02	ohm
Select a Standard Value	R <sub>OA</sub> =R <sub>OB</sub>	4.00E+02	ohm
Program Current Sense PWM Ramp Resistor	$R_{TA}=R_{TB}$	8.50E+02	ohm
Select Standard Values	$R_{TA}=R_{TB}$	8.45E+02	ohm
Program Current Sense PWM Ramp Timing Capacitor	$C_{TA} = C_{TB}$	4.90E-07	F
Select Standard Values	C <sub>TA</sub> =C <sub>TB</sub>	4.70E-07	F
Select High Side Resistor on Peak Current Limit Divider	R <sub>PK1</sub>	6.80E+03	ohm
Calculated Low Side Resistor on Peak Current Limit Divider	R <sub>PK2</sub>	6.80E+03	ohm
Select Low Side Resistor on Peak Current Limit Divider	R <sub>PK2</sub>	6.80E+03	ohm
Calculated Timing Resistor	R <sub>RT</sub>	7.50E+04	ohm
Select Timing Resistor	R <sub>RT</sub>	7.50E+04	ohm
Calculated Programmable Duty Cycle Limit Resistor	R <sub>DMX</sub>	6.60E+04	ohm
Select Programmable Duty Cycle Limit Resistor	R <sub>DMX</sub>	6.65E+04	ohm
Select High Side Resistor for VSENSE Voltage Divider	R <sub>A</sub>	3.00E+06	ohm
Calculated Low Side Resistor on VSENSE voltage Divider	R <sub>B</sub>	2.33E+04	ohm
Select Low Side Resistor on VSENSE voltage Divider	R <sub>B</sub>	2.37E+04	ohm
Calculated Nominal Over Voltage Trip Point	V <sub>OVP</sub>	406	V
Voltage Divider Gain	H	7.84E-03	
Voltage Amplifier Output Impedance at double f <sub>LINE</sub>	Z <sub>O</sub>	1.47E+04	ohm
Calculated Pole Capacitance for the Voltage Loop	$C_{PV}$	9.85E-08	F
Select a Standard Value	$C_{PV}$	1.00E-07	F
Calculated Voltage Loop Crossover Frequency	$f_{VC}$	13.2	Hz
Calculated Voltage Loop Zero Compensation Resistor	$R_{ZV}$	1.20E+05	ohm
Select a Standard Value	$R_{ZV}$	1.20E+05	ohm
Calculated Voltage Loop Zero Compensation Capacitor	$C_{ZV}$	1.00E-06	F
Select a Standard Value	$C_{ZV}$	1.00E-06	F
Calculated Current Synthesis Programmable Resistor	R <sub>SYN</sub>	4.61E+04	ohm
Select a Standard Value	R <sub>SYN</sub>	4.60E+04	ohm
Voltage Calculation for Selecting Multiplier Resistor	V <sub>1</sub>	68.563	V
Voltage Calculation for Selecting Multiplier Resistor	$V_2$	2.030	V
Multiplier Resistor	R <sub>IMO</sub>	1.56E+04	ohm
Select a Standard Value	R <sub>IMO</sub>	1.56E+04	ohm
Current Loop Power Stage Gain at Loop Crossover	G <sub>PSC</sub>	2.777	
Current Loop Zero Resistor	R <sub>ZC1</sub> =R <sub>ZC2</sub>	3.60E+03	ohm

Select a Standard Value	R <sub>ZC1</sub> =R <sub>ZC2</sub>	3.60E+03	ohm
Current Loop Zero Capacitor	C <sub>ZC1</sub> =C <sub>ZC2</sub>	4.42E-09	F
Select a Standard Value	$C_{ZC1}=C_{ZC2}$	4.30E-09	F
Current Loop Pole Capacitor	C <sub>PC1</sub> =C <sub>PC2</sub>	8.84E-10	F
Select a Standard Value	$C_{PC1}=C_{PC2}$	8.20E-10	F
Calculated Soft Start Capacitor (Be sure $C_{SS} > or = C_{ZV}$ )	C <sub>SS</sub>	2.2222E-06	F
Select a Standard Value (Be sure $C_{SS} > or = C_{ZV}$ )	C <sub>SS</sub>	2.20E-06	F
Program Dither Magnitude Resistor	R <sub>RDM</sub>	3.13E+04	ohm
Select a Standard Value	R <sub>RDM</sub>	3.12E+04	ohm
Program Dither Rate Capacitor	C <sub>CDR</sub>	2.08E-10	F
Select a Standard Value	C <sub>CDR</sub>	2.20E-07	F
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