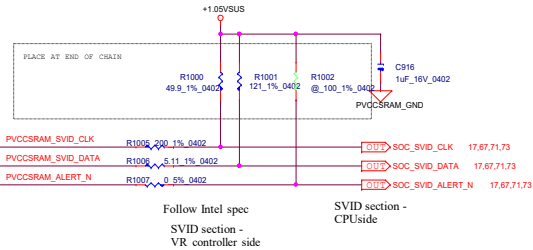
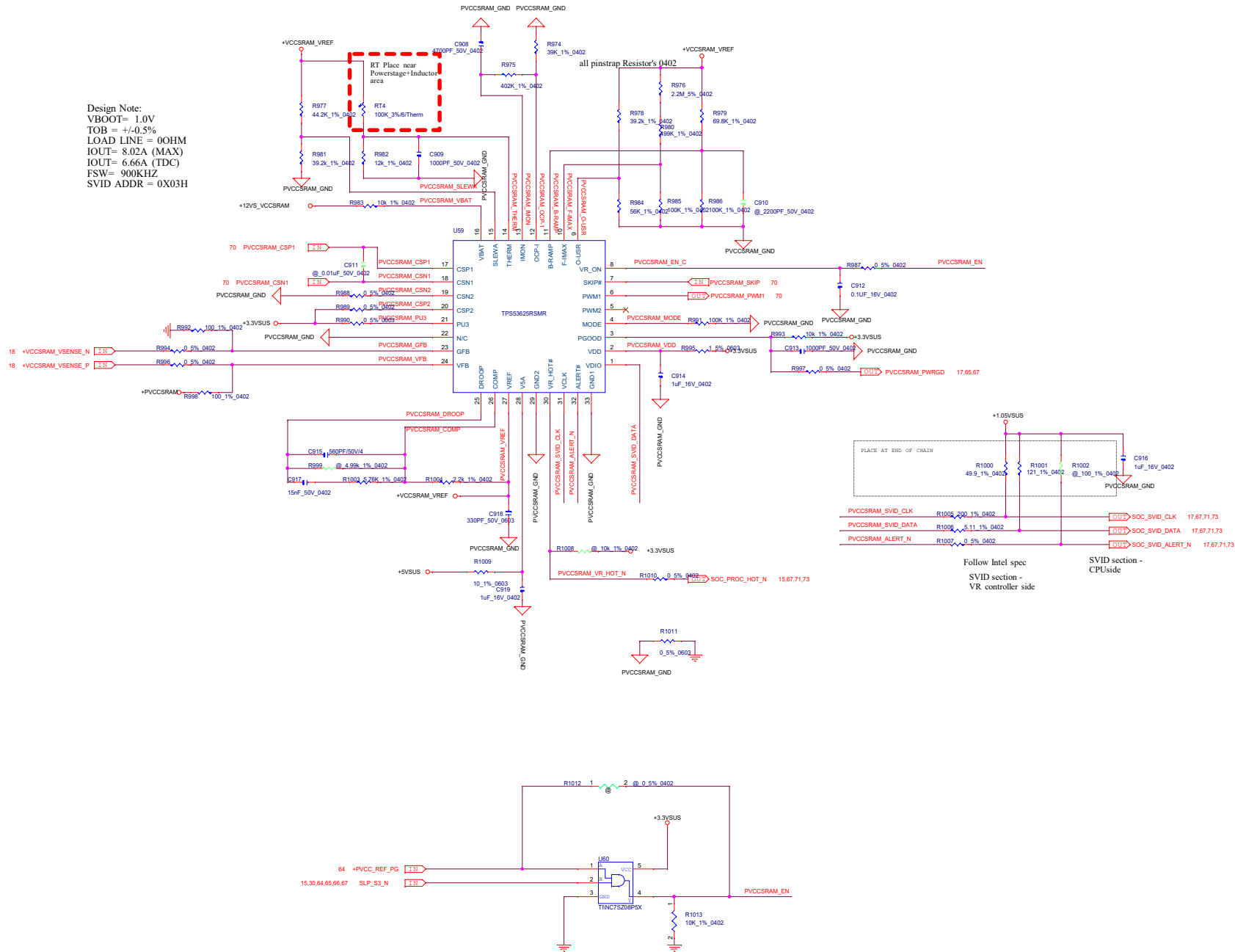
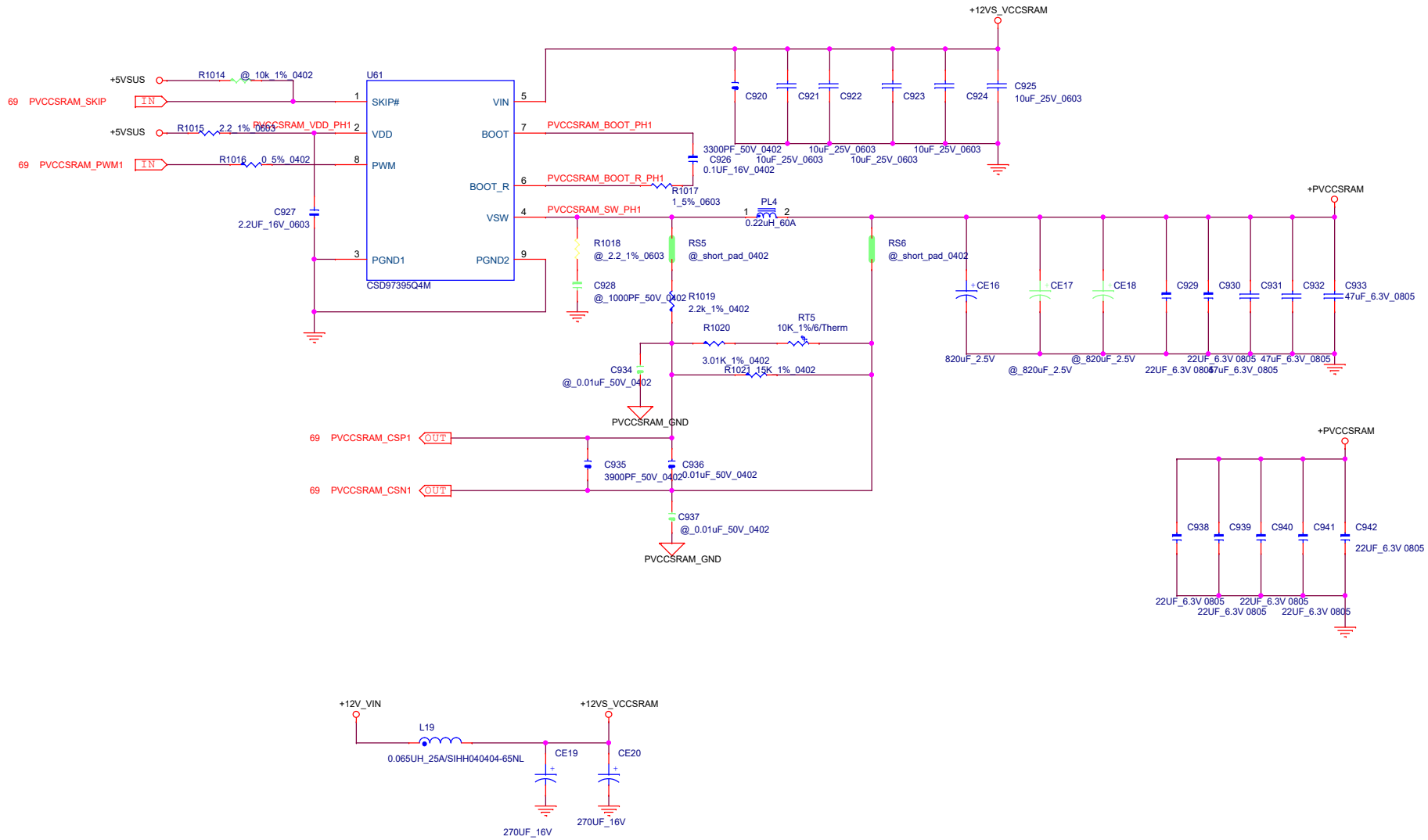


Design Note:
 VBOOT= 1.0V
 TOB = +/-0.5%
 LOAD LINE = 0OHM
 IOUT= 8.02A (MAX)
 IOUT= 6.66A (TDC)
 FSW= 900KHZ
 SVID ADDR = 0X03H

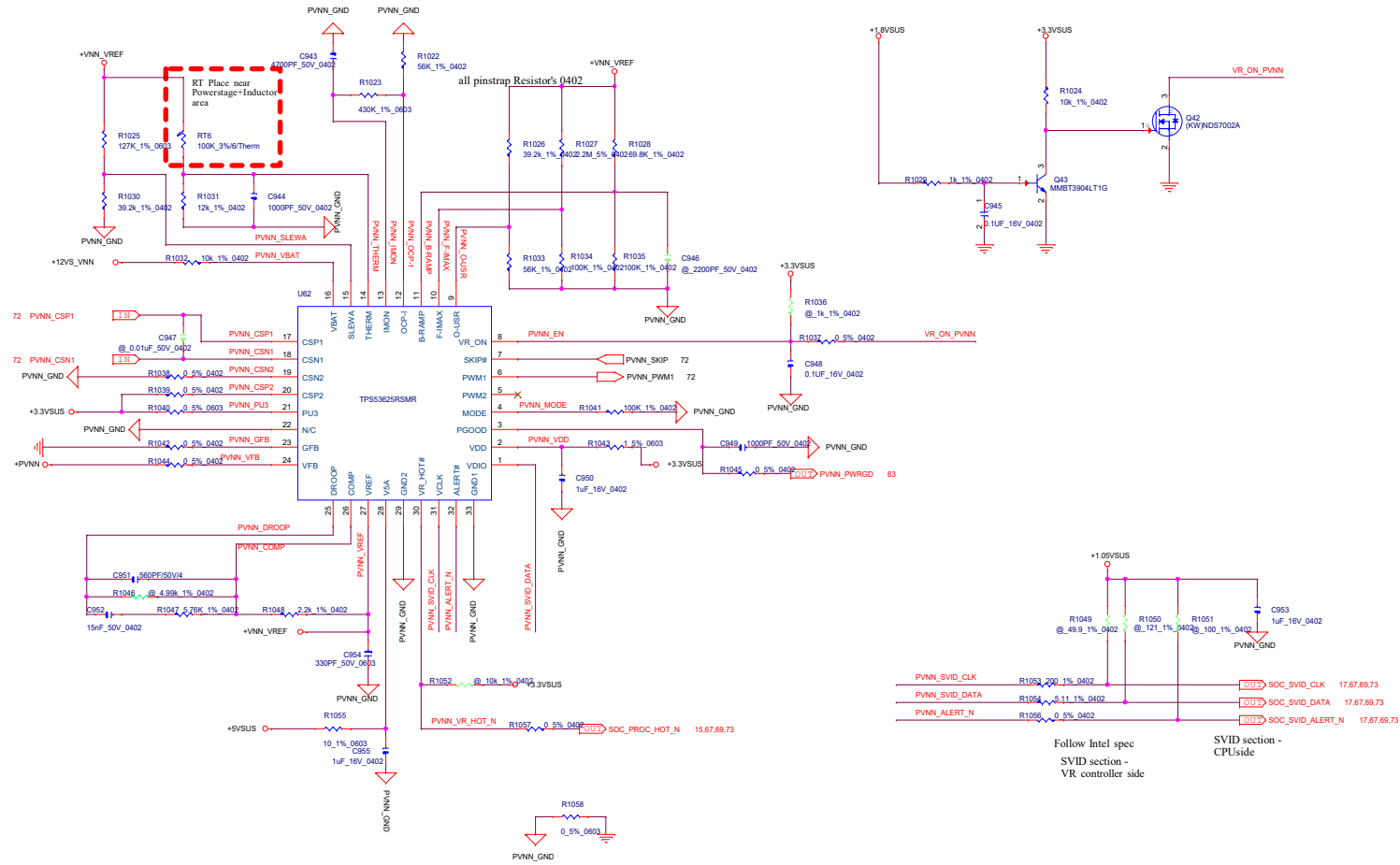


Follow intel spec
 SVID section -
 VR controller side

SVID section -
 CPUside



Design Note:
 VBOOT = 1.0V
 TOB = +/-0.5%
 LOAD LINE = 0OHM
 IOUT = 10.01A (MAX)
 IOUT = 8.31A (TDC)
 FSW = 900KHZ
 SVID_ADDR = 0X01H



Follow Intel spec
 SVID section -
 VR controller side

SVID section -
 CPUside

