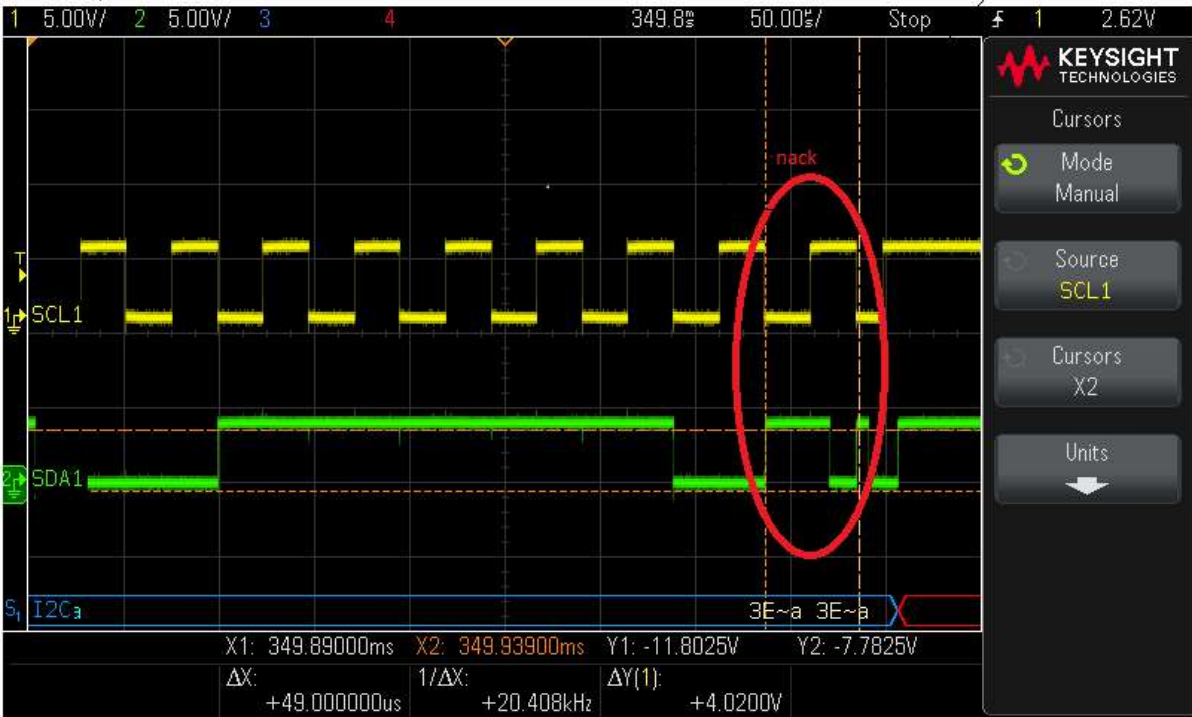
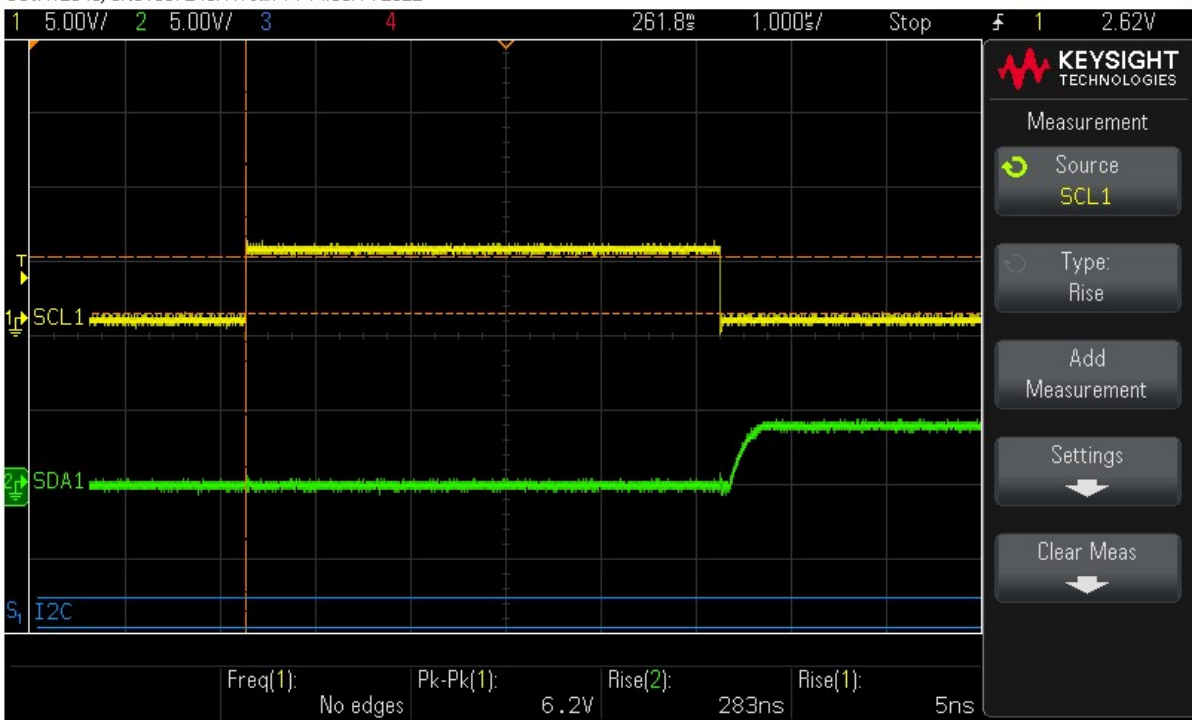


DSOX12046, CN61037243, Fri Jan 14 13:03:45 2022



**Delayed Acknowledgement from slave device at 9<sup>th</sup> clock controller considered as NACK**

DSOX12046, CN61037243, Fri Jan 14 14:38:14 2022



**Rise time of SCL is 5ns and Rise time of SDA is 283ns**