

The parts related to the question about the circuit using ICs are as follows (Q1, Q3, CBOOT1)

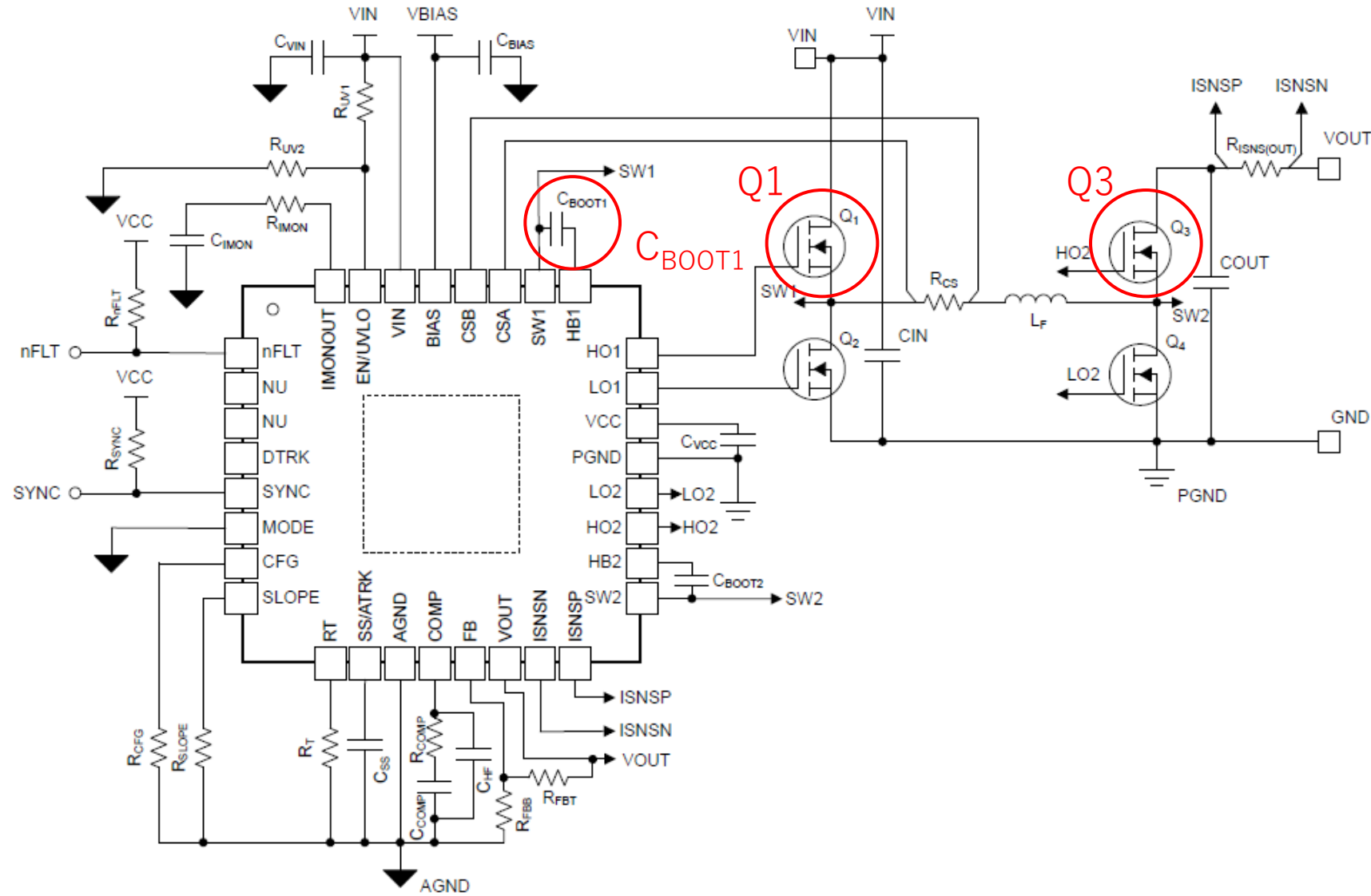


Figure 8-1. Simplified Schematic of a Typical Application

This is the gate voltage waveform when the output is shorted as described in Question 1.
The waveform on the left is a zoomed-in version of the waveform on the right.

Q3 Vgs

Q1 Vgs

