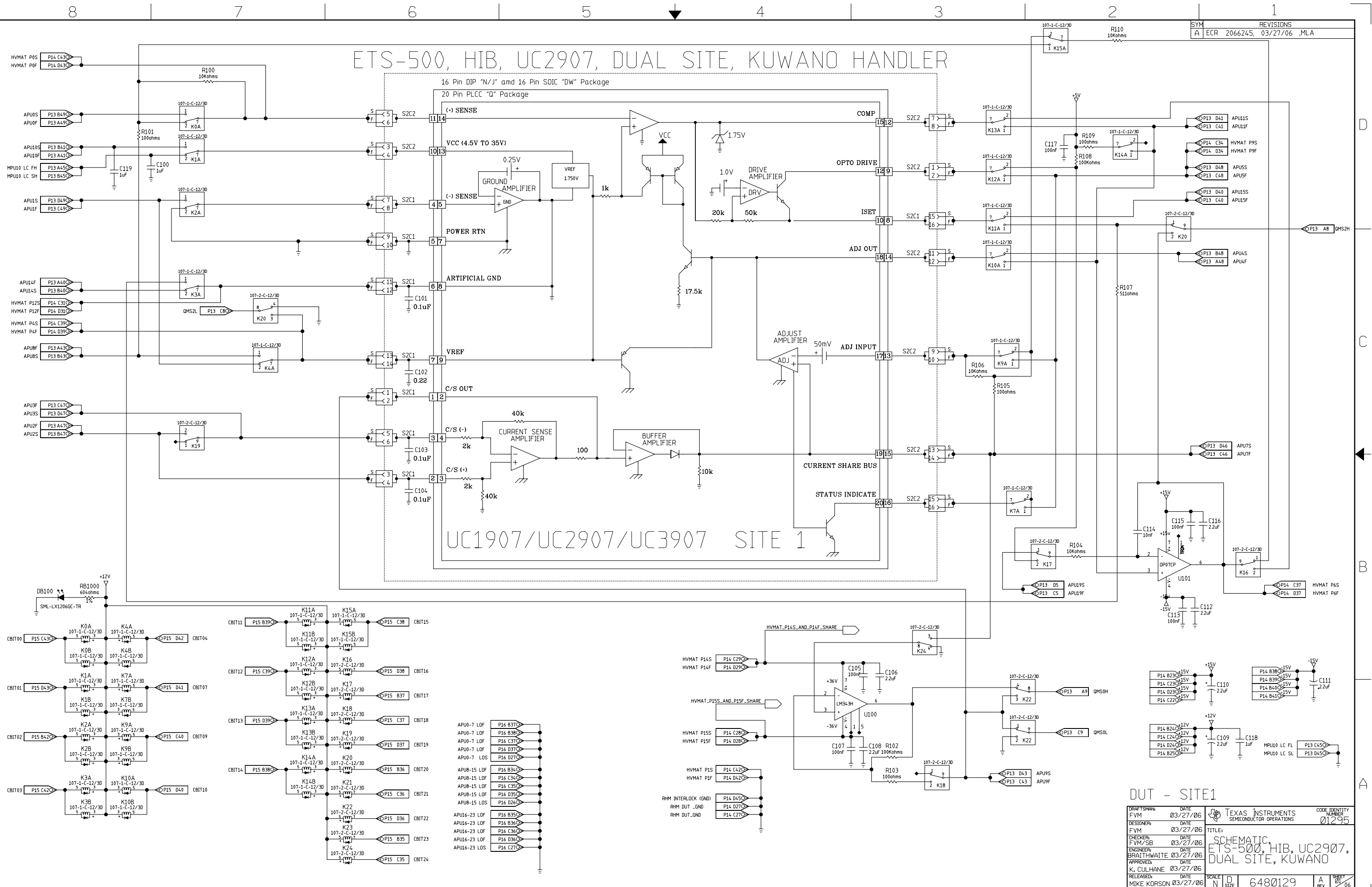
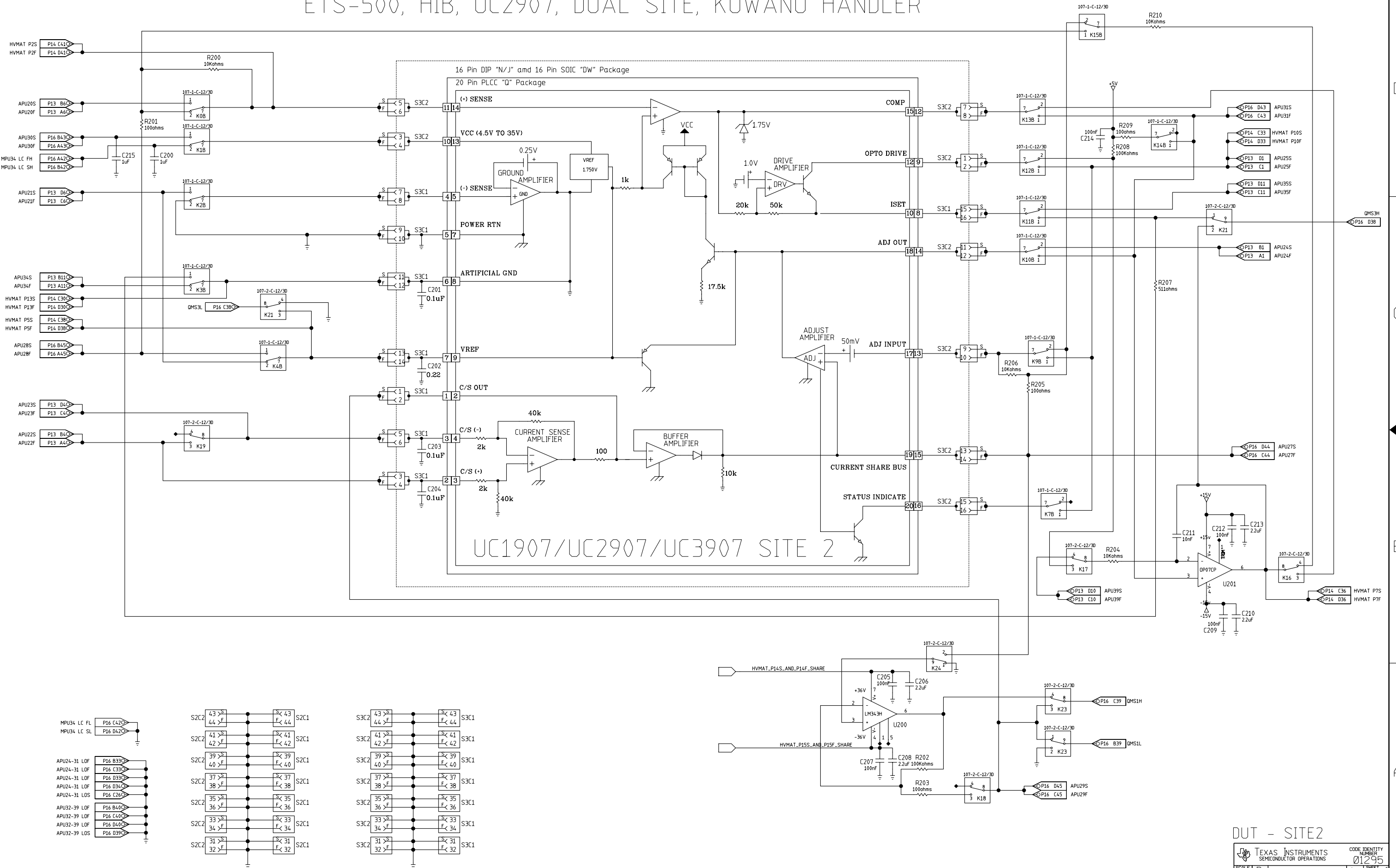




ETS-500, HIB, UC2907, DUAL SITE, KUWANO HANDLER



DUT - SITE2

THIS SIDE CURRENTLY UP IN
MALAYSIA WHEN DOCKED TO
KUWANO HANDLERS

CABLE EXITS HEAD THIS SIDE

P15

CONNECTORS MOUNTED
(ON TOP (DUT SIDE) OF PCB

DUAL/SINGLE SITE LOCATIONS
(UNUSED CONNECTORS CAN BE DELETED)

S1C2 S2C2 S3C2 S4C2

S1 S2 S3 S4

S1C1 S2C1 S3C1 S4C1

DUT PIN 1 STARTS HERE
FOR NOTCH UP TESTING

CONNECTION PIN 1

P13

DUT / HANDLER SIDE VIEW OF HIB

1/2 OF DUT PINS ON THIS CONNECTOR

DUT PINS SEQUENCED THIS DIRECTION

SITE 1
(Quad Site)

1/2 OF DUT PINS ON THIS CONNECTOR

DUT PINS SEQUENCED THIS DIRECTION

DUT PIN # 1 ALWAYS STARTS HERE

NEXT HIGHEST DUT PIN # ALWAYS IS HERE

EXAMPLE PIN #'s FOR A 16 PIN PART

SITE 2

(Quad Site)

SITE 1

(Dual Site)

SITE 1

(Single Site)

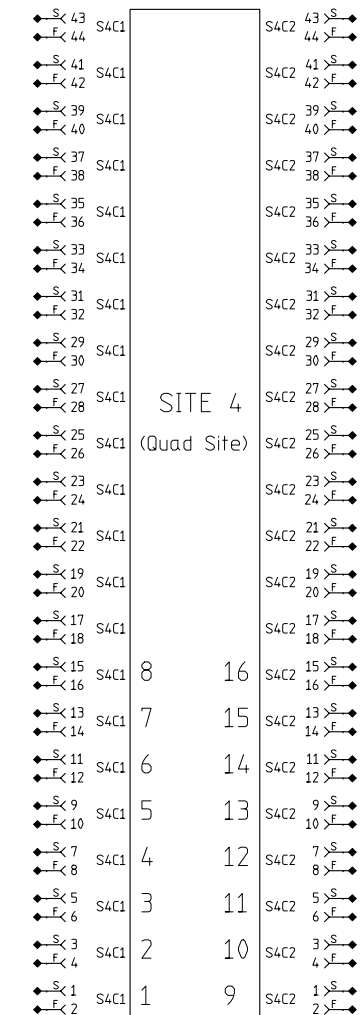
SITE 3
(Quad Site)

SITE 2
(Dual Site)

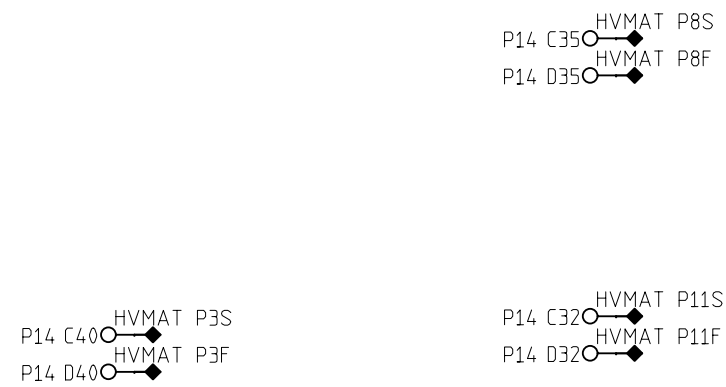
USE CONVENTION OUTLINED ON PAGE ONE TO ASSIGN DUT PINS

USE CONVENTION OUTLINED ON PAGE ONE TO ASSIGN DUT PINS

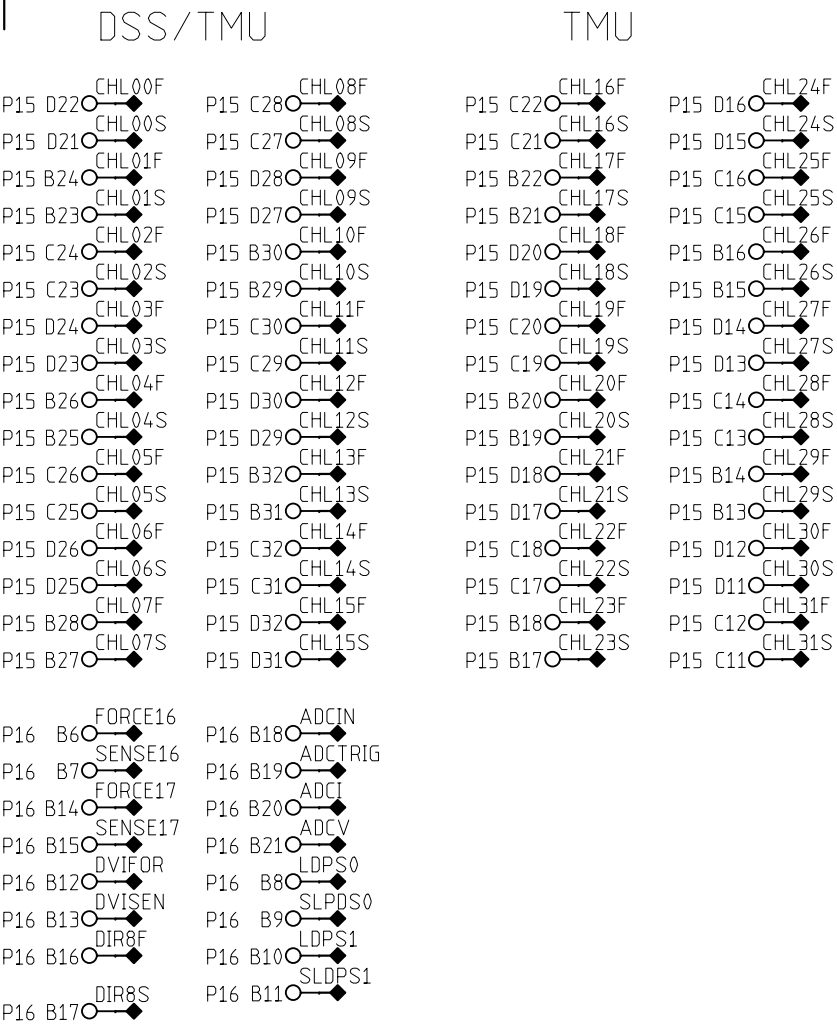
USE CONVENTION OUTLINED ON PAGE ONE TO ASSIGN DUT PINS



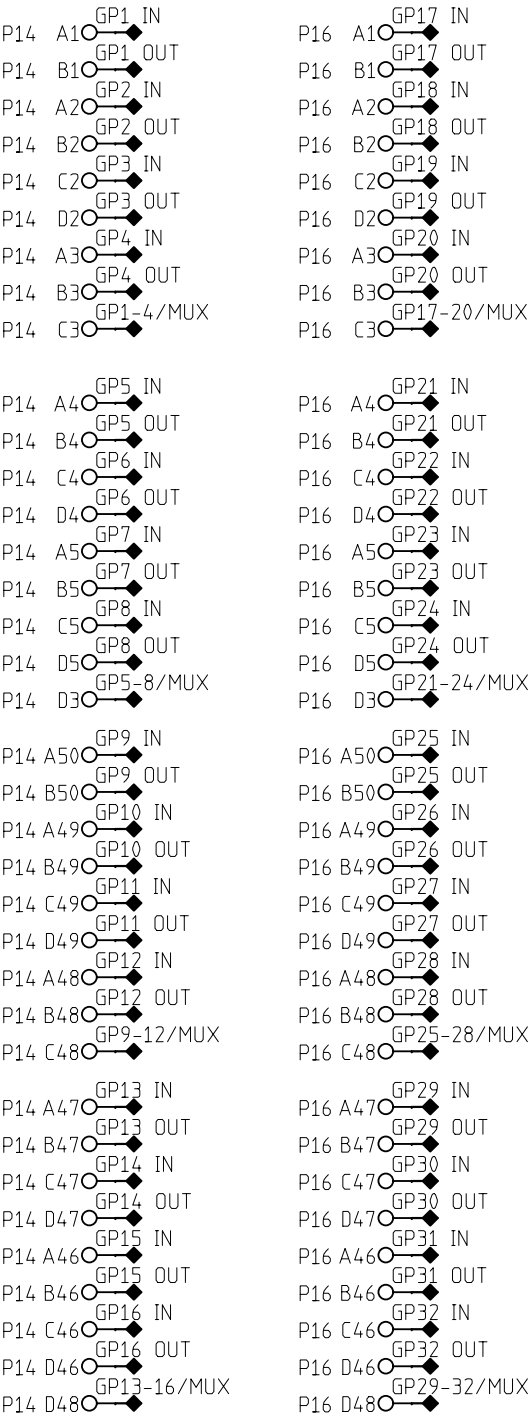
HV MATRIX PINS



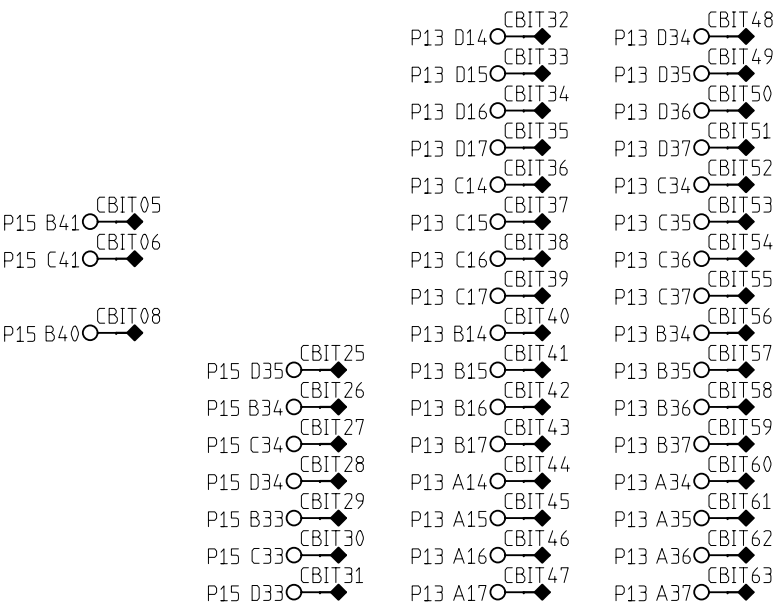
TMUX Connections



GP RELAYS

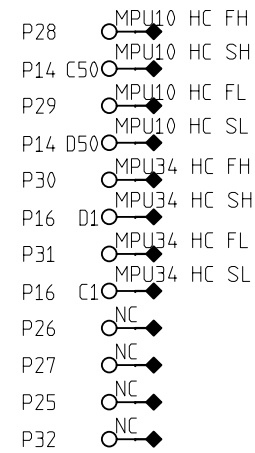


CBIT RELAY DRIVERS

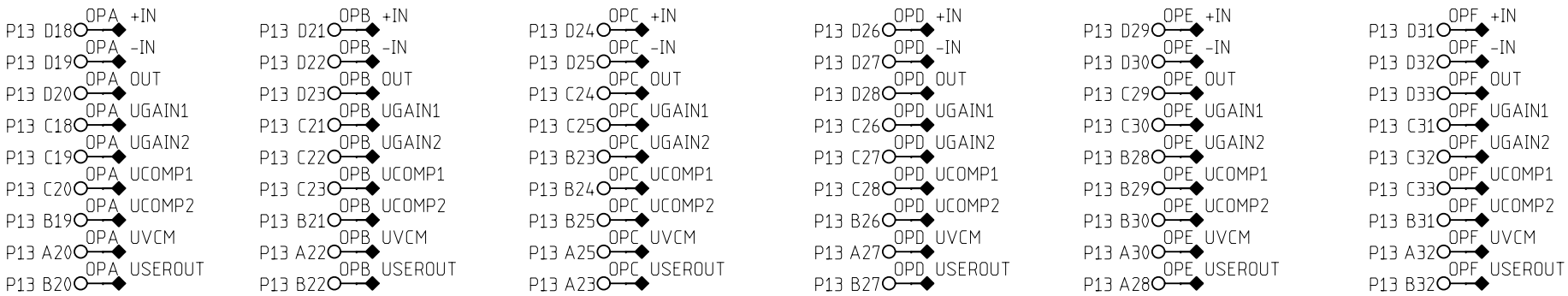


MPU

HIGH CURRENT PATH
LO CURRENT PATH

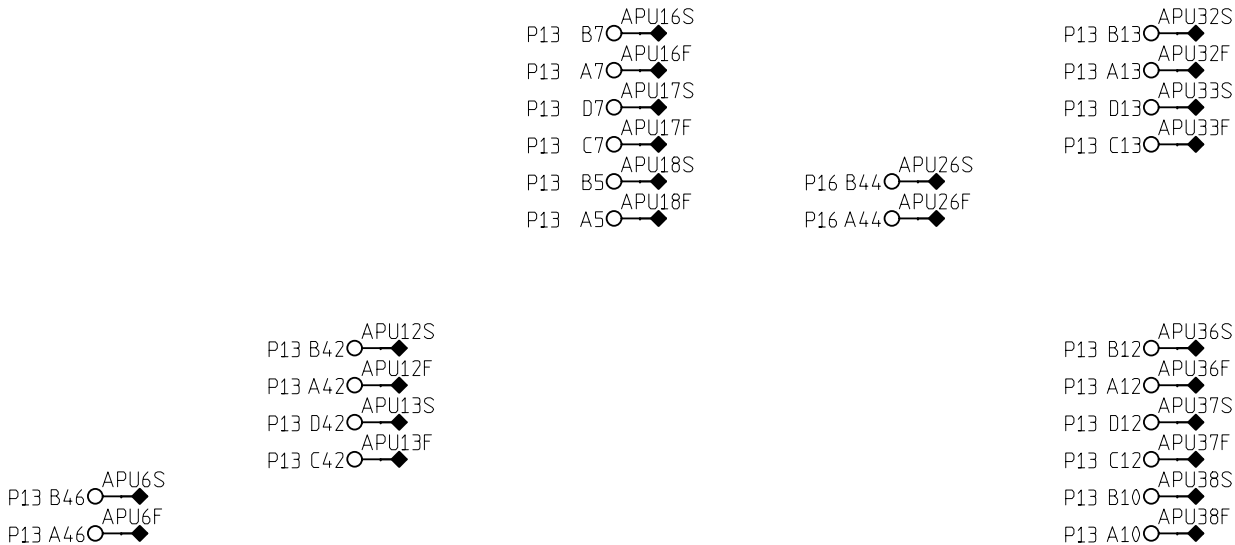


OP-AMP LOOPS

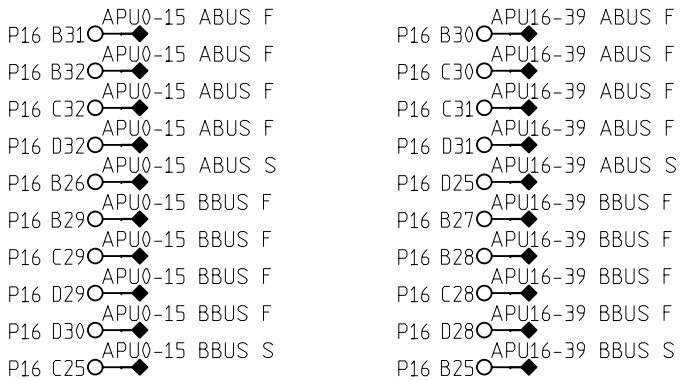


P13 B18 USER PWR

APU

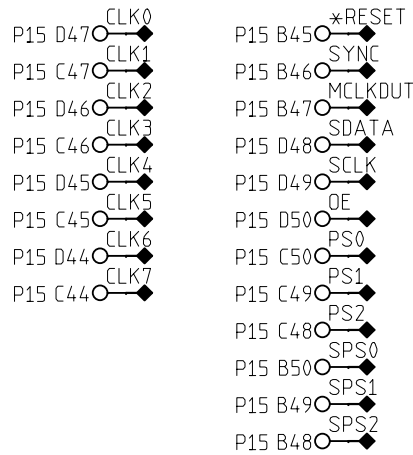


APU ABUS & BBUS CONNECTIONS



APU LO CONNECTIONS

MASTER CLOCK



QMS

WCU

