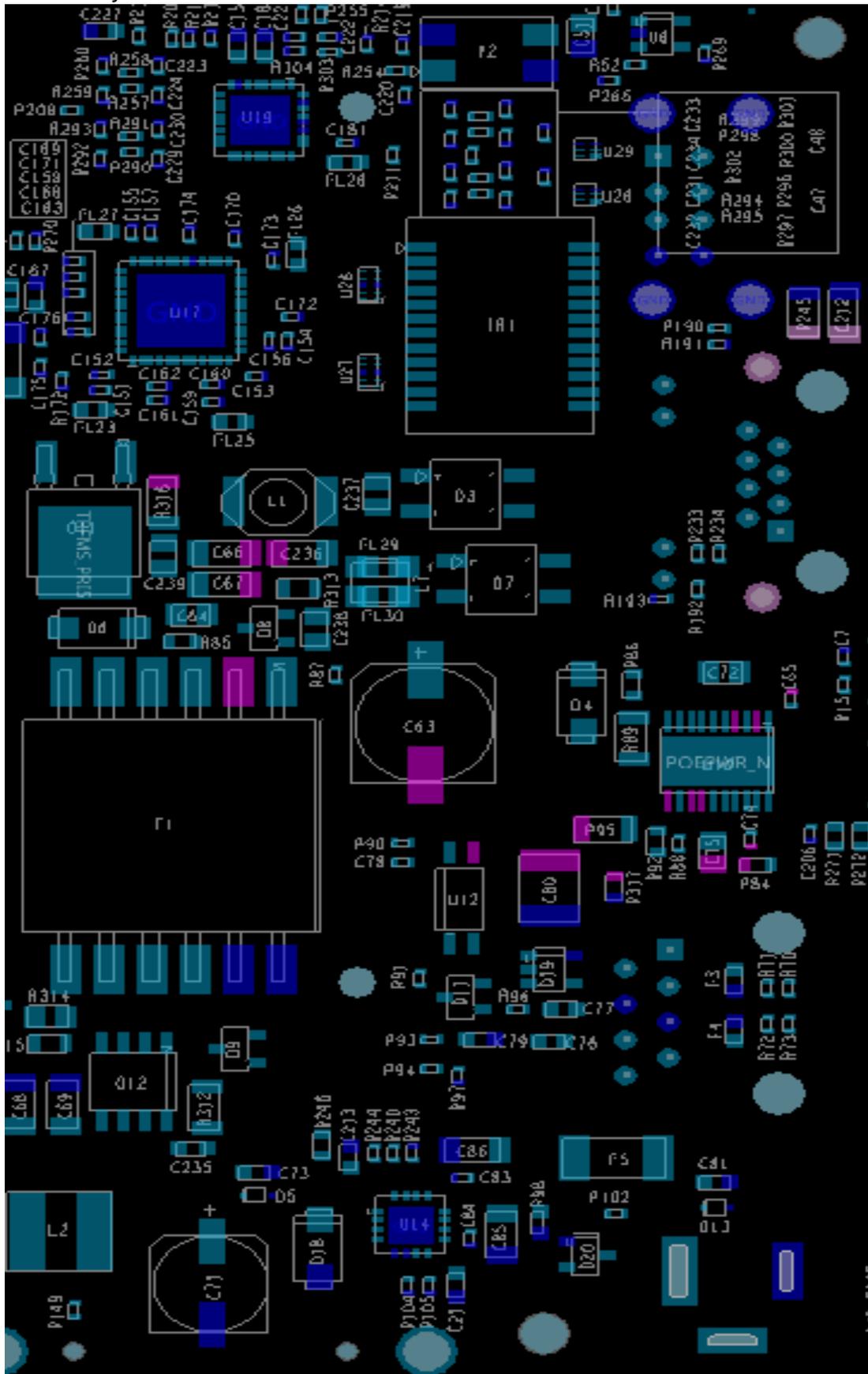
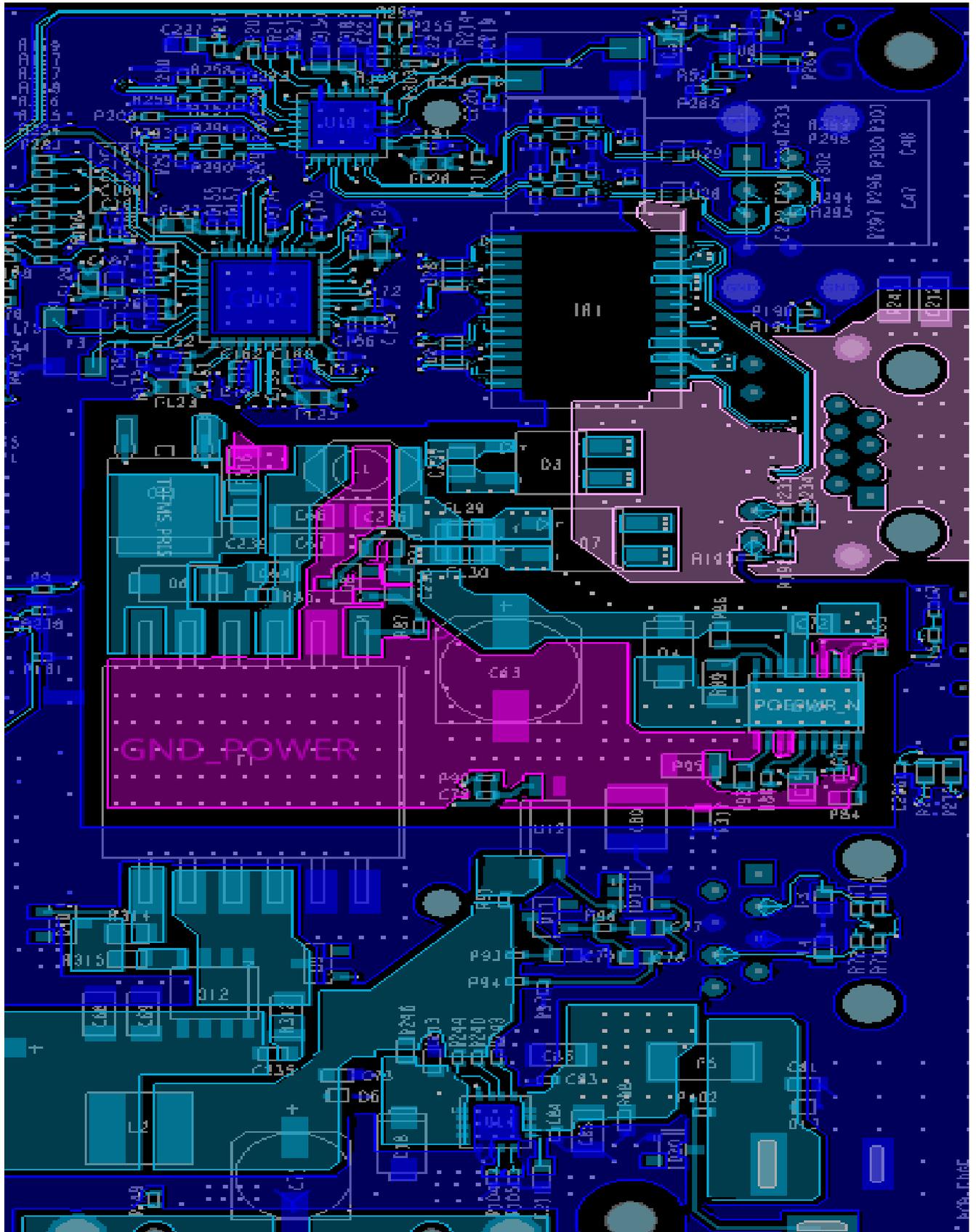


# SCH and layout details.

Bottom layer.

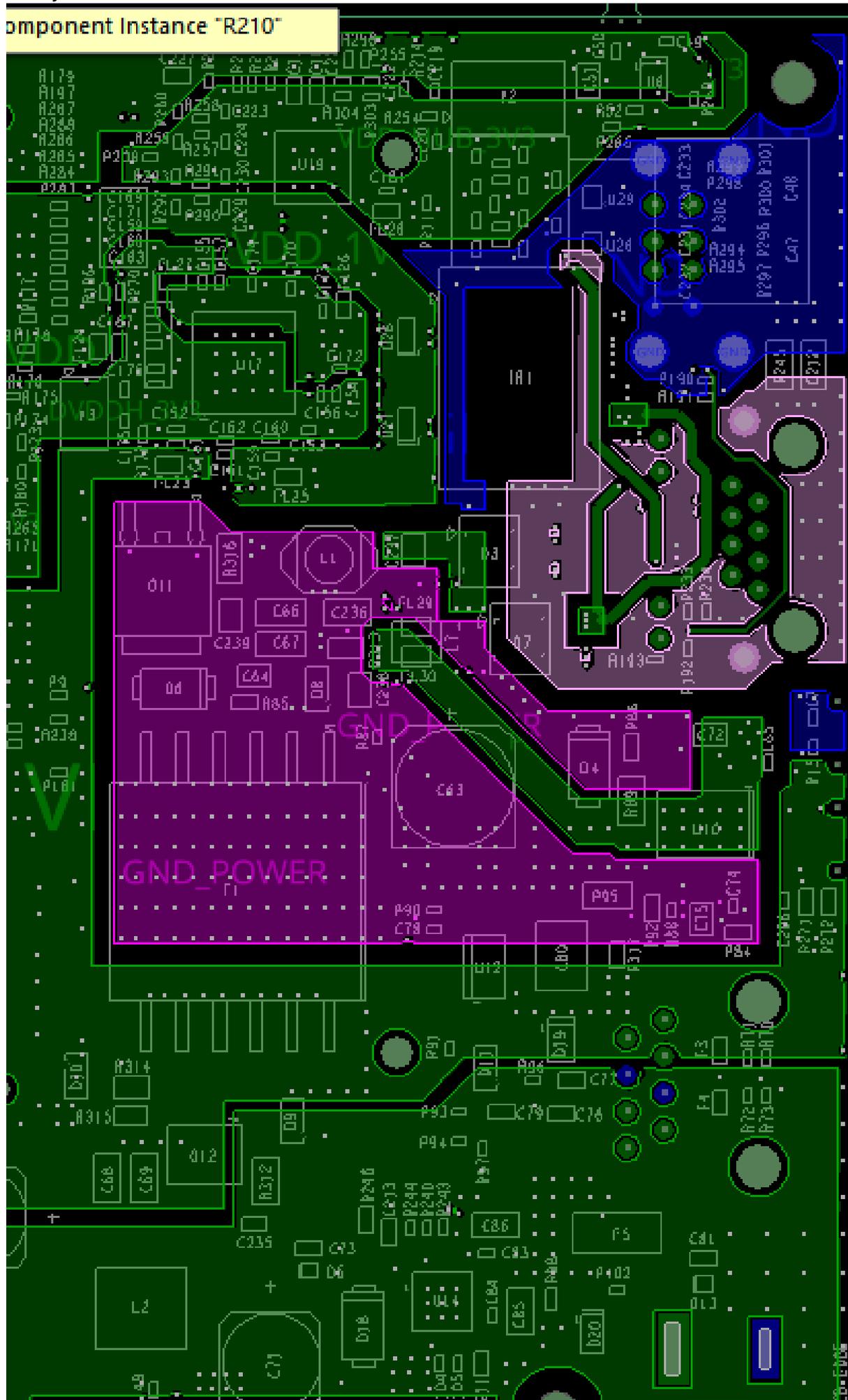


Bottom layer with signal routing and copper planes





Component Instance "R210"





L2 layer

