

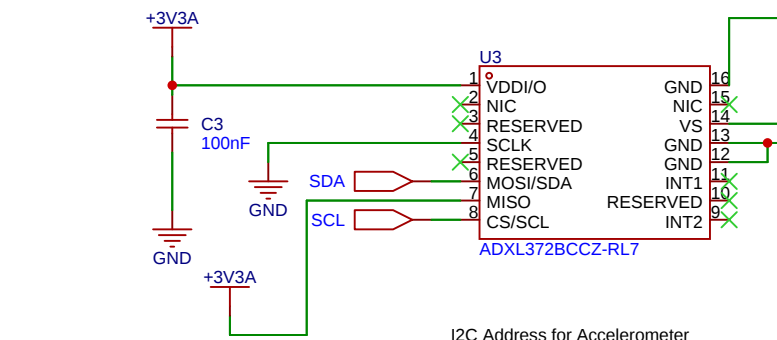
ADXL372

parameters given in Table 3 are met. There is no minimum SCL frequency, with the exception that when reading data, the clock must be fast enough to read an entire sample set before new data overwrites it. Single byte or multibyte reads/writes are supported. With the MISO pin low, the I²C address for the device is 0x1D, and an alternate I²C address of 0x53 can be chosen by pulling the MISO pin high.

There are no internal pull-up or pull-down resistors for any unused pins; therefore, there is no known state or default state for the pins if left floating or unconnected. It is a requirement that SCLK be connected to ground when communicating to the ADXL372 using the I²C.

Due to communication speed limitations, the maximum output data rate when using 400 kHz I²C is 800 Hz and scales linearly with a change in the I²C communication speed. For example, using I²C at 100 kHz limits the maximum ODR to 200 Hz. Operation at an output data rate above the recommended maximum can result in undesirable effect on the acceleration data, including missing samples or additional noise.

V_{DD I/O}



I2C Address for Accelerometer
Connected Miso pin to 3V3 to get 0X53 I2C address

on MG Board Miso Pin is conencted
to GND so we will have different I2C
adress on that Accelerometer

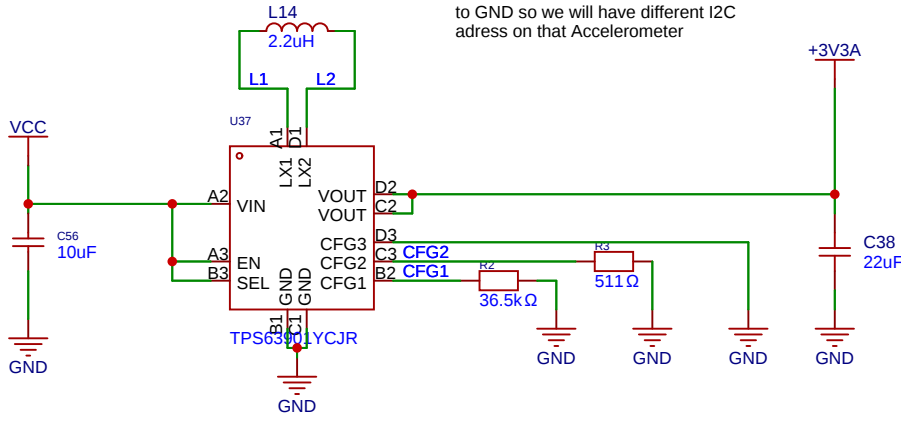


Table 3. Battery Charge Current Programming

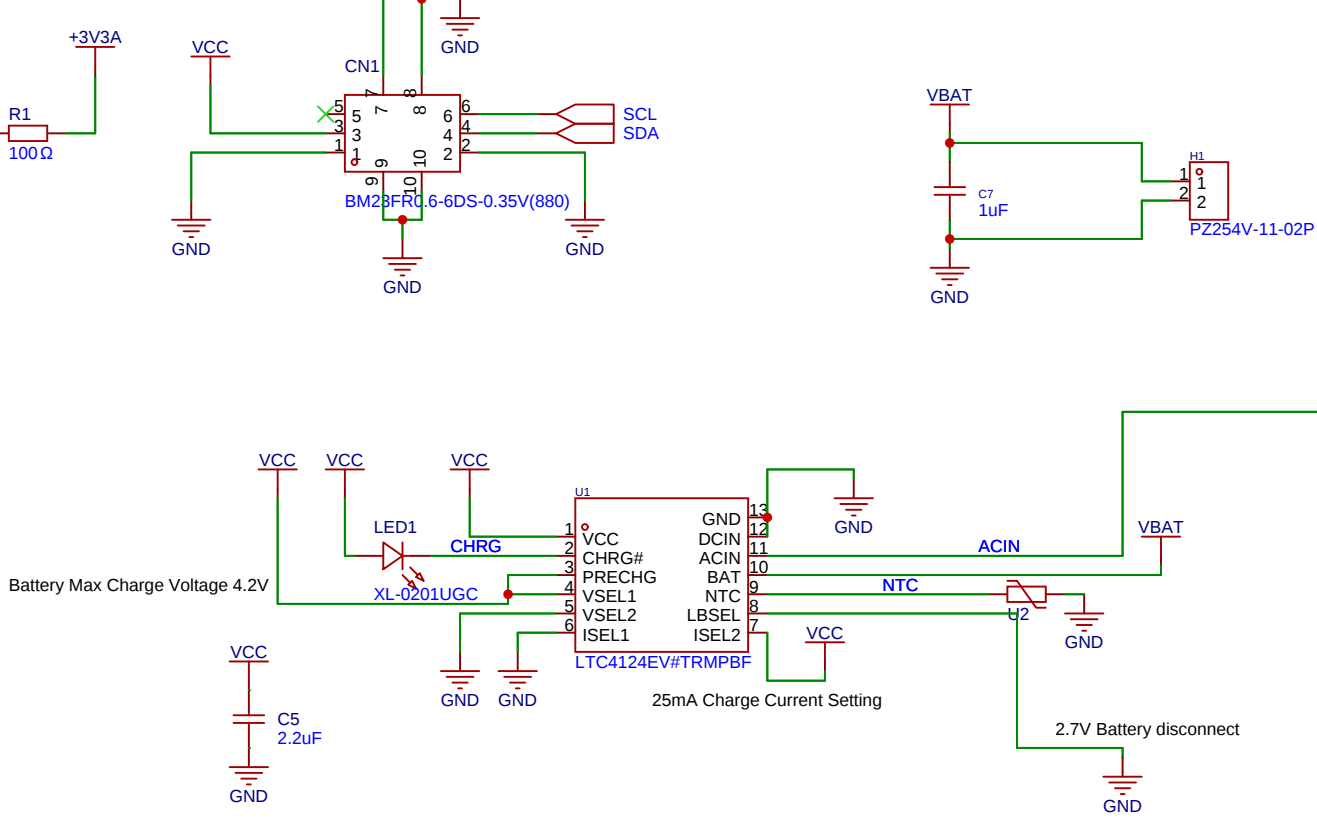
ISEL1	ISEL2	Charge Current
High	High	100mA
High	Low	50mA
Low	High	25mA
Low	Low	10mA

and GND.

CHRG (Pin 2): Charge Status Indicator. This pin is an open drain output with a 300μA current source pull down. In a typical application, this pin is pulled up by an LED to V_{CC}. This pin has four possible states shown in Table 1.

Table 1. CHRG States

CHRG	Status
High Impedance	No Input Power
Blink Slow (0.8Hz)	Powered On and Charging
Blink Fast (6Hz)	Charging Paused due to Battery Temperature Out of Range, or Charging Stopped due to Bad Battery Detected, or Limited Charging due to Undervoltage Current Limit
Pull Down	End of Charge C/10 Reached or Charging Complete

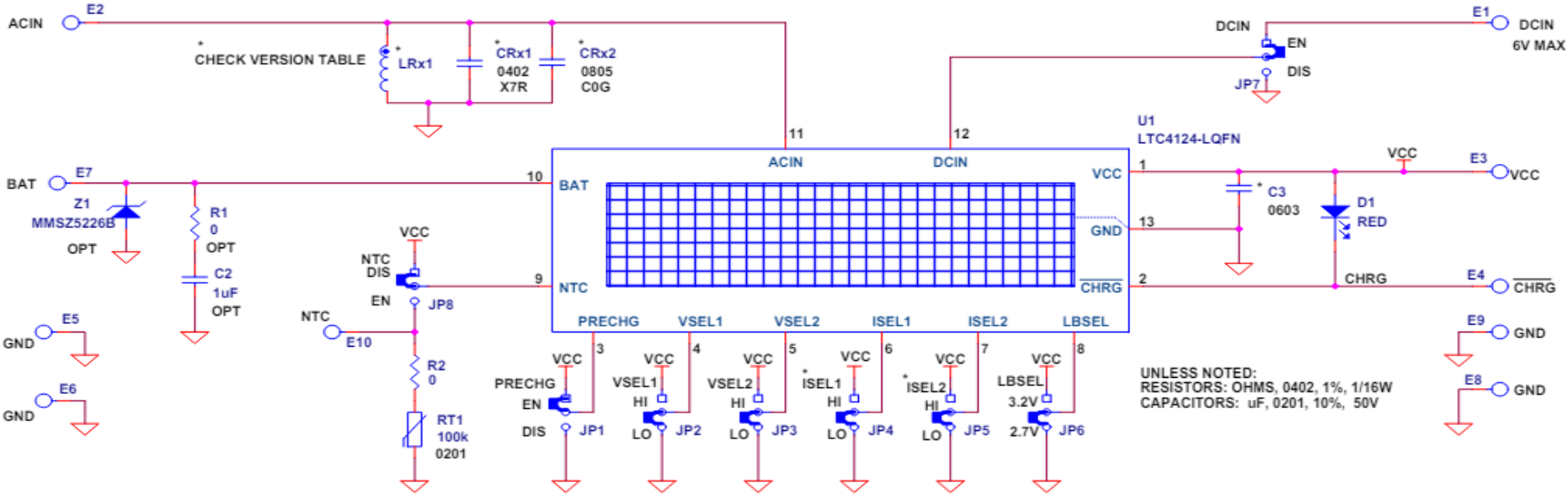


Battery Max Charge Voltage 4.2V

25mA Charge Current Setting

2.7V Battery disconnect

LC Tank Circuit for Charging



VSEL1	VSEL2	CHARGE VOLTAGE
HI	HI	4.35V
HI	LO	4.20V
LO	HI	4.10V
LO	LO	4.00V

PCA ADDITIONAL PARTS

* VERSION TABLE

SKU#	CHARGE CURRENT	LRx1	CRx1	CRx2	C3	ISEL1	ISEL2
-A	10mA	7.2uH	3300pF	OPT	OPT	LO	LO
-B	25mA	13uF	OPT	0.047uF	2.2uF	LO	HI
-C	50mA	12.6uF	OPT	0.047uF	10uF	HI	LO
-D	100mA	12.6uF	OPT	0.047uF	47uF	HI	HI

Schematic	Schematic1		Update Date	2024-11-07
Page	P1		Create Date	2024-09-17
Drawn	EasyEDA	MG V1.3(Coil/Battery PCB)	Part Number	JLPCPCB-001
Reviewed	EasyEDA Pro			
EasyEDA		VER	SIZE	PAGE 1 OF 1
		V0.1	A3	EasyEDA.com