

VREG DESIGN FOR TI REVIEW

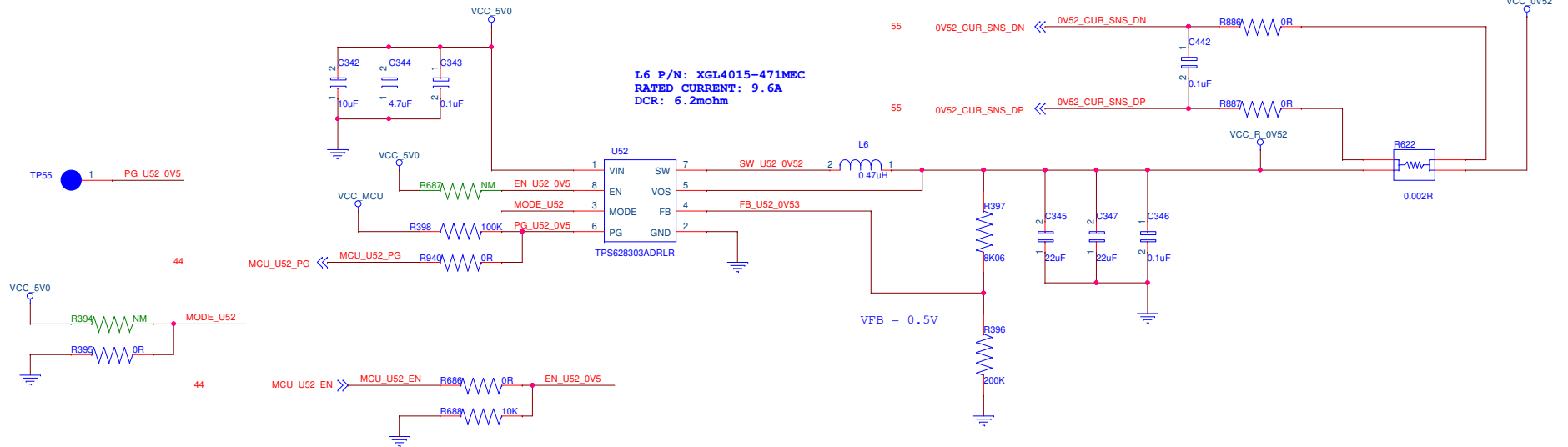
5V TO 0.52V REGULATOR

NOTE:
ALL THE RESISTORS ARE 1% TOLERANCE
ALL THE INPUT AND OUTPUT CAPS ARE 25V RATED

CAD NOTE:
PLACE C442 AND SHUNT RESISTOR NEARER
TO THE CURRENT SENSE IC TO REDUCE THE VOLTAGE DROP

CAD NOTE:
PLACE DECAPS CLOSE TO IC

0.52V @2A



NOTE:
ALL THE RESISTORS ARE 1% TOLERANCE
ALL THE INPUT AND OUTPUT CAPS ARE 25V RATED

CAD NOTE:
PLACE C526 AND SHUNT RESISTOR NEARER
TO THE CURRENT SENSE IC TO REDUCE THE VOLTAGE DROP

0.75V @2.5A

