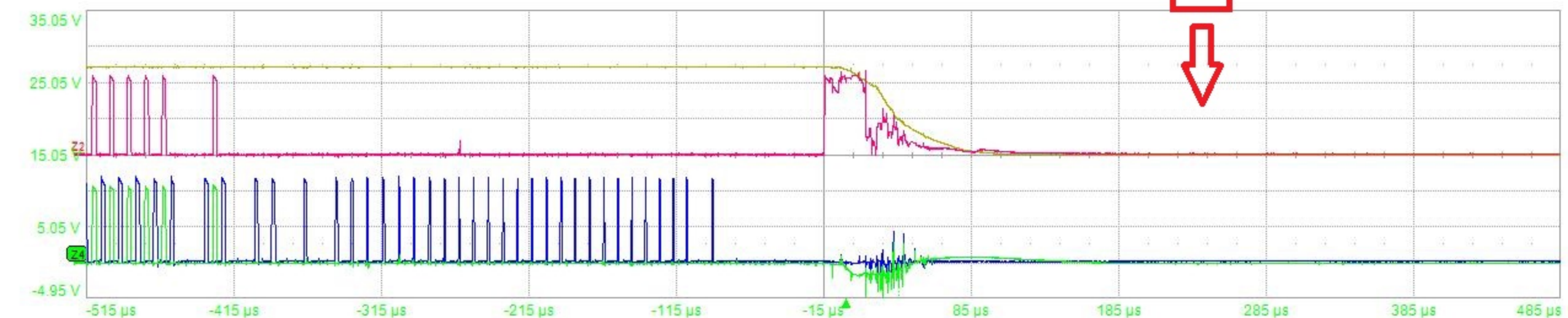
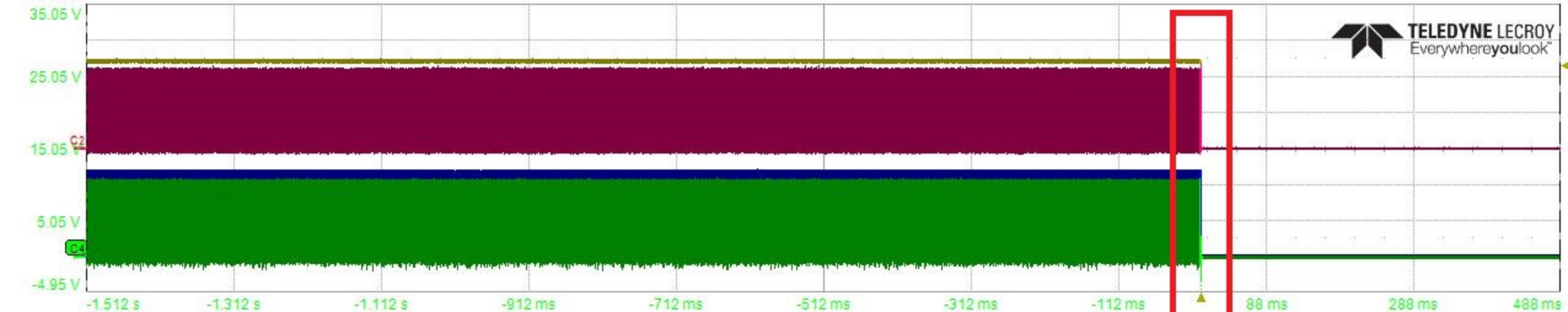


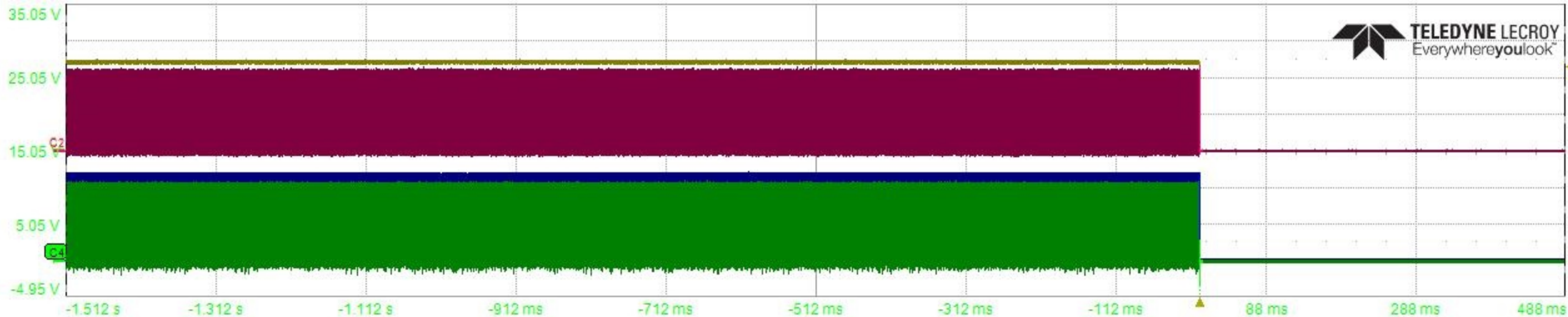


Measure	P1:max(C1)	P2:max(C2)	P3:max(C3)	P4:max(C4)	P5:---	P6:---	P7:---	P8:---
value	24.71 V	11.49 V	11.94 V	11.01 V				
mean	24.68000 V	11.42000 V	12.1450 V	10.97000 V				
min	24.65 V	11.35 V	11.94 V	10.93 V				
max	24.71 V	11.49 V	12.35 V	11.01 V				
sdev	30.00 mV	70.00 mV	205.0 mV	40.00 mV				
num	2	2	2	2				
status	✓	✓	↓	↓				

C1 is output voltage (24V)
C2 is SR low side gate
C3 is SR high side gate
C4 is SR low side gate

C1	DC1M	C2	DC1M	C3	DC1M	C4	DC1M	Z1	zoom(C1)	Z2	zoom(C2)	Z3	zoom(C3)	Z4	zoom(C4)
10.0 V/div		5.00 V/div		5.00 V/div		5.00 V/div		10.0 V/div		5.00 V/div		5.00 V/div		5.00 V/div	
50 mV ofst		0 mV offset		-14.900 V		-15.050 V		100 μs/div		100 μs/div		100 μs/div		100 μs/div	

Tbase	512 ms	Trigger	C1 DC
	200 ms/div	Stop	22.9 V
10 MS	5 MS/s	Edge	Neg



Measure	P1:max(C1)	P2:max(C2)	P3:max(C3)	P4:max(C4)	P5:---	P6:---	P7:---	P8:---
value	24.71 V	11.49 V	11.94 V	11.01 V				
mean	24.68000 V	11.42000 V	12.1450 V	10.97000 V				
min	24.65 V	11.35 V	11.94 V	10.93 V				
max	24.71 V	11.49 V	12.35 V	11.01 V				
sdev	30.00 mV	70.00 mV	205.0 mV	40.00 mV				
num	2	2	2	2				
status	✓	✓	↓	↓				

C1 is output voltage (24V)
C2 is SR low side gate
C3 is SR high side gate
C4 is SR low side gate

C1	DC1M	C2	DC1M	C3	DC1M	C4	DC1M	Z1	zoom(C1)	Z2	zoom(C2)	Z3	zoom(C3)	Z4	zoom(C4)
10.0 V/div		5.00 V/div		5.00 V/div		5.00 V/div		10.0 V/div		5.00 V/div		5.00 V/div		5.00 V/div	
50 mV ofst		0 mV offset		-14.900 V		-15.050 V		20.0 μs/div		20.0 μs/div		20.0 μs/div		20.0 μs/div	

Tbase	512 ms	Trigger	C1 DC
	200 ms/div	Stop	22.9 V
10 MS	5 MS/s	Edge	Neg