

A

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C

D

A

B

C

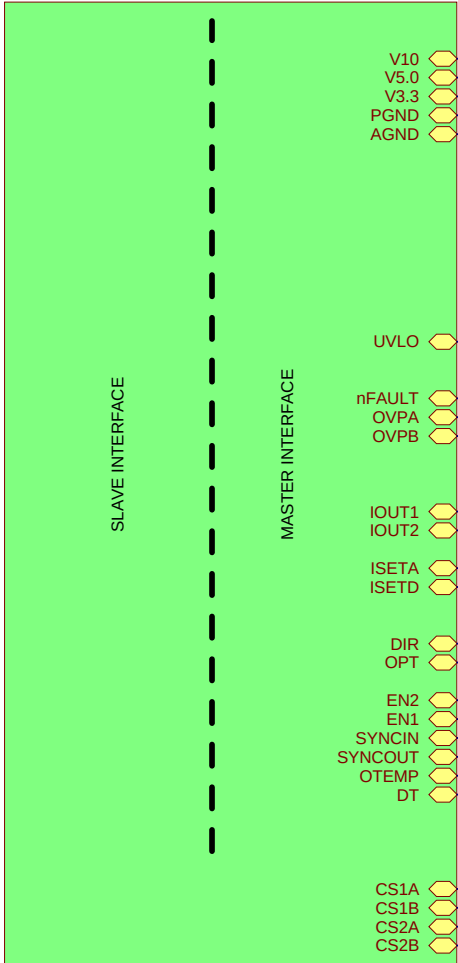
D

Revision History	
Revision	Notes

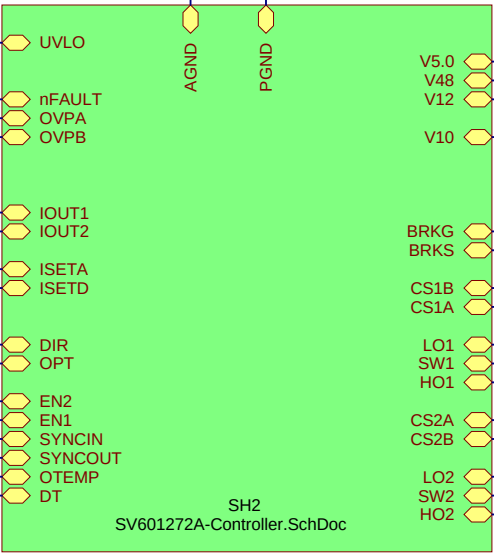
SH7
SV601272A-HW.SchDoc

SH6
SV601272A-Analog.SchDoc

SH5
SV601272A-Bias.SchDoc

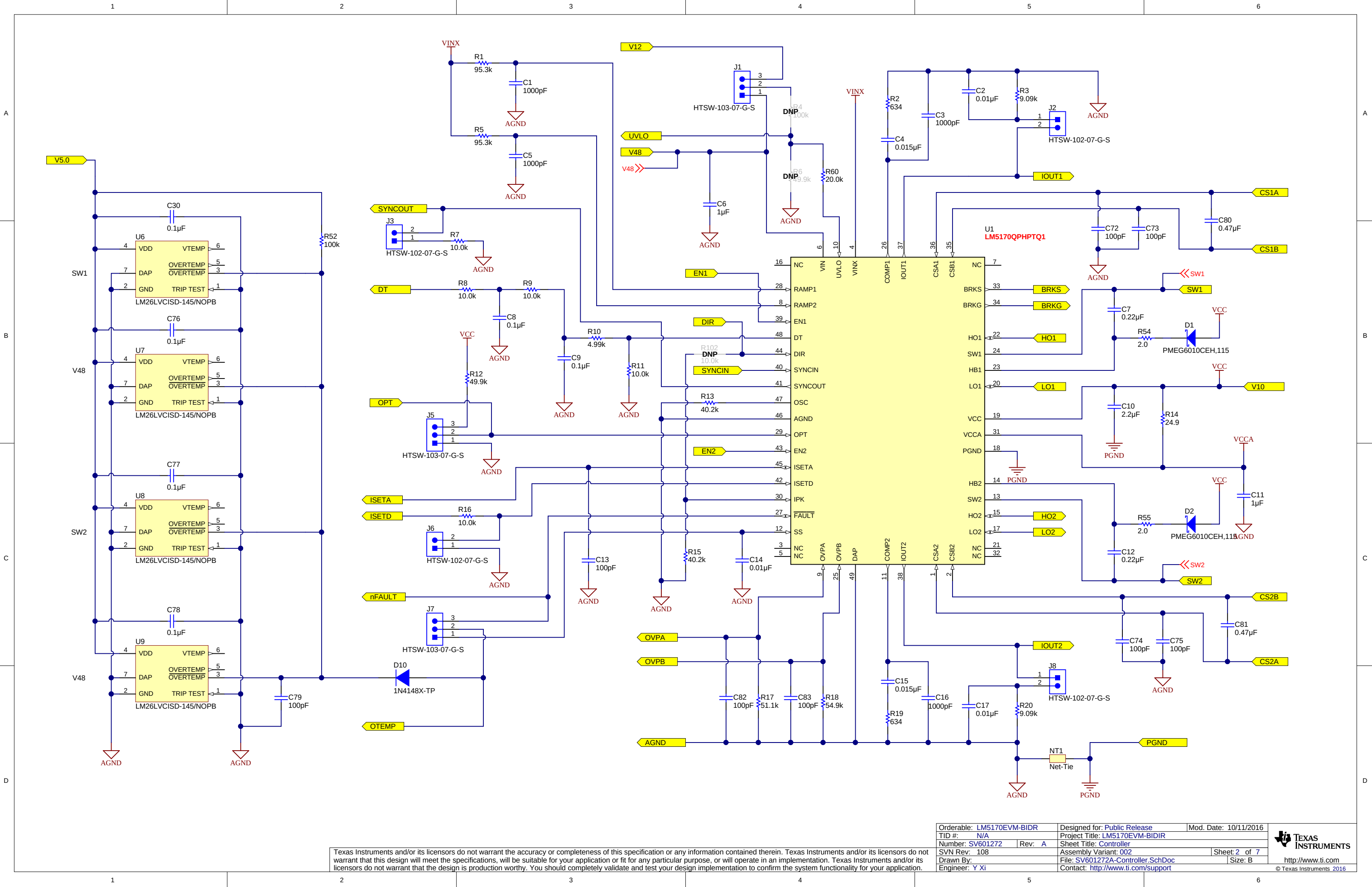


SH4
SV601272A-Interface.SchDoc



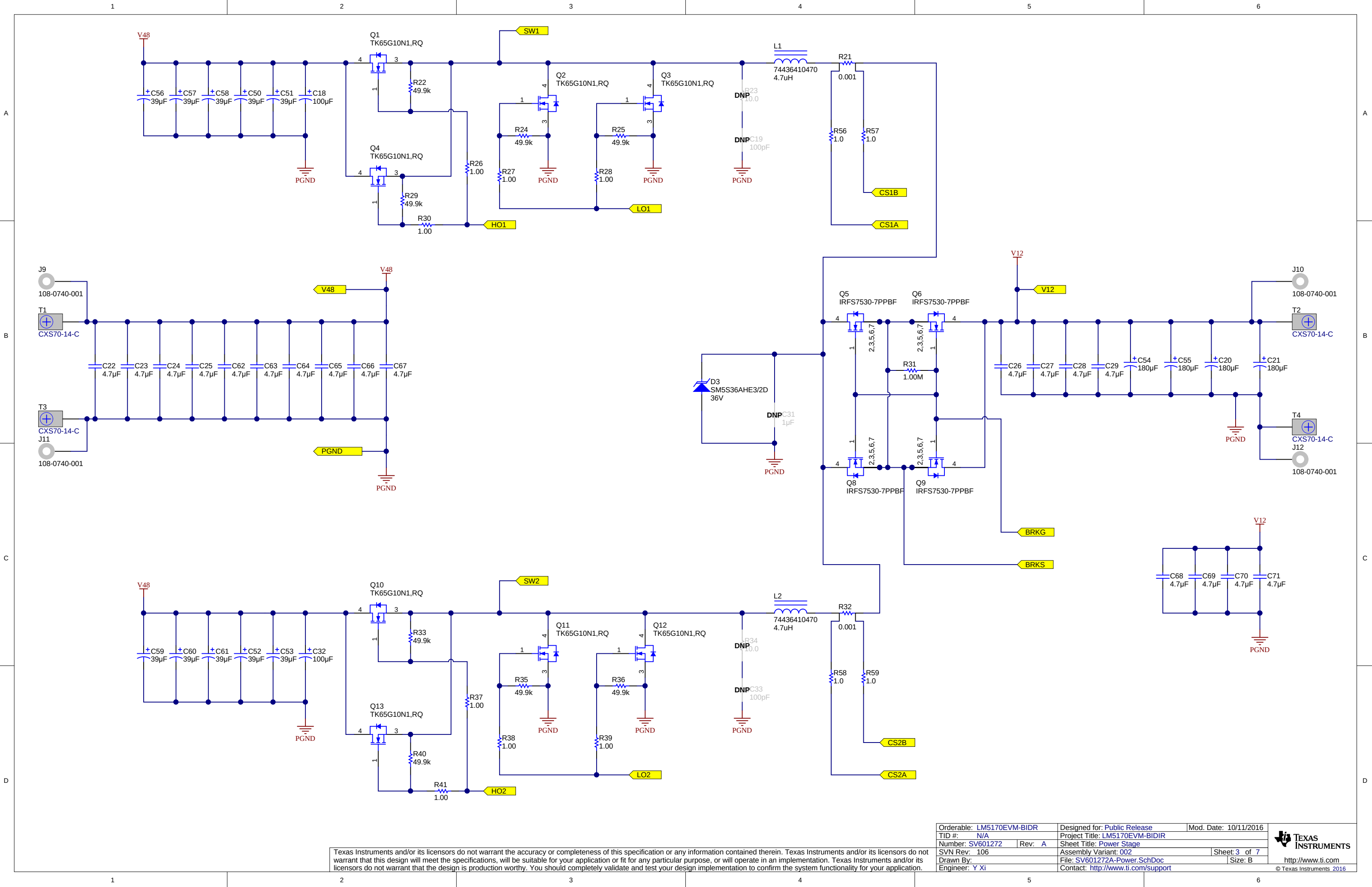
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TID #:	N/A	Project Title: LM5170EVM-BIDIR	
Number:	SV601272	Rev:	A
SVN Rev:	93	Assembly Variant:	002
Drawn By:			File: SV601272A-Cover.SchDoc
Engineer:	Y Xi		Contact: http://www.ti.com/support



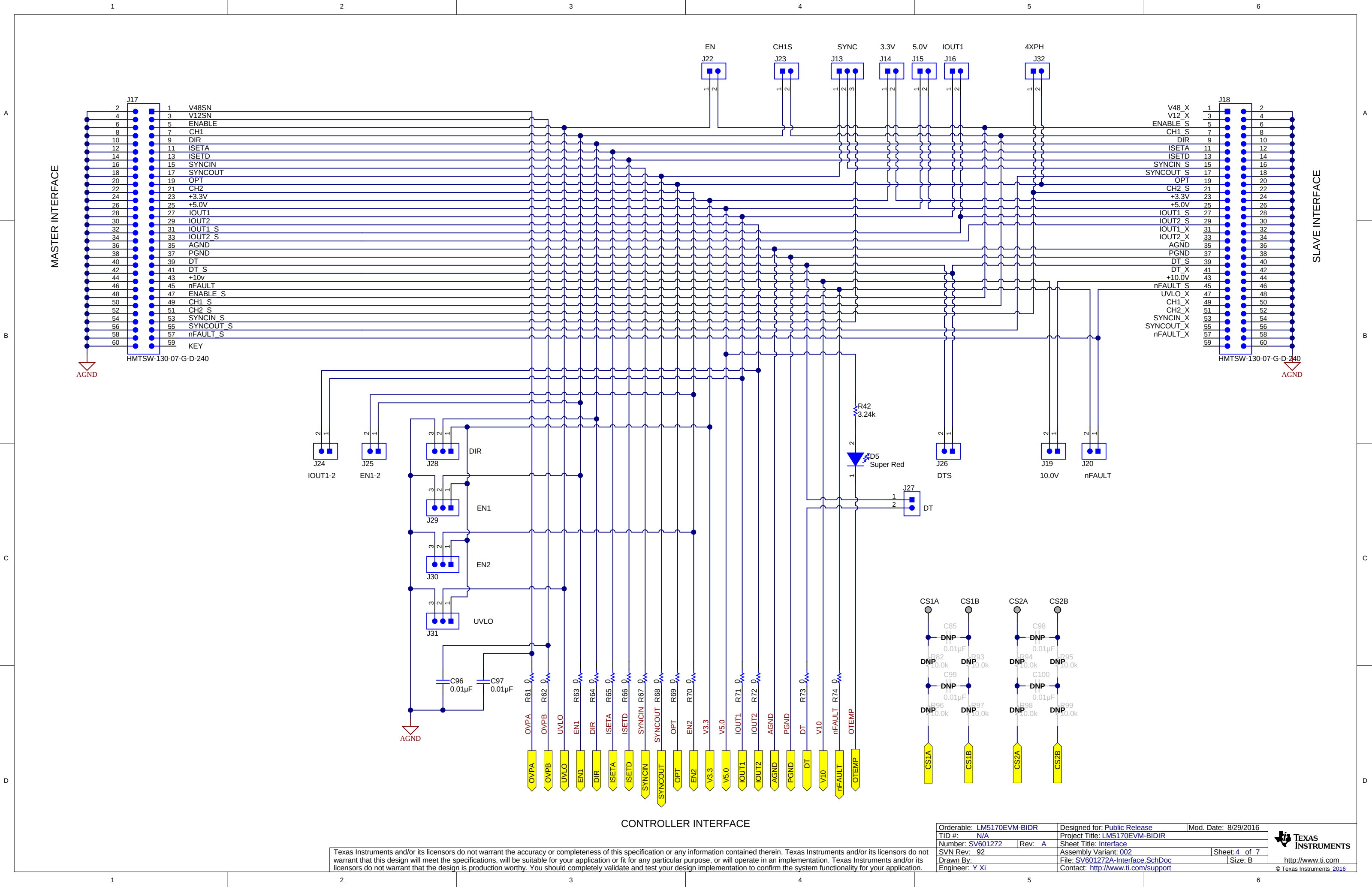
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TID #: N/A	Project Title: LM5170EVM-BIDIR	
Number: SV601272	Rev: A	Sheet Title: Controller
SVN Rev: 108	Assembly Variant: 002	Sheet: 2 of 7
Drawn By:	File: SV601272A-Controller.SchDoc	Size: B
Engineer: Y Xi	Contact: http://www.ti.com/support	



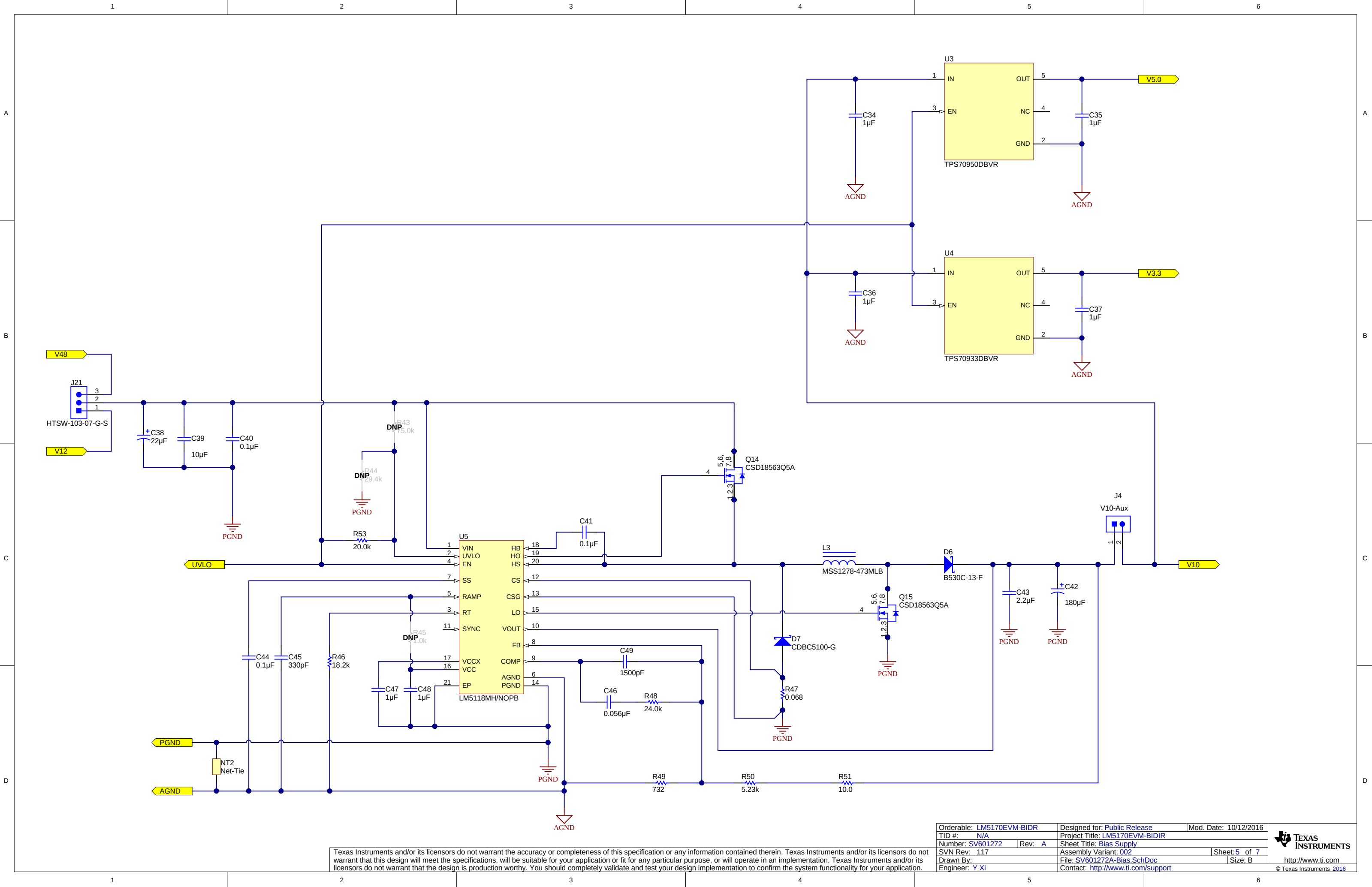
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TID #: N/A		Project Title: LM5170EVM-BIDIR			
Number: SV601272		Rev: A		Sheet Title: Power Stage	
SVN Rev: 106		Assembly Variant: 002		Sheet: 3 of 7	
Drawn By:		File: SV601272A-Power.SchDoc		Size: B	
Engineer: Y Xi		Contact: http://www.ti.com/support			



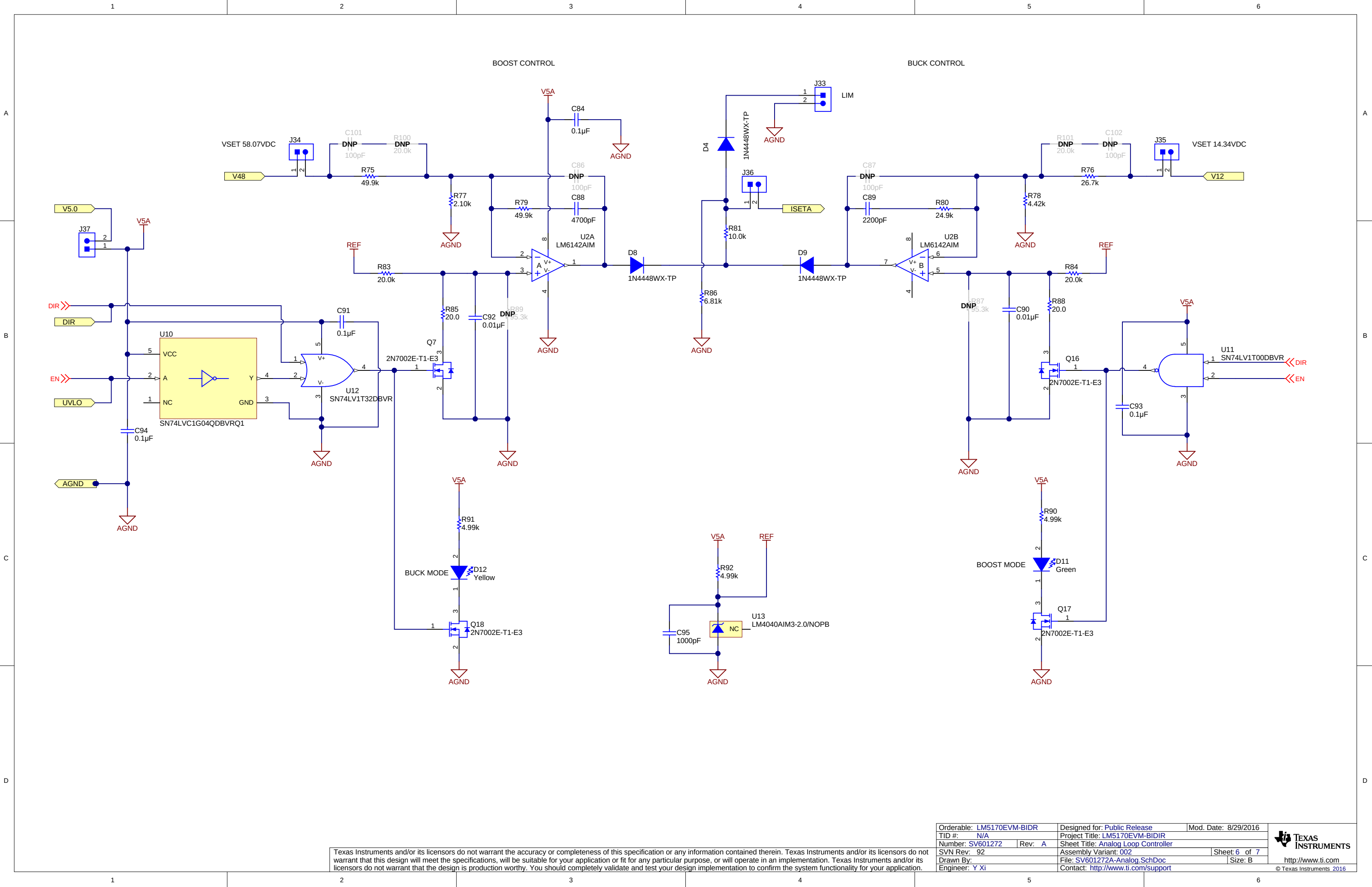
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TID #: N/A		Project Title: LM5170EVM-BIDR	
Number: SV601272		Rev: A	
SVN Rev: 92		Assembly Variant: 002	
Drawn By: Y Xi		File: SV601272A-Interface.SchDoc	
Engineer: Y Xi		Contact: http://www.ti.com/support	
		Sheet: 4 of 7	Size: B



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TID #: N/A	Project Title: LM5170EVM-BIDIR	
Number: SV601272	Rev: A	Sheet Title: Bias Supply
SVN Rev: 117	Assembly Variant: 002	Sheet 5 of 7
Drawn By:	File: SV601272A-Bias.SchDoc	Size: B
Engineer: Y Xi	Contact: http://www.ti.com/support	



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TID #: N/A		Project Title: LM5170EVM-BIDIR			
Number: SV601272		Rev: A		Sheet Title: Analog Loop Controller	
SVN Rev: 92		Assembly Variant: 002			Sheet: 6 of 7
Drawn By:		File: SV601272A-Analog.SchDoc			Size: B
Engineer: Y Xi		Contact: http://www.ti.com/support			

