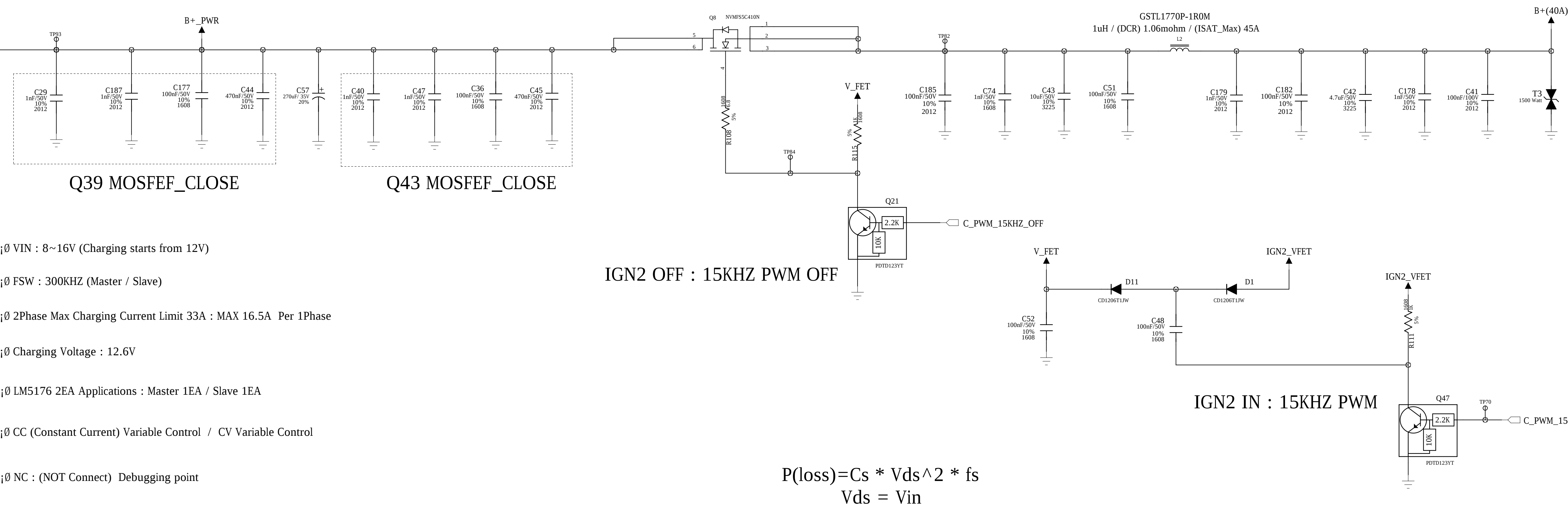


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

$$I = \text{Sqaure}((175-75) / (0.92\text{mohm} * 1.95 * 39)) = 37.81\text{A}$$

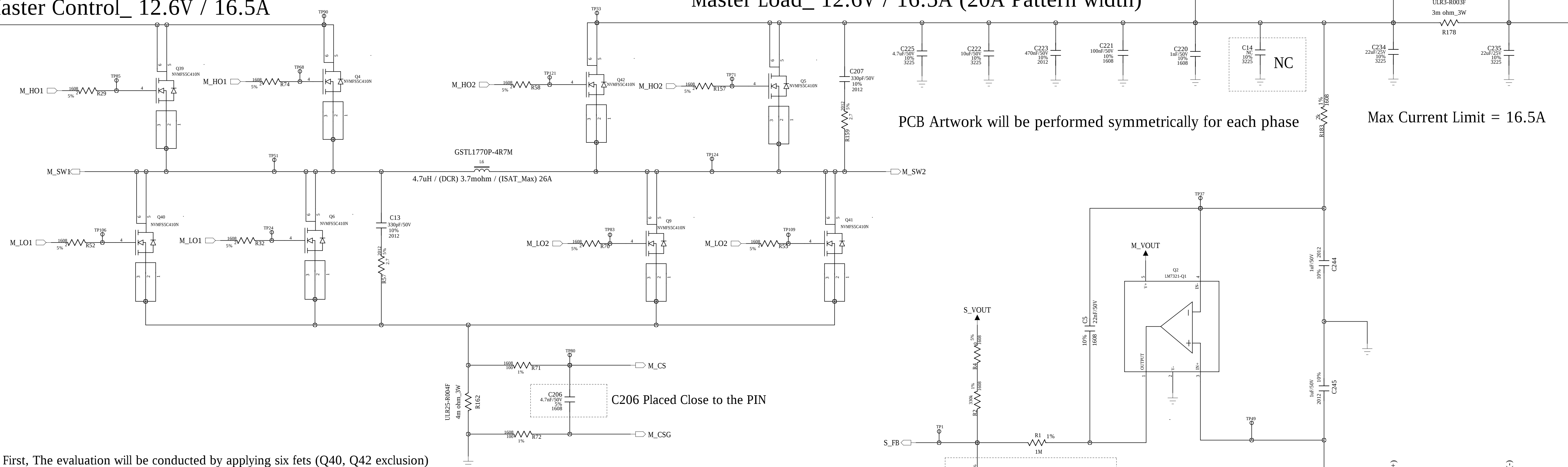
$$I(\text{INPUT A}) = (12.6V * 33A / 12.5V * 0.9) = 38.5A$$



V(IN)	12V	12.5V	14.8V	16V
I(IN)	38.5A	36.96V	31.22A	28.88A

Master Control_ 12.6V / 16.5A

Master Load_ 12.6V / 16.5A (20A Pattern width)



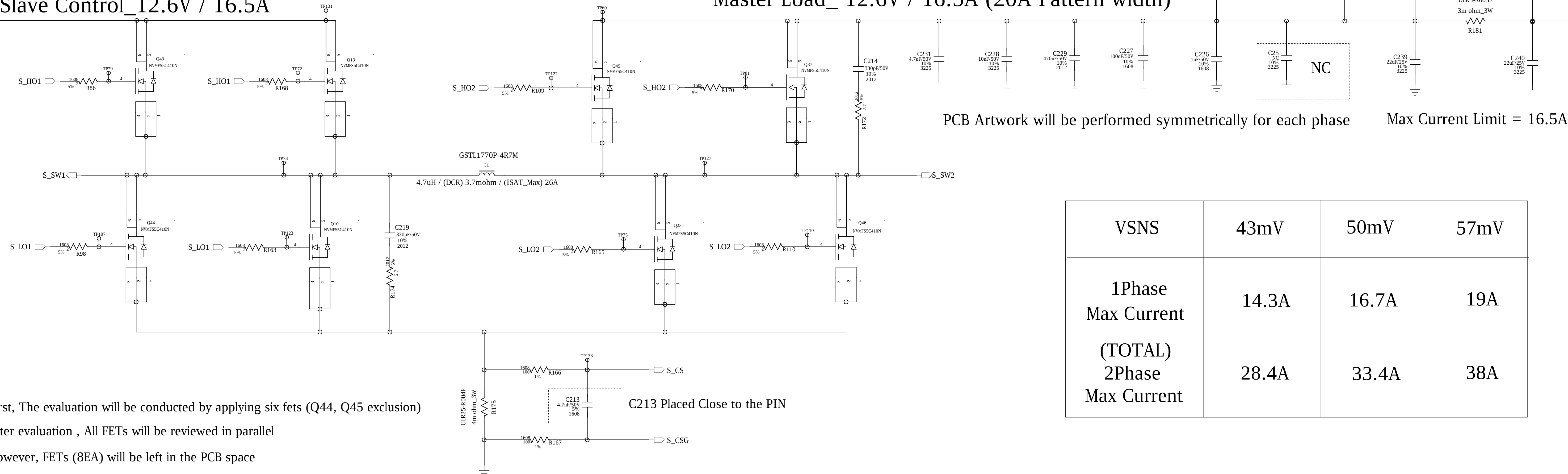
First, The evaluation will be conducted by applying six fets (Q40, Q42 exclusion)
After evaluation , All FETs will be reviewed in parallel
However, FETs (8EA) will be left in the PCB space

$$P(\text{loss}) = C_s * V_{ds}^2 * f_s$$

$$V_{ds} = V_{in}$$

Slave Control_12.6V / 16.5A

Master Load_ 12.6V / 16.5A (20A Pattern width)



First, The evaluation will be conducted by applying six fets (Q44, Q45 exclusion)
After evaluation , All FETs will be reviewed in parallel
However, FETs (8EA) will be left in the PCB space

VSNS	43mV	50mV	57mV
1Phase Max Current	14.3A	16.7A	19A
(TOTAL) 2Phase Max Current	28.4A	33.4A	38A

Buck	66mV	80mV	94mV
4mohm	16.5A	20A	23.5A
Boost	100mV	120mV	140mV
4mohm	25A	30A	35A

		EMPLOYER: TITLE:			
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED:	DATED:	<Code>	E	REVISION	2.00
QUALITY CONTROL:	DATED:				
RELEASED:	DATED:	SCALE: <Scale>			SHEET: hr 2

