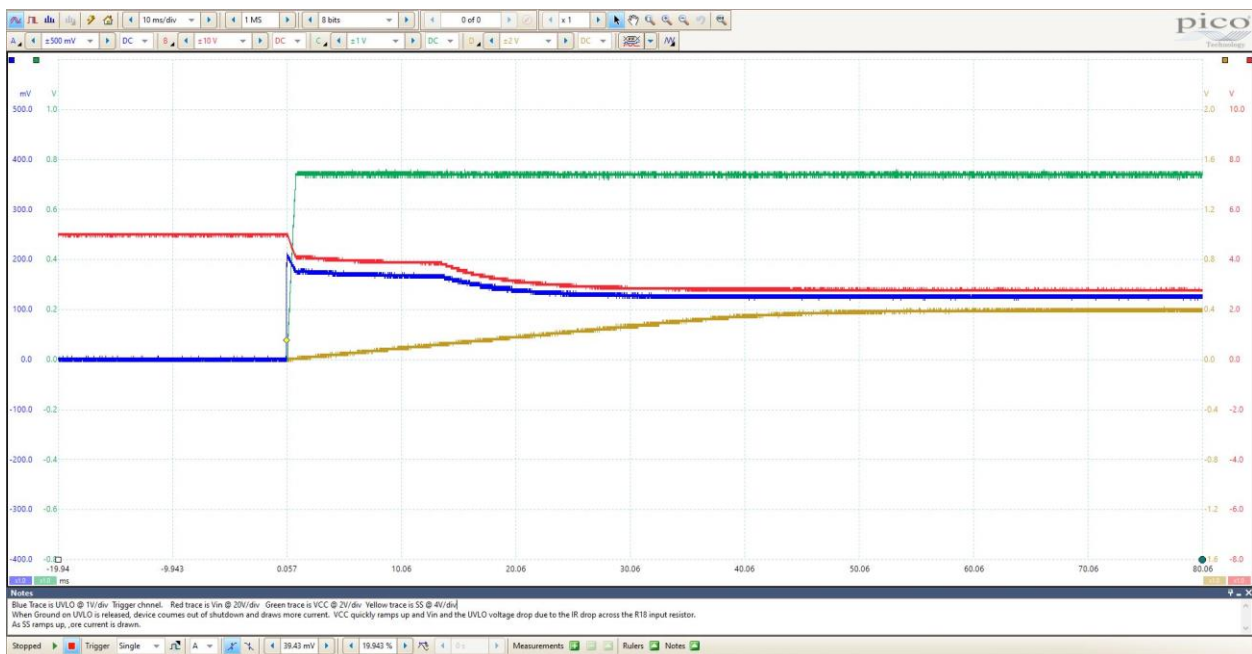


## Scope plots of LM5121 Design

This plot shows the startup sequence. The scope is triggered on the Blue trace. This is the UVLO voltage as the IC. The sequence is started by first applying the input voltage from an external power supply set to 50V. This is done while the VLO voltage is held at Ground. Then the Ground is removed and the UVLO voltage is allowed to quickly ramp up to  $V_{in}$  scaled by the 20K/499K voltage divider. The RED trace is the input voltage as seen across the input capacitors. The input voltage drops when the UVLO threshold is crossed because the IC draws more current and the MOSFETs are not being turned on, so the input current still goes through the input resistor and diode (D4 and R18). The Green trace is VCC. That voltage comes up as the UVLO threshold is crossed. The Yellow trace is the SS signal. That starts to ramp up also.



Blue = 1V/div Red = 20V/div Green = 2V/div Yellow = 4V/div

The next plot replaces the VCC trace with the DG pin on the IC. The Red trace ( $V_{in}$ ) and the Green trace (DG-pin) are on the same scale. This clearly shows that the charge pump does not push the DG voltage above the  $V_{in}$  voltage.

The third plot replaces the DG pin with the DS pin for the Green trace.

The last plot has the LO FET drive signal on the Green trace. This shows that the switching operation starts at a point that appears to align with the VCC coming up.

