

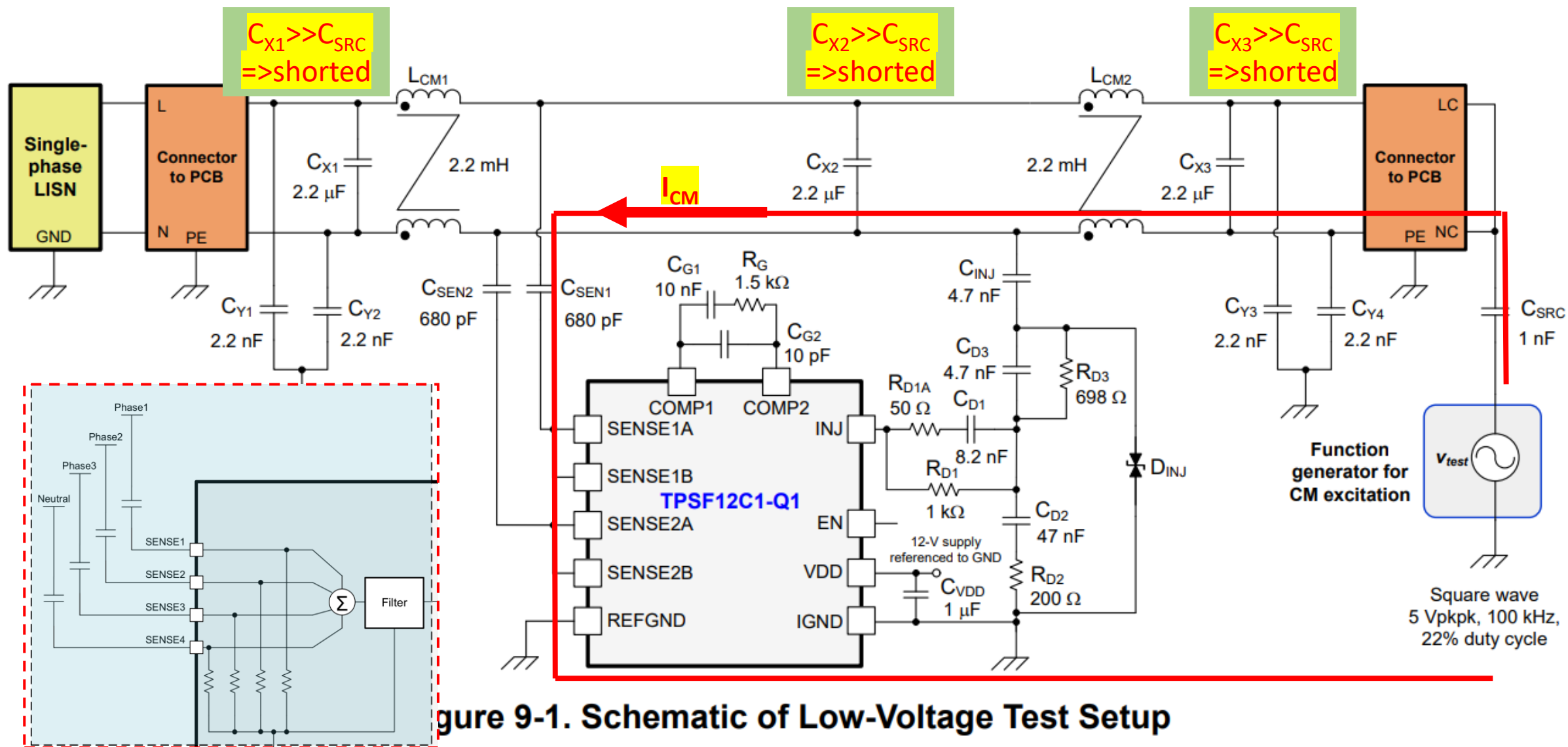
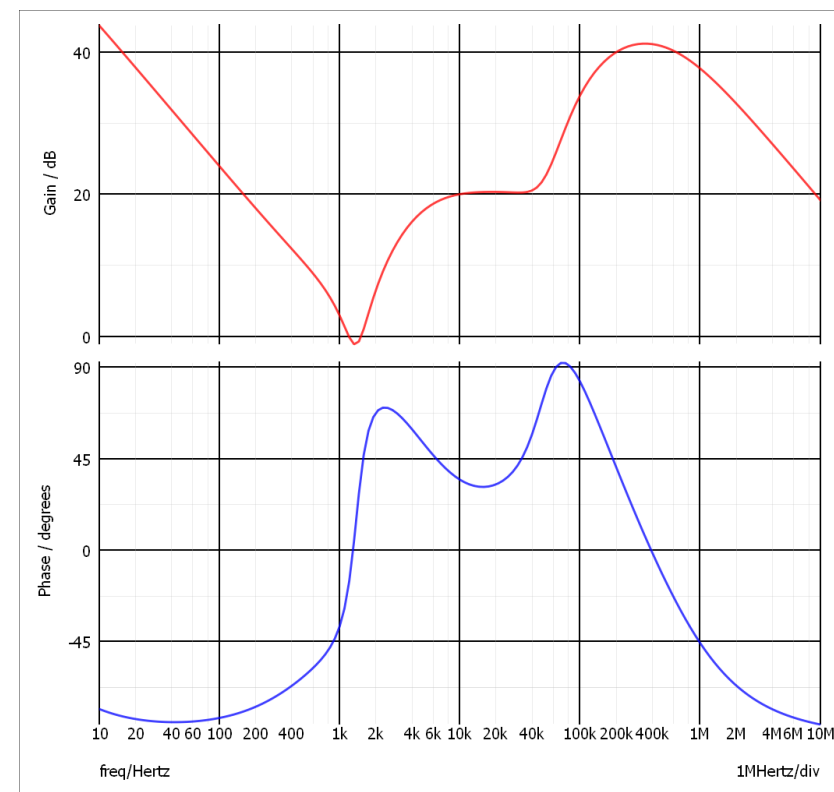
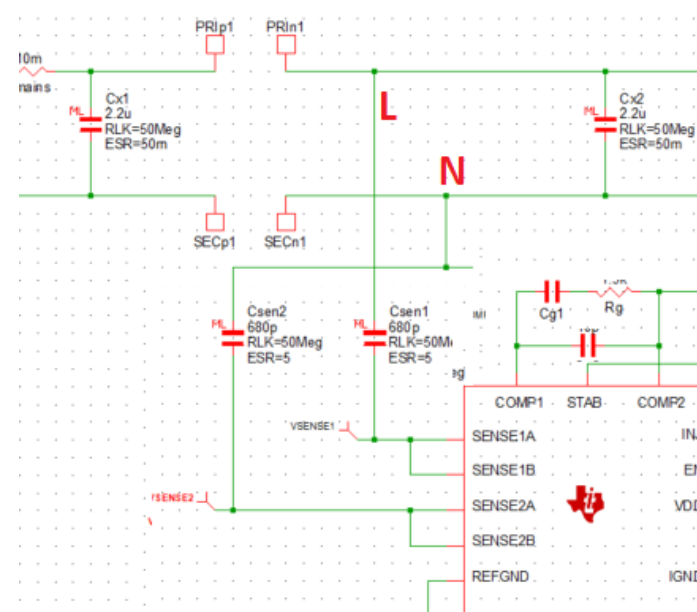
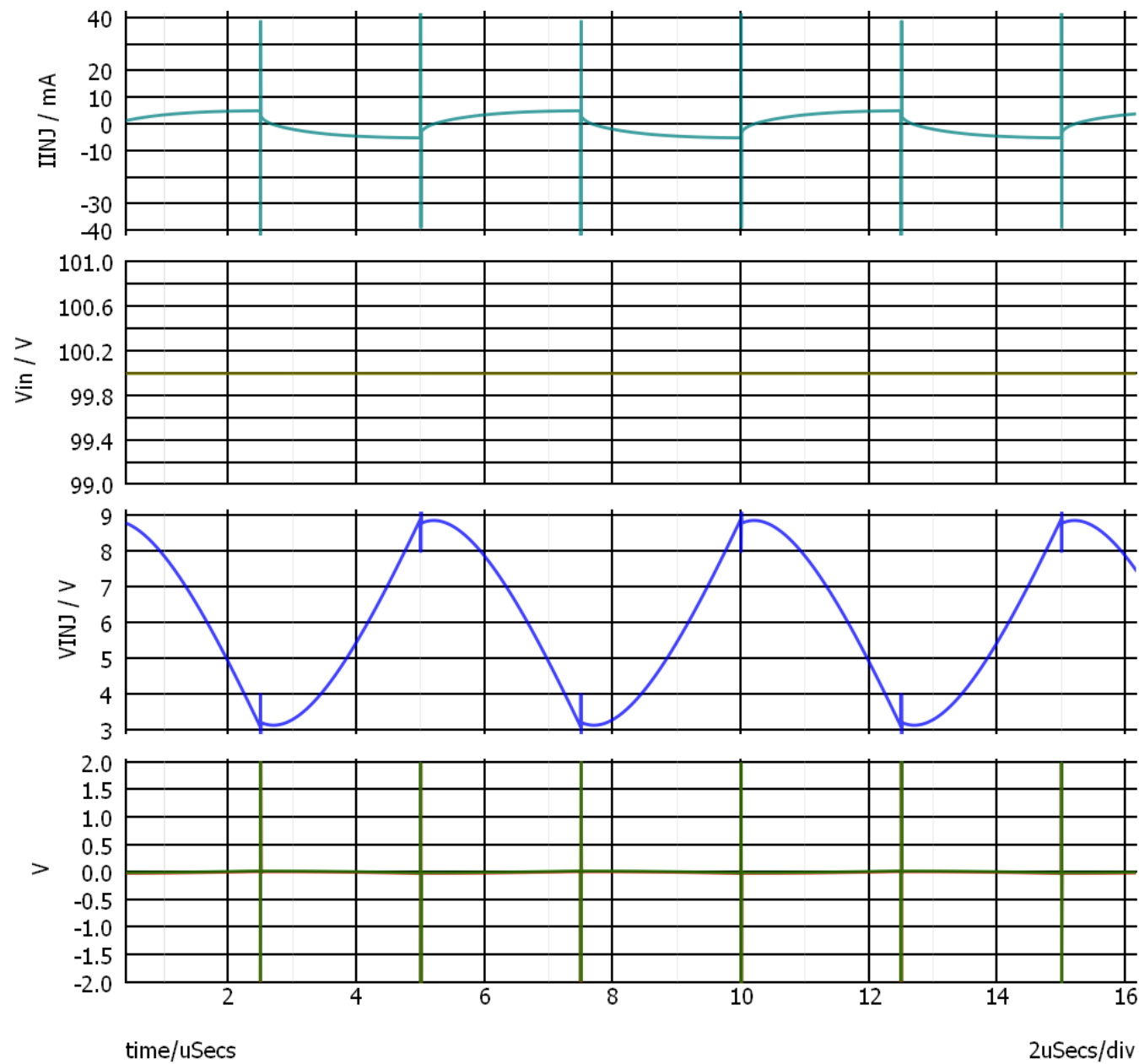
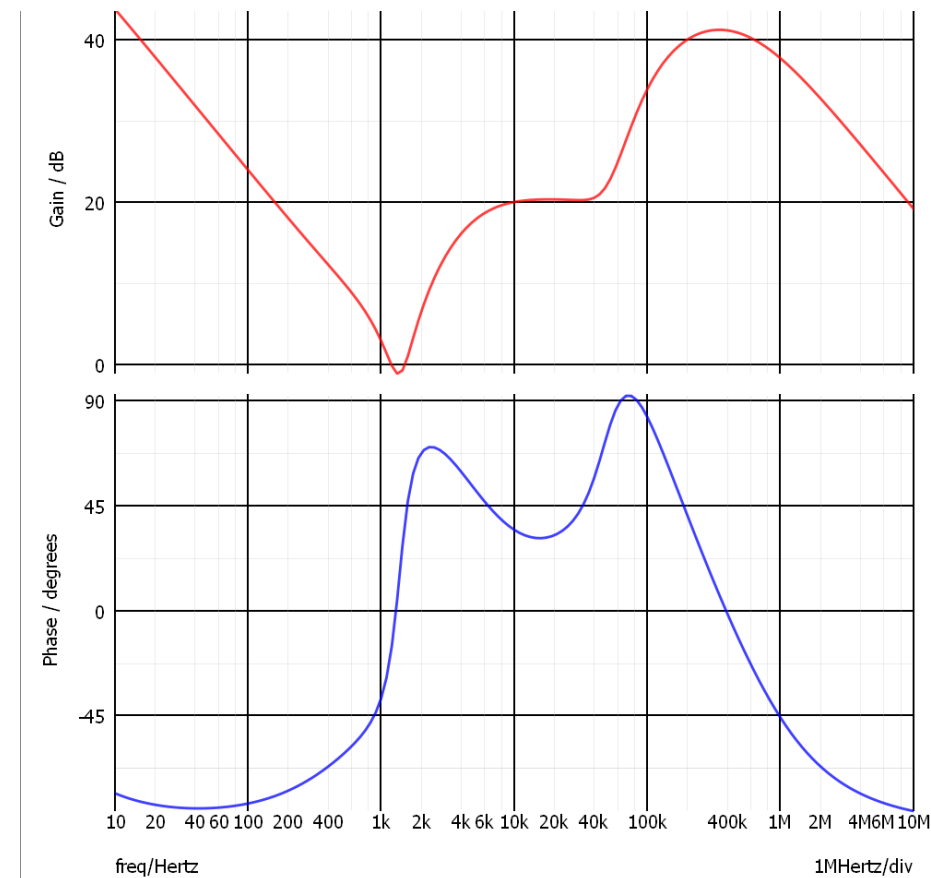
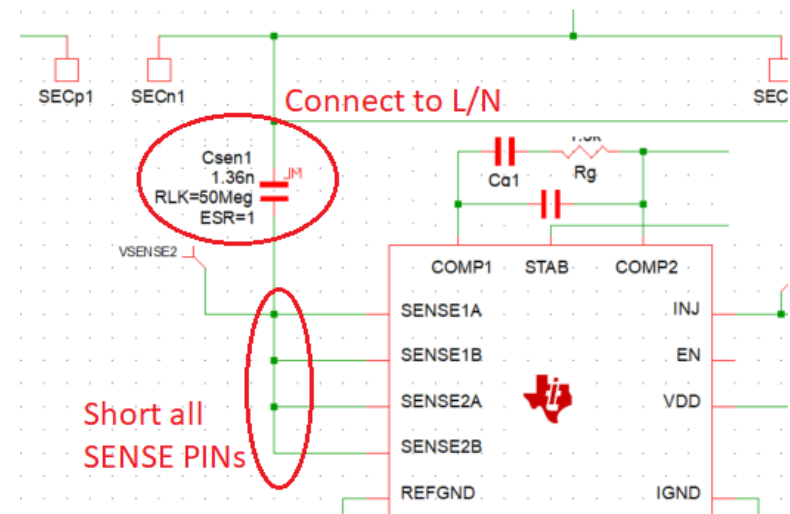
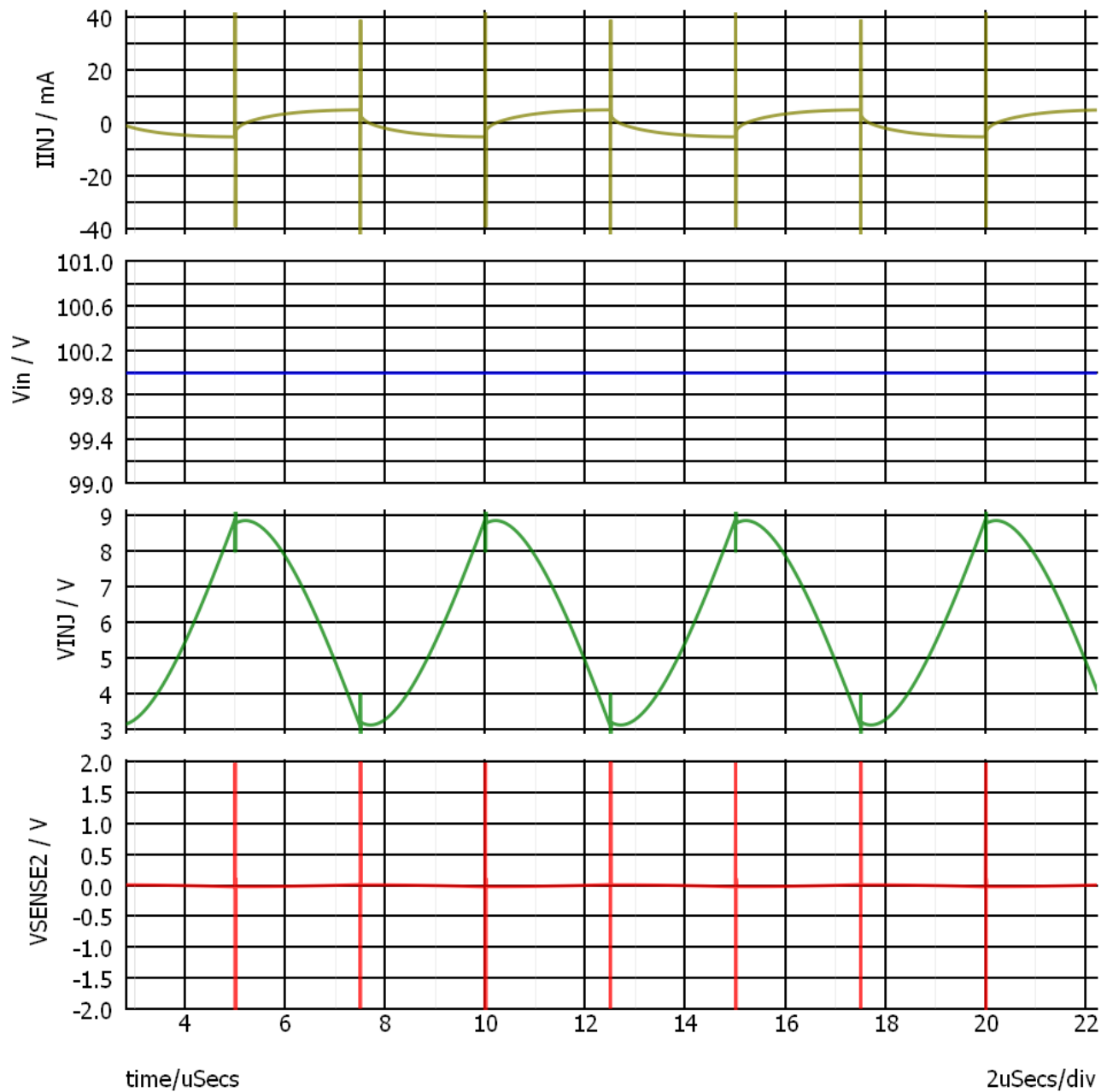


Figure 9-1. Schematic of Low-Voltage Test Setup

Original circuit



Short all sense pins



Move AEF IC to L/N side

