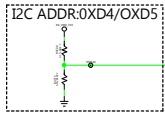
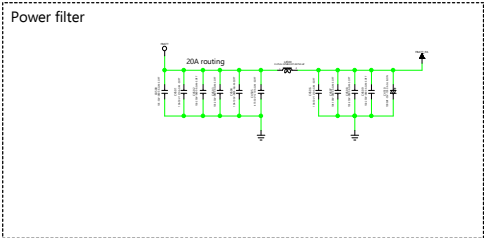
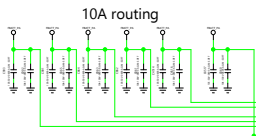


AUDIO CLASS\_D\_PA

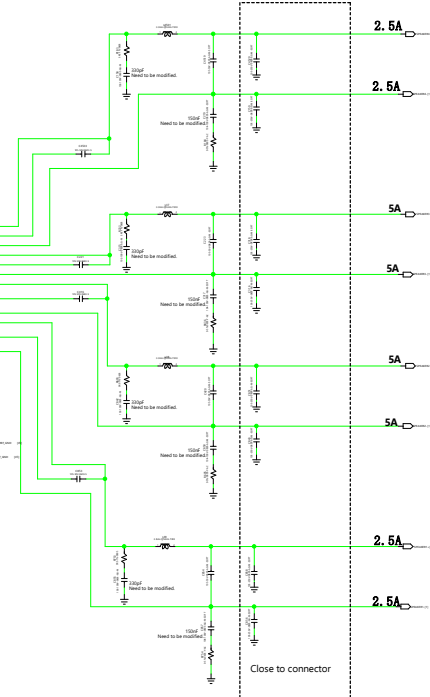


connect to MCU

connect to SOC/ISP



18mA



| PATEO    |            | PORSCH_PPE_SCHEMATIC_V1.0 |            | V1.0     |  |
|----------|------------|---------------------------|------------|----------|--|
| DATE     | 2023-10-20 | DESIGN                    | 1.0        | REVISION |  |
| DESIGNER | Much       | DATE                      | 2023-10-20 | DESIGNER |  |
| DESIGNER | Much       | DATE                      | 2023-10-20 | DESIGNER |  |
| DESIGNER | Much       | DATE                      | 2023-10-20 | DESIGNER |  |

