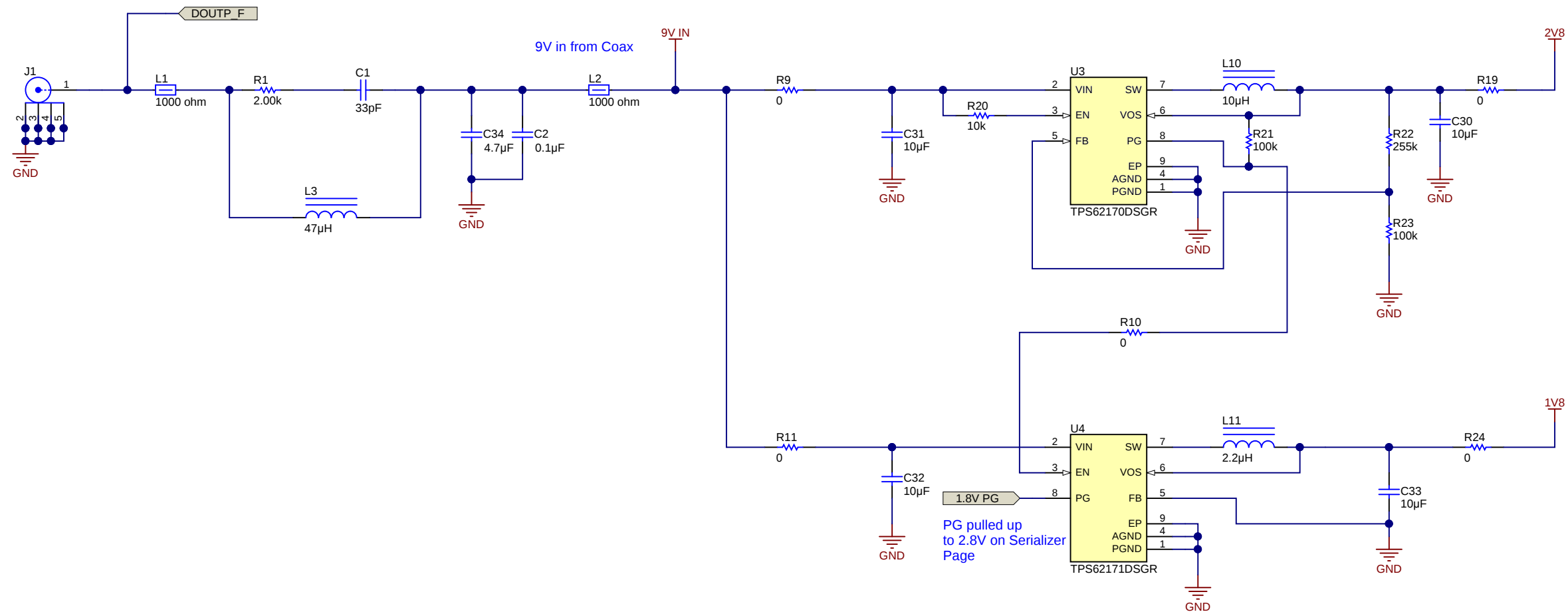


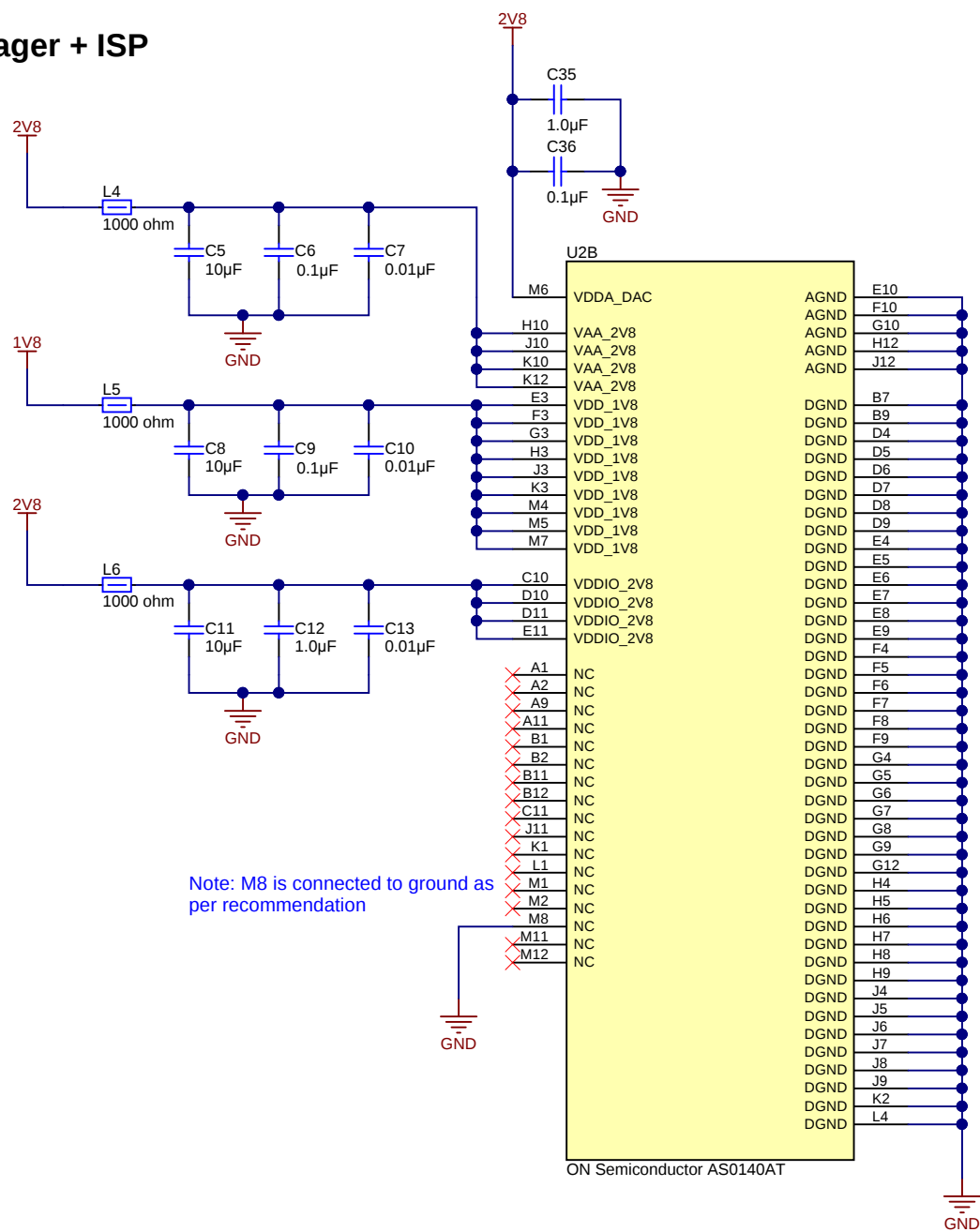
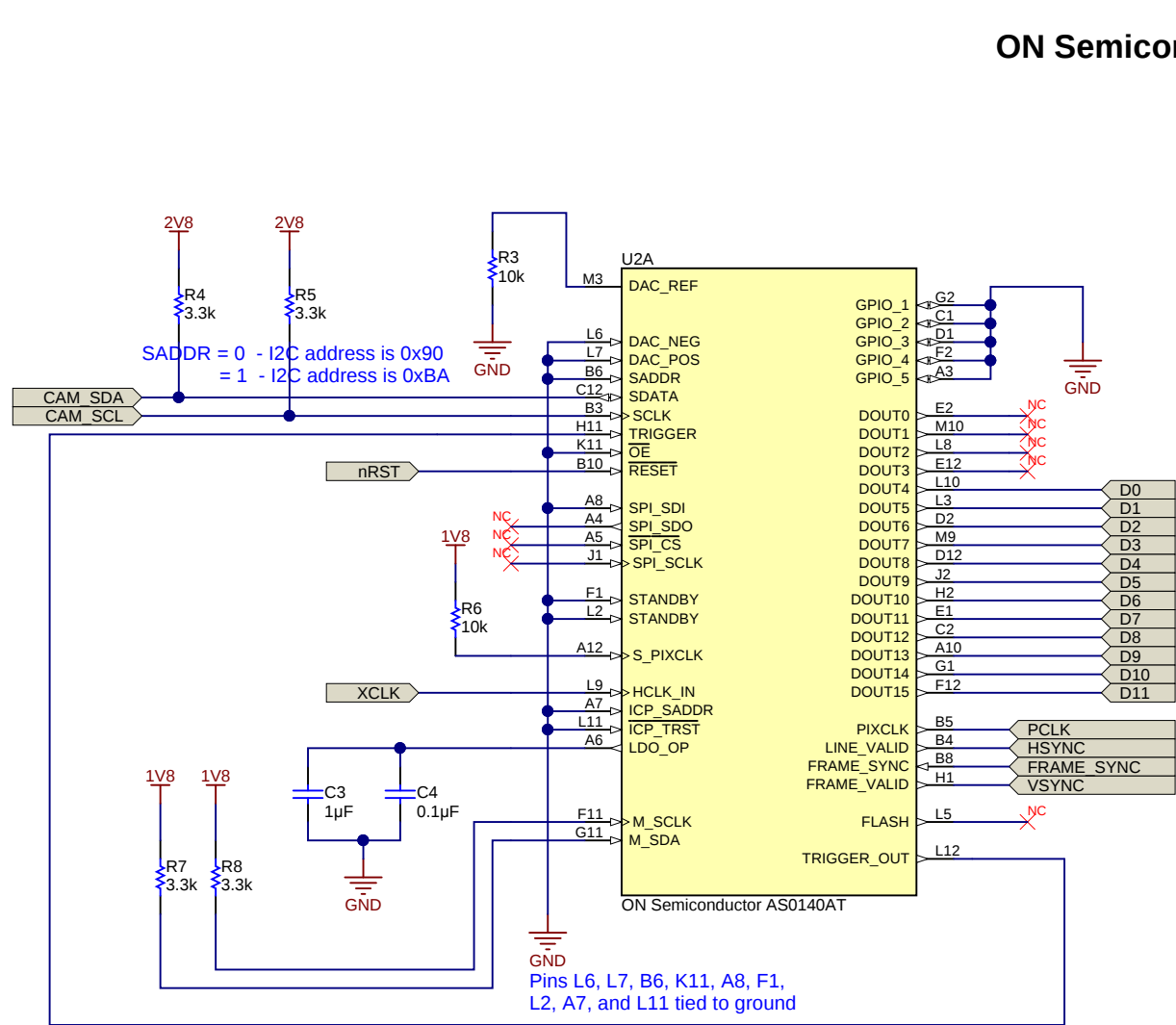
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Place Block Diagram here (if appropriate) or delete this text box.
If using a block diagram from another tool, save the picture as a .bmp file.
Then, use menu Place|Drawing Tools|Graphic to insert the .png/.svg/.bmp file on the schematic.

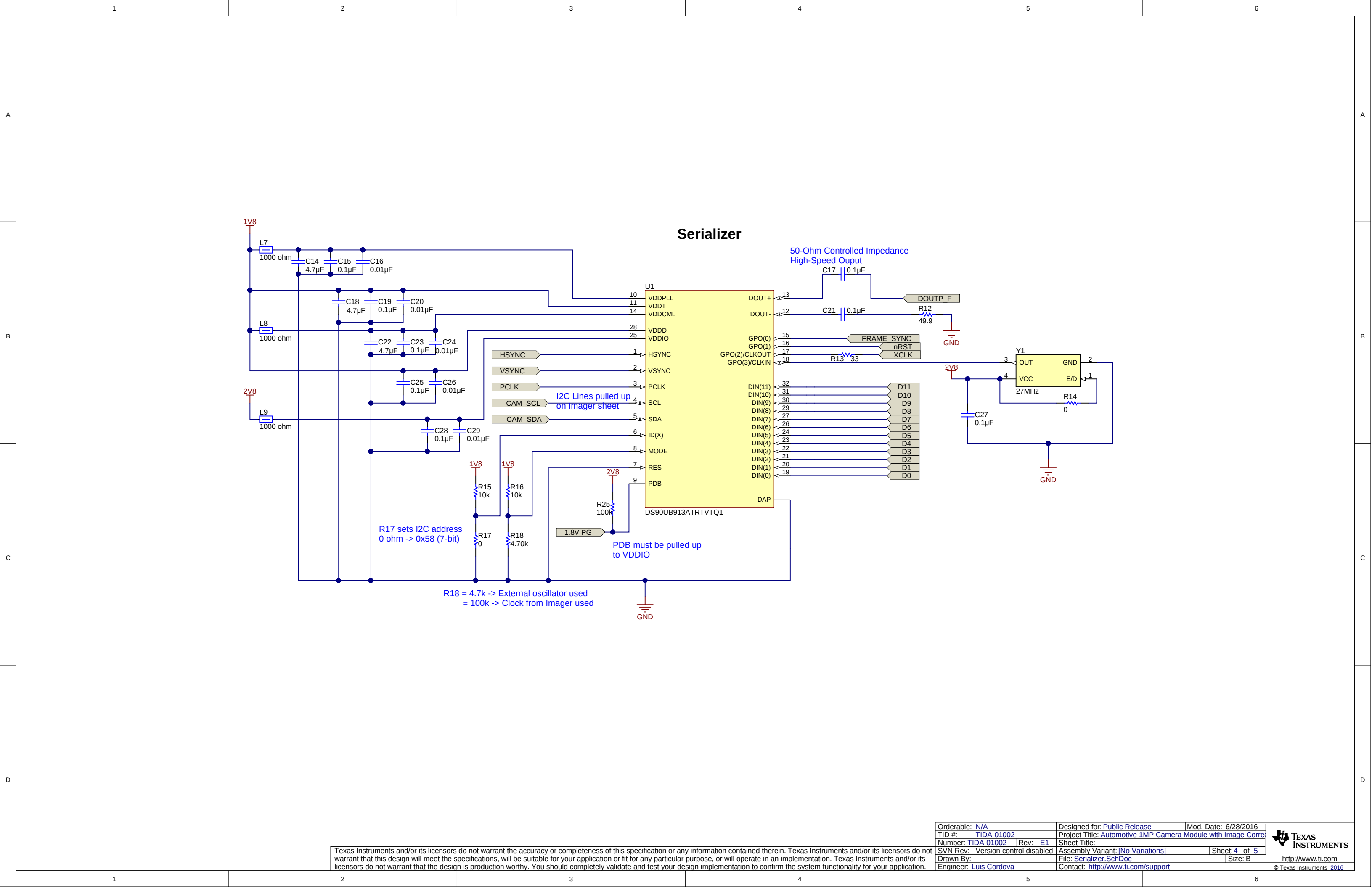
Power over Coax Filter & Power Supply



ON Semiconductor Imager + ISP



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FID1

FID2

FID3

FID4

FID5

FID6

PCB Number: TIDA-01002

PCB Rev: E1

PCB

LOGO

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PCB

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Pb-Free Symbol

PCB

LOGO

FCC disclaimer

ZZ1

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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