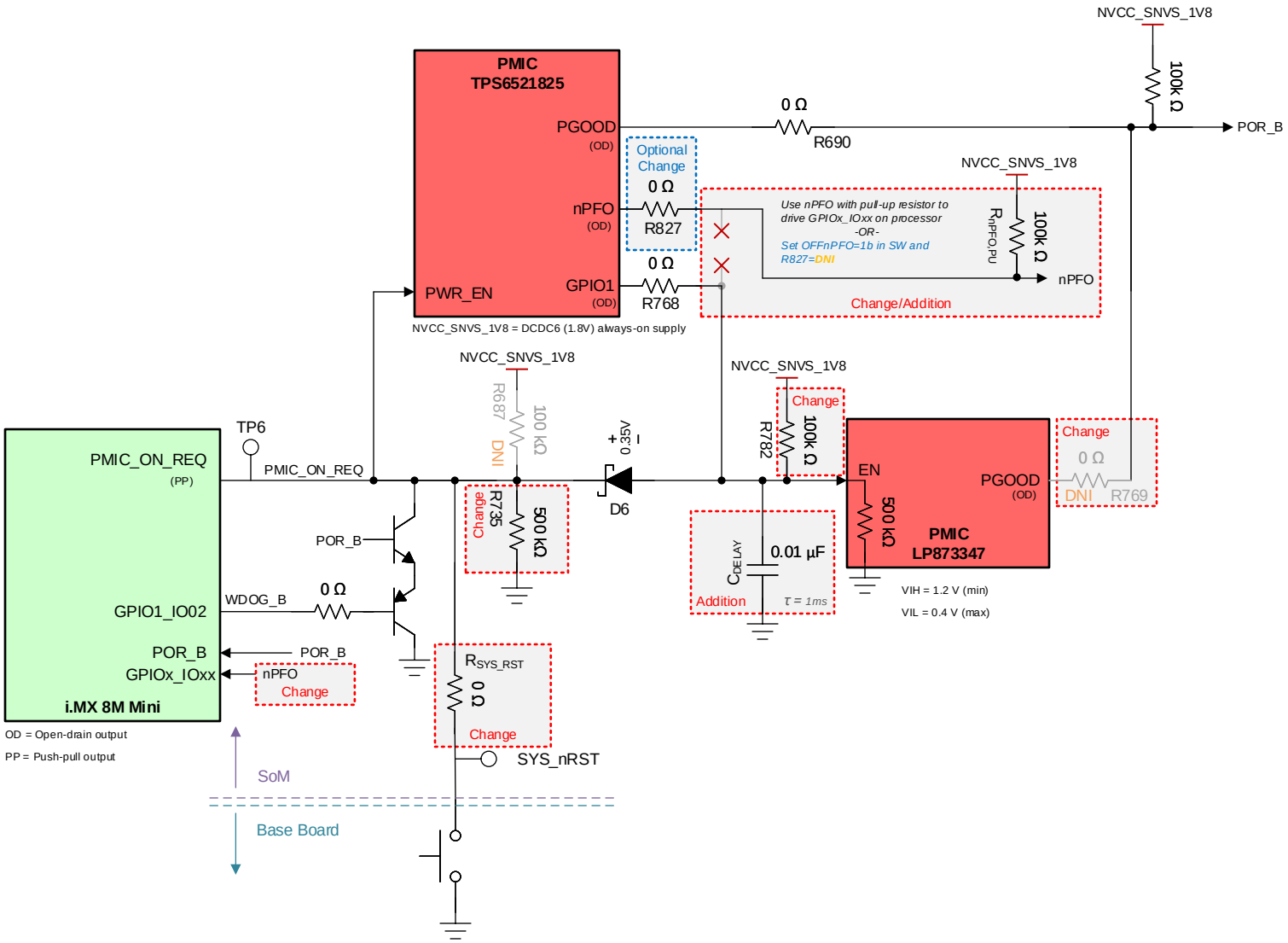
NVCC_SNVS_1V8
100k Ω
PMIC
TPS6521825
PGOOD
(OD)
0 Ω
R690
POR_B
Optional
Change
0 Ω
nPFO
(OD)
R827
NVCC_SNVS_1V8
Use nPFO with pull-up resistor to
drive GPIOx_IOxx on processor
-OR-
Set OFFnPFO=1b in SW and
R827=DNI
RnPFO,PU
100k Ω
nPFO
0 Ω
GPIO1
(OD)
R768
PWR_EN
Change/Addition
NVCC_SNVS_1V8 = DCDC6 (1.8V) always-on supply
NVCC_SNVS_1V8
NVCC_SNVS_1V8
R687
DNI
100 kΩ
Change
R782
100k Ω
TP6
PMIC_ON_REQ
(PP)
PMIC_ON_REQ
+
0.35V
−
D6
EN
PGOOD
(OD)
Change
0 Ω
DNI
R769
POR_B
Change
R735
500 kΩ
500 kΩ
PMIC
LP873347
CDELAY
0.01 µF
Addition
τ = 1ms
VIH = 1.2 V (min)
VIL = 0.4 V (max)
WDOG_B
0 Ω
GPIO1_IO02
POR_B
POR_B
GPIOx_IOxx
nPFO
Change
RSYS_RST
0 Ω
Change
i.MX 8M Mini
OD = Open-drain output
PP = Push-pull output
SYS_nRST
SoM
Base Board