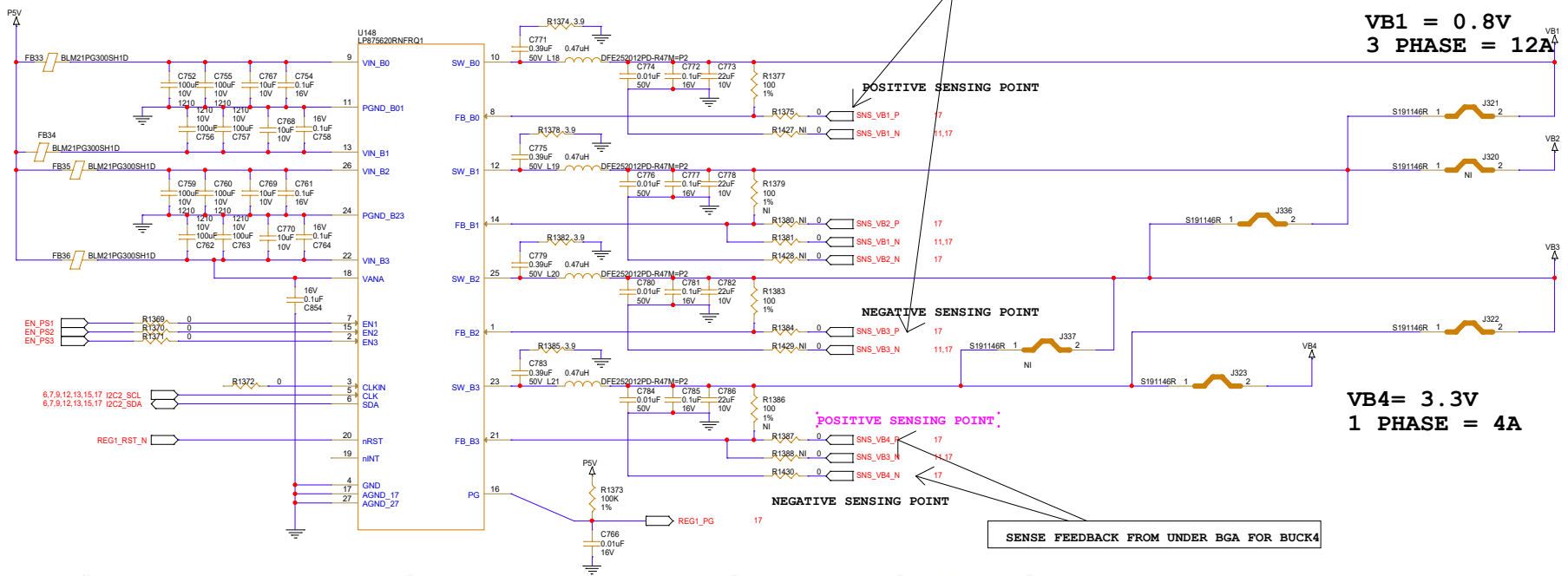
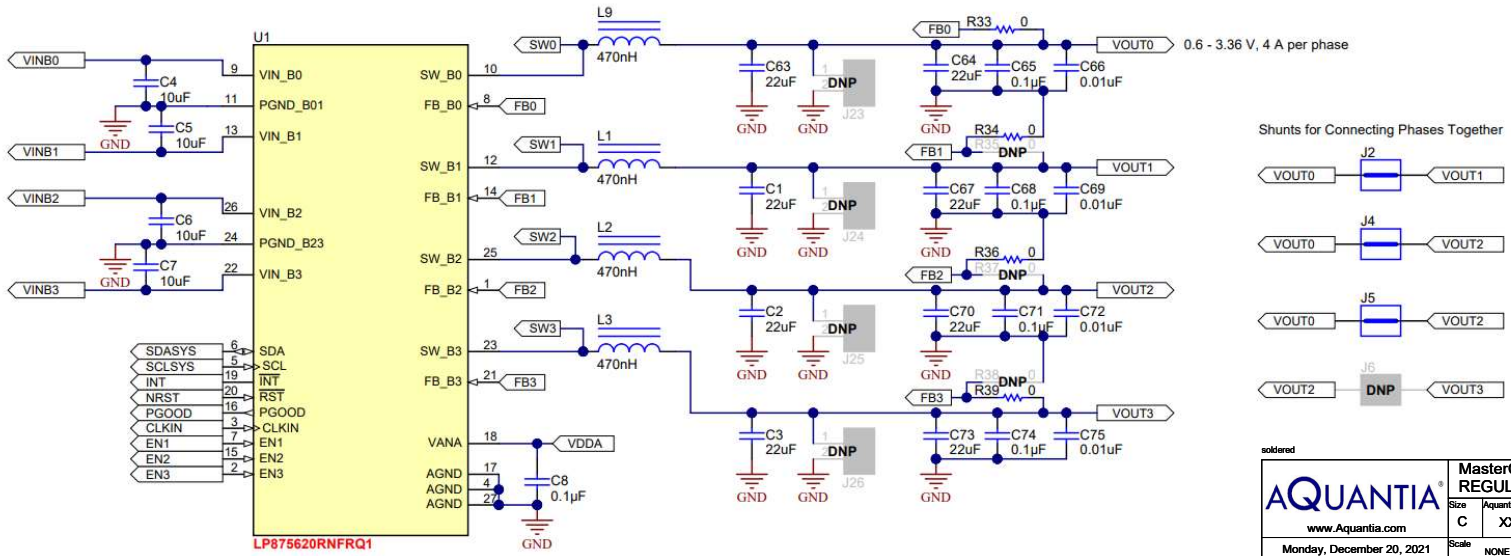


NI = NOT INSTILLED

DEVICE ID: LP875620 --> 3+1 PHASE
I2C ADDRESS : 0X63



Description	Bit Name	LP8756x-Q1	Configurable
I ² C slave ID (7-bit)	LP875610 - 4 ph	0x62	No
	LP875620 - 3+1	0x63	
	LP875630 - 2+1+1	0x64	
	LP875640 - 1+1+1+1	0x65	
	LP875650 - 2+2	0x66	



MasterCard Design Validation Board
REGULATOR3

AQUANTIA

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Monday, December 20, 2021

Size C

Aquantia Drawing # XXXX-2-A1

Scale NONE

Drawing # XXXX-2-A1

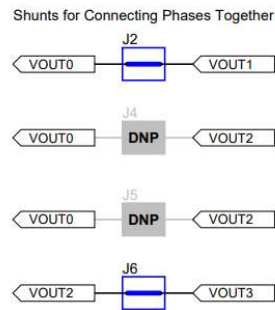
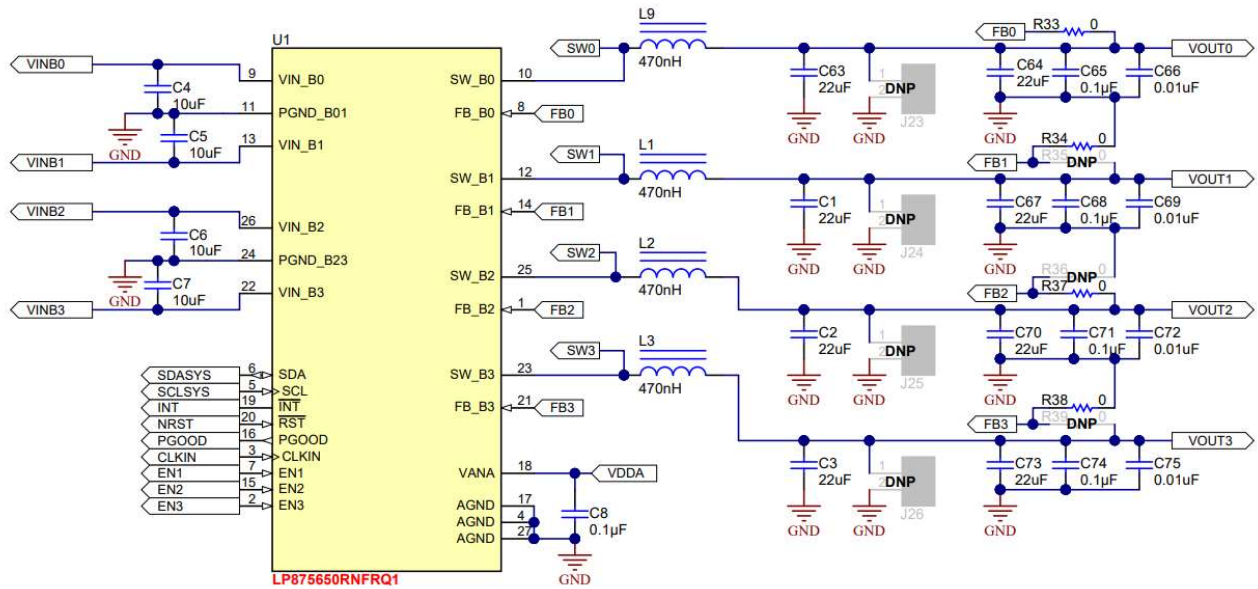
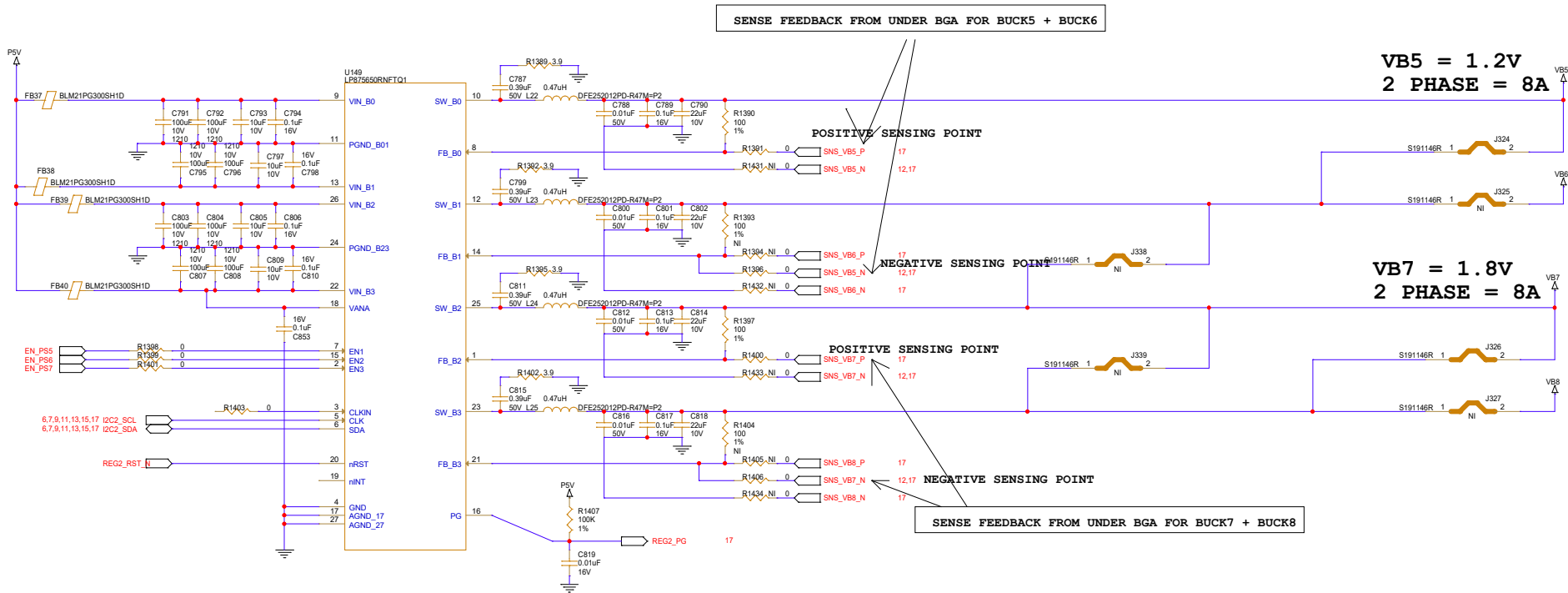
Confidential

Rev A1

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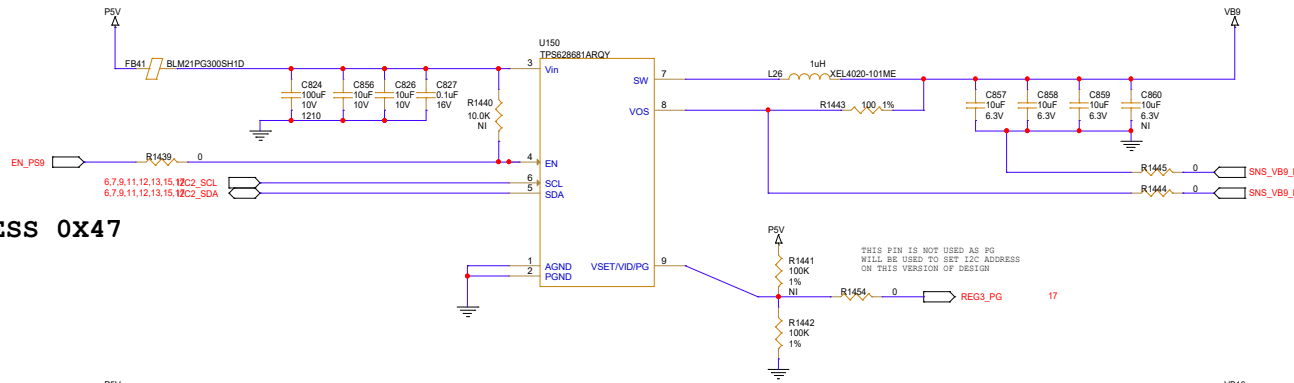
NI = NOT INSTALLED

DEVICE ID: LP875650 --> 2+2 PHASE
I2C ADDRESS : 0X66



soldered		MasterCard Design Validation Board	
AQUANTIA		REGULATOR3	
Size	C	Aquantia Drawing #	XXXX-2-A1
		Drawing #	XXXX-2-A1
Monday, December 20, 2021	Scale	NONE	Confidential
			Sheet 12 of 17

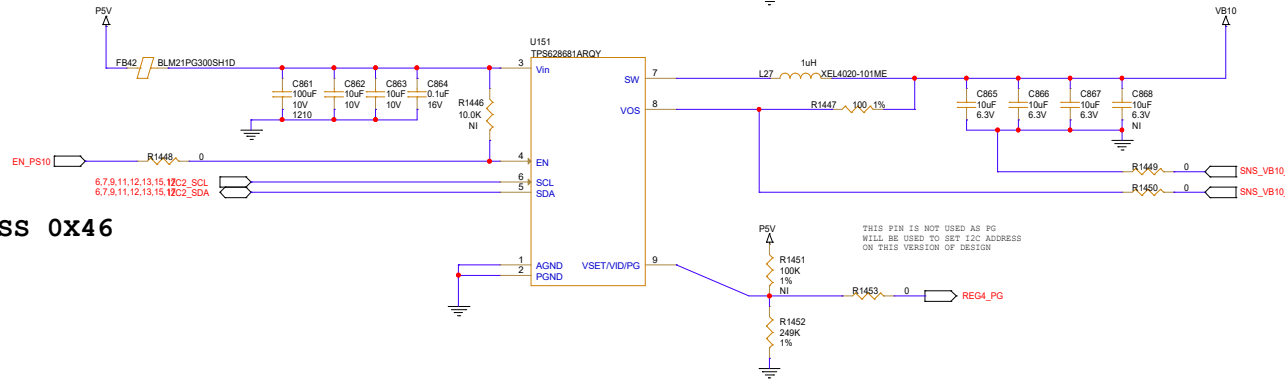
NI = NOT INSTALLED



VB9 = 0.8V

SENSE FEEDBACK FROM UNDER BGA FOR BUCK9

I2C ADDRESS 0X47



VB10 = 1.2V

SENSE FEEDBACK FROM UNDER BGA FOR BUCK10

I2C ADDRESS 0X46

Table 8-1. Start-up Output Voltage and I²C Target Address Options

RESISTOR (E96 SERIES, ±1% ACCURACY) AT VSET/VID	START-UP OUTPUT VOLTAGE (TYP)	I ² C TARGET ADDRESS
249 kΩ	Voltage Factor * 1.15 V	0b1000110 (0x46)
205 kΩ	Voltage Factor * 1.10 V	0b1000101 (0x45)
162 kΩ	Voltage Factor * 1.05 V	0b1000100 (0x44)
133 kΩ	Voltage Factor * 1.00 V	0b1000011 (0x43)
105 kΩ	Voltage Factor * 0.95 V	0b1000010 (0x42)
86.6 kΩ	Voltage Factor * 0.90 V	0b1000001 (0x41)
68.1 kΩ	Voltage Factor * 0.85 V	0b1001000 (0x48)
56.2 kΩ	Voltage Factor * 0.80 V	0b1001001 (0x49)
44.2 kΩ	Voltage Factor * 0.75 V	0b1001010 (0x4A)
36.5 kΩ	Voltage Factor * 0.70 V	0b1001011 (0x4B)
28.7 kΩ	Voltage Factor * 0.65 V	0b1001100 (0x4C)
23.7 kΩ	Voltage Factor * 0.60 V	0b1001101 (0x4D)
18.7 kΩ	Voltage Factor * 0.55 V	0b1001110 (0x4E)
15.4 kΩ	Voltage Factor * 0.50 V	0b1001111 (0x4F)
12.1 kΩ	Voltage Factor * 0.45 V	0b1000000 (0x40)
10 kΩ	Voltage Factor * 0.40 V	0b1000111 (0x47)

