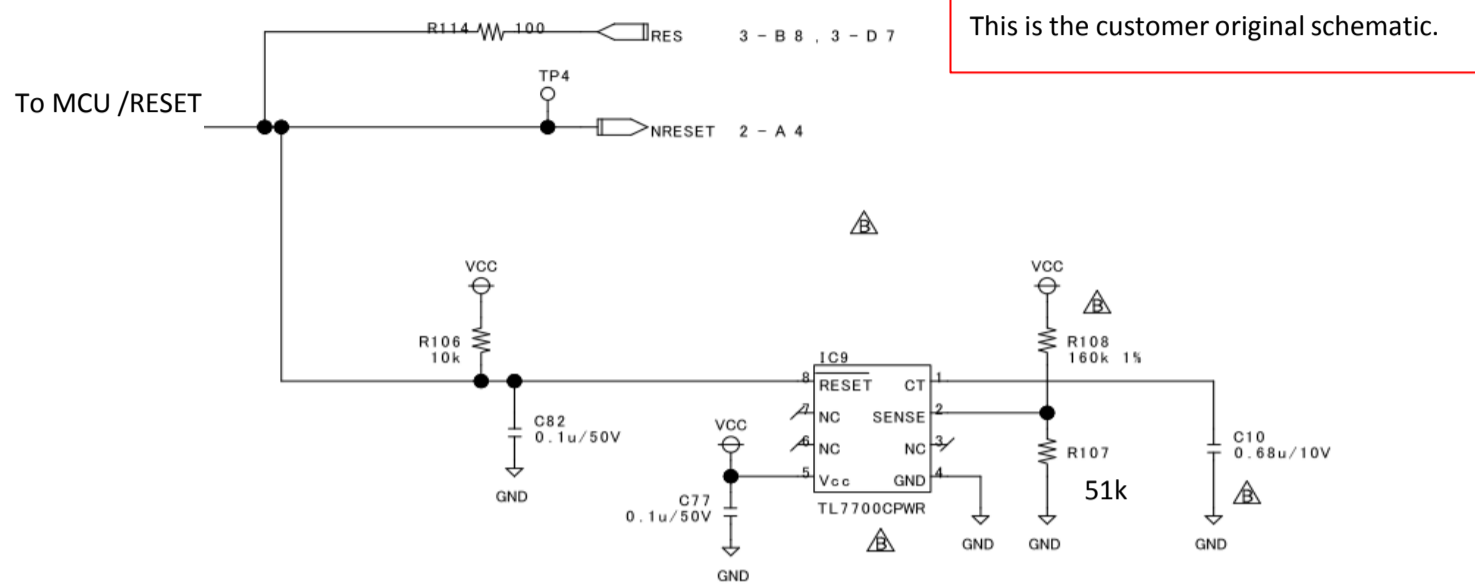


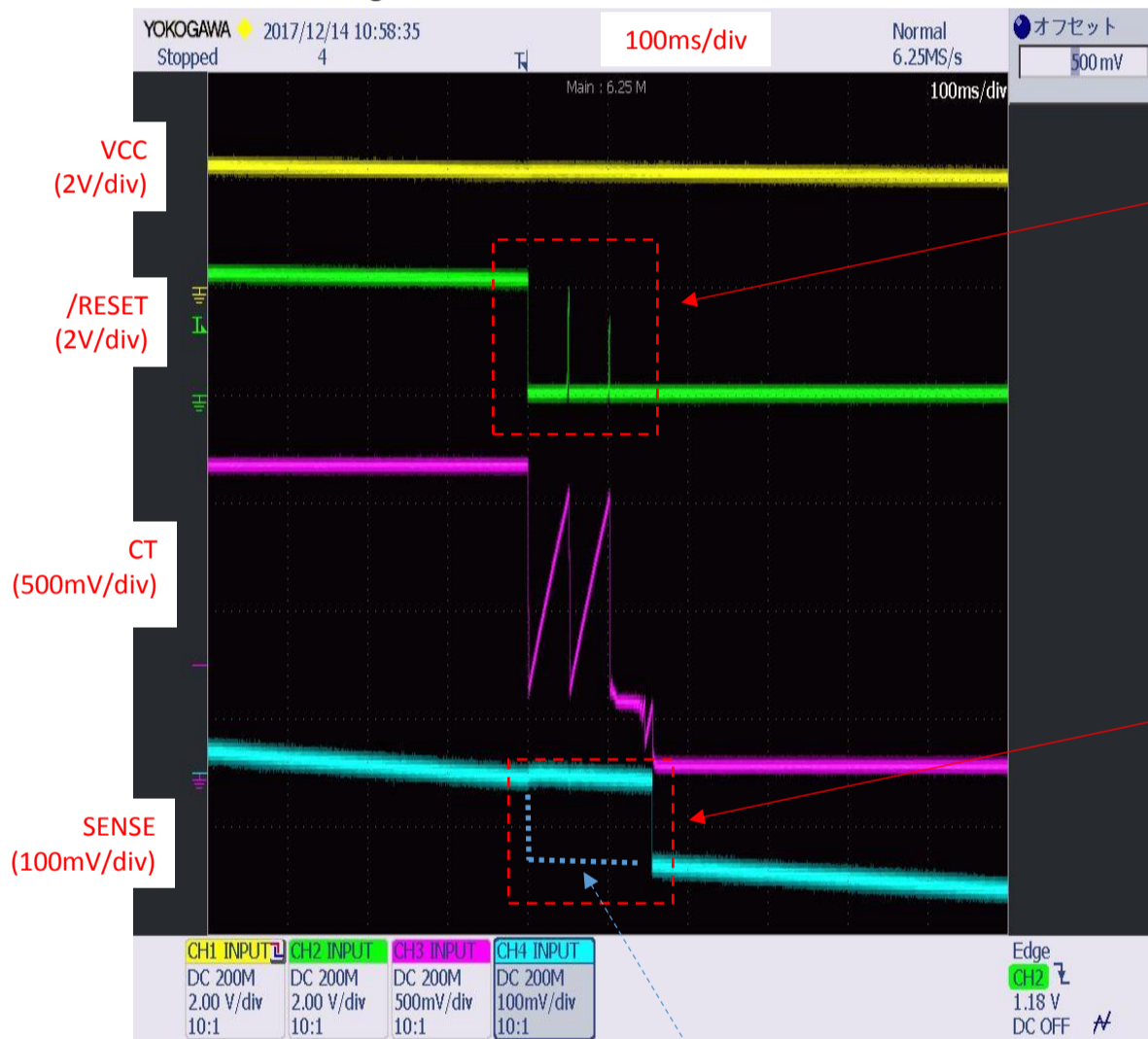
TL7700 RESET signal H/L chattering problem

Kazuya Nakai 12/15/2017

Customer Schematic related to TL7700



TL7700 RESET signal waveform

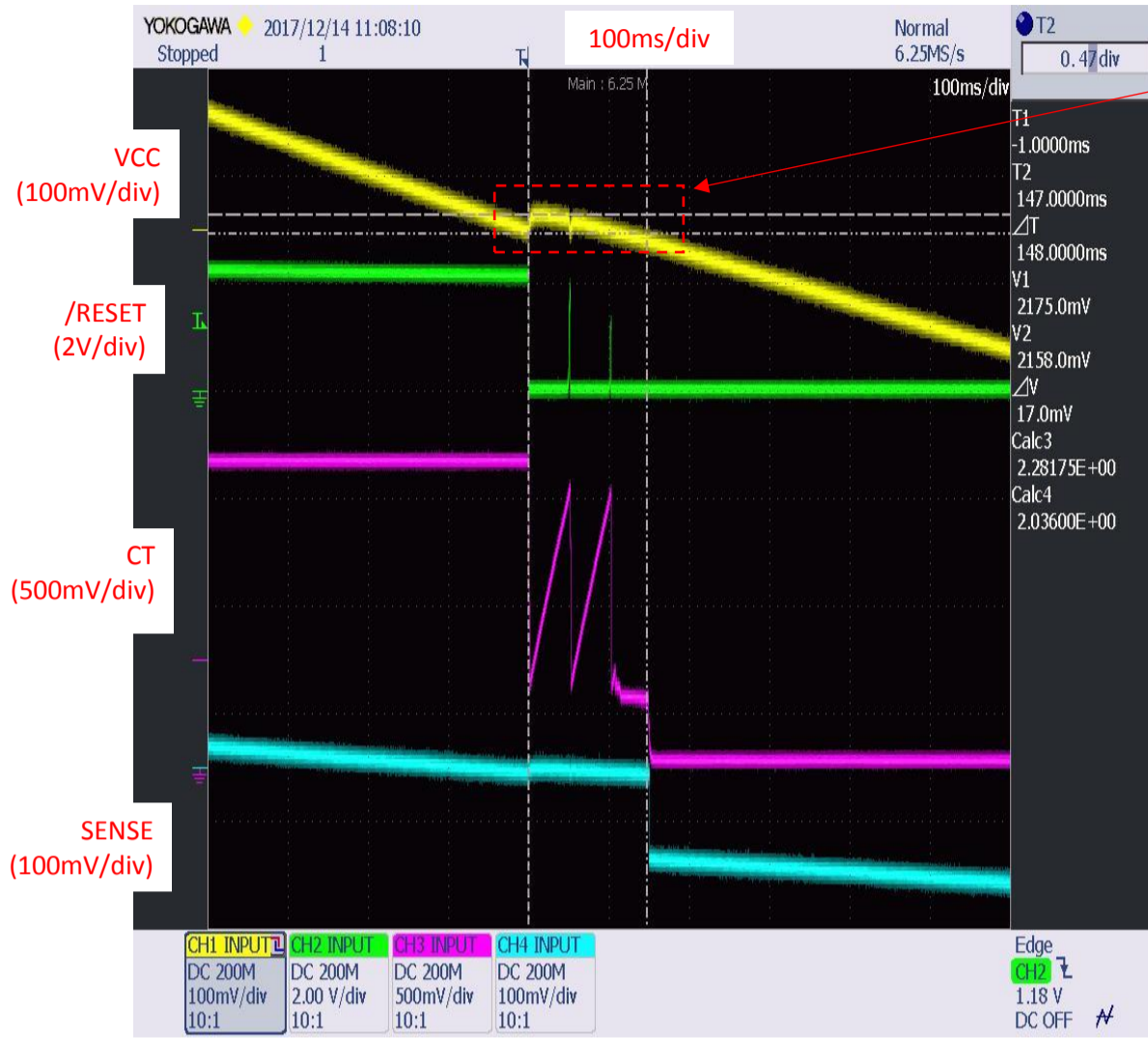


The problem is that TL7700 /RESET signal is repeated H level and L level a few times when VCC goes down across threshold level.

Question 1: Why SENSE pin voltage is not shift down immediately by the internal constant current source when /RESET is goes L level?

Expected voltage of SENSE pin.

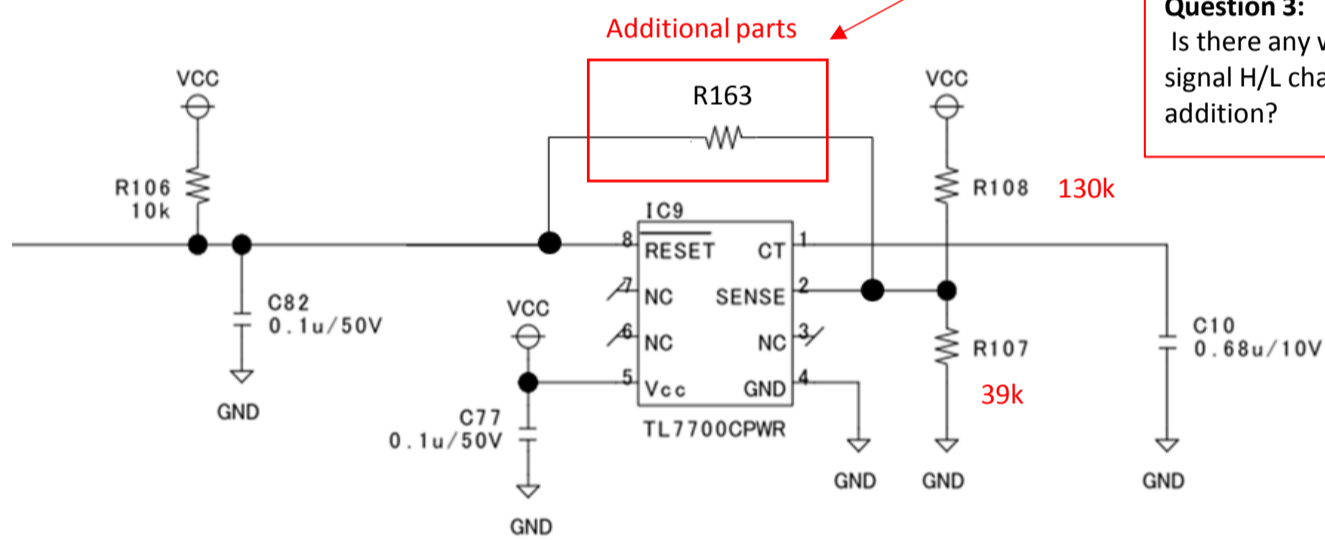
TL7700 RESET signal waveform(VCC scale magnified)



VCC voltage goes up a little bit when /RESET is goes L level because MCU consumption current becomes very low by /RESET low level.

Question 2:
Our customer added this resistor (R163) to avoid /RESET H/L chattering and as the result, /RESET H/L chattering problem was removed.
Is this countermeasure not problem for TL7700?

Countermeasure schematic against /RESET H/L chattering



Question 3:
Is there any way to avoid the RESET signal H/L chattering except R163 addition?

EOF.