

After applying to the PCBA, it was found that the main IC was unstable, which was caused by the power supply (3.3V).

According to the specifications on the product page of your website, the output voltage of the FB pin of the device TLV62569PDRRLR should be 0.6V

(±2%), i.e. 0.588V~0.612V

8.2 Typical Application

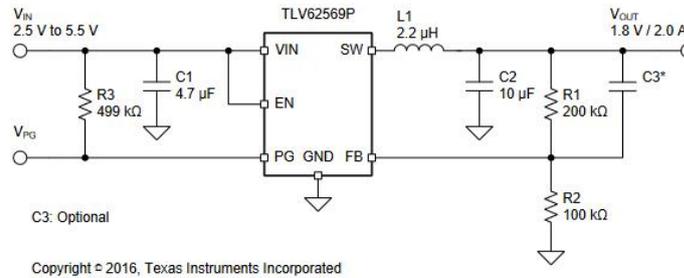


Figure 5. TLV62569 1.8-V Output Application

8.2.1 Design Requirements

For this design example, use the parameters listed in Table 2 as the input parameters.

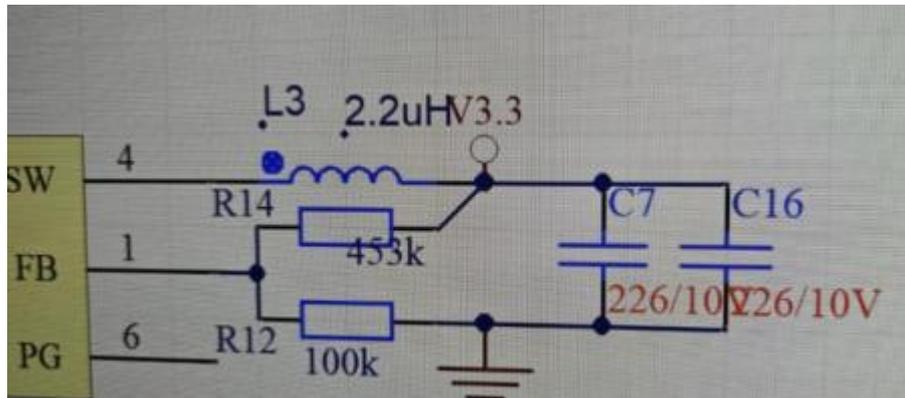
Table 2. Design Parameters

DESIGN PARAMETER	EXAMPLE VALUE
Input voltage	2.5 V to 5.5 V
Output voltage	1.8 V
Maximum output current	2.0 A

The schematic diagram of the application circuit is as follows:

According to the calculation formula, using a 453K resistance ($0.6 * (453+100) / 100 = 3.318V$), the final estimate can be obtained in principle

Output voltage (3.3V)



In this case, the final output is $(0.627 * (453+100) / 100 = 3.47V$ or so, which is beyond that of some chips

Withstand range, resulting in abnormal work.

