TPS65941213-Q1 and TPS65941111-Q1 troubleshoot

Reference:

Optimized TPS65941213-Q1 and TPS65941111-Q1 PMIC User Guide for Jacinto 7 J721E, PDN-0C

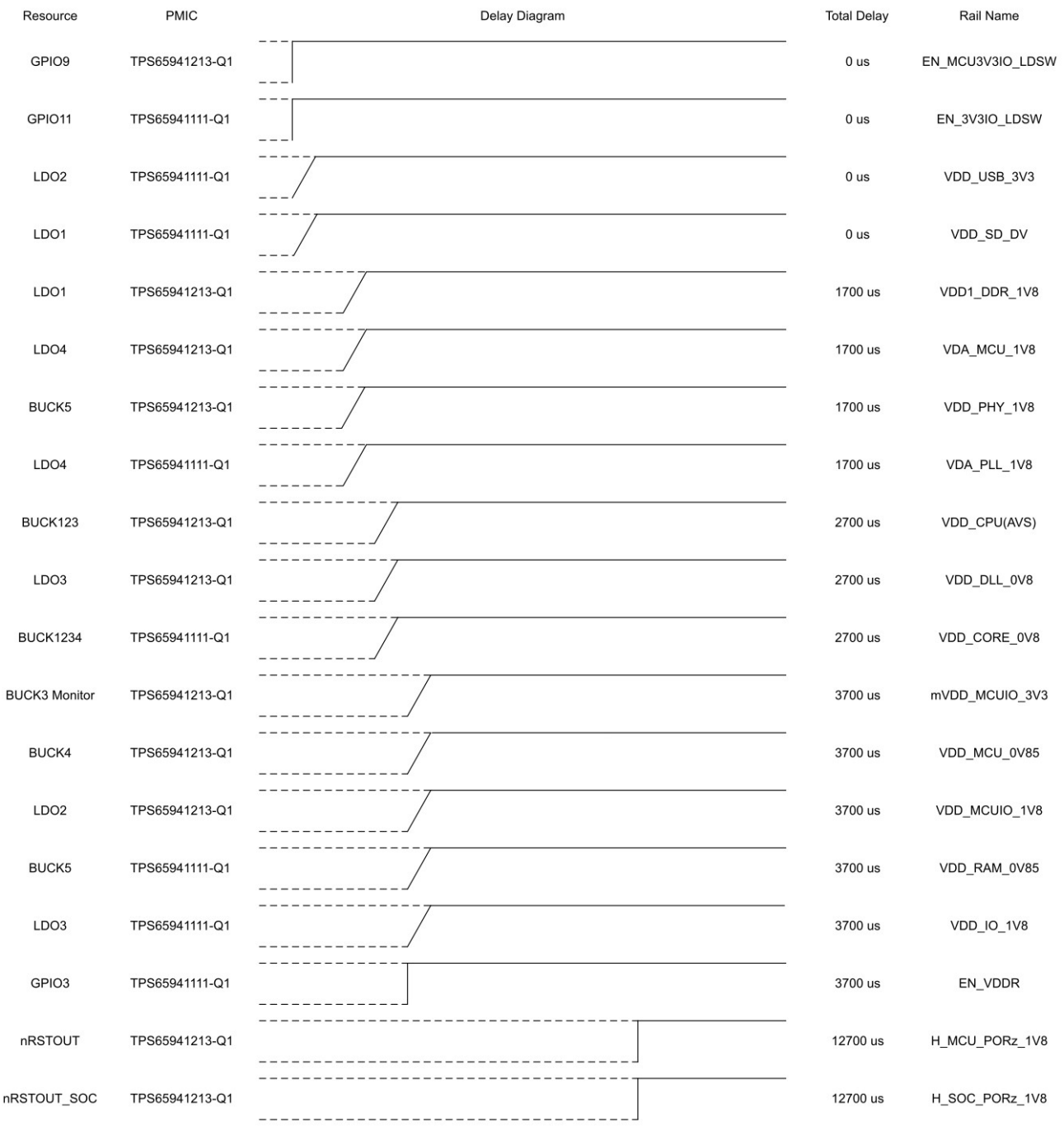


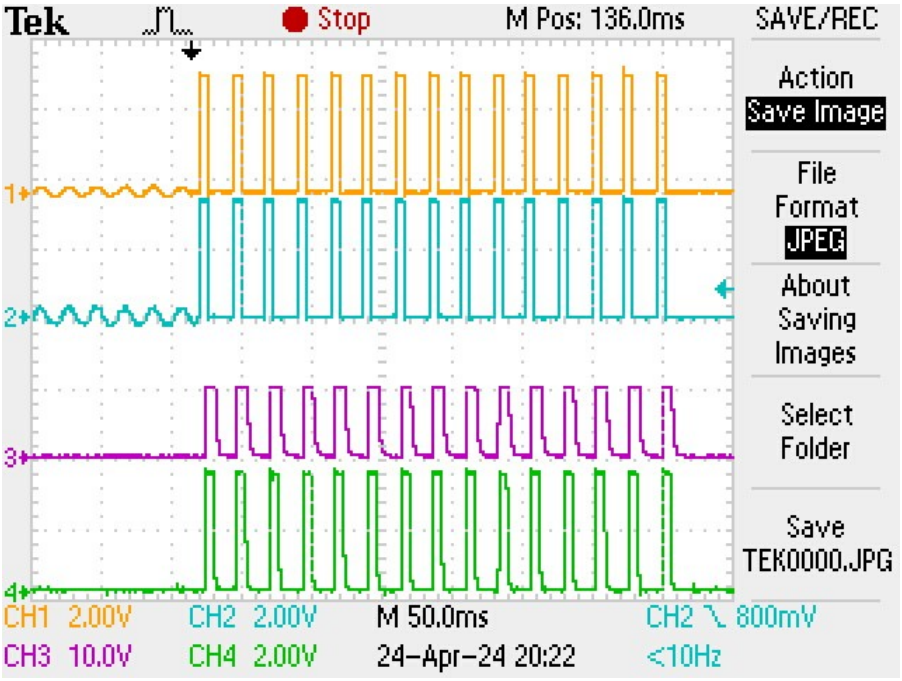
Figure 6-11. TO_ACTIVE Sequence

Test Setup:

1) The card is powered ON, PMICs enabled and observations recorded

2) No change in the circuit

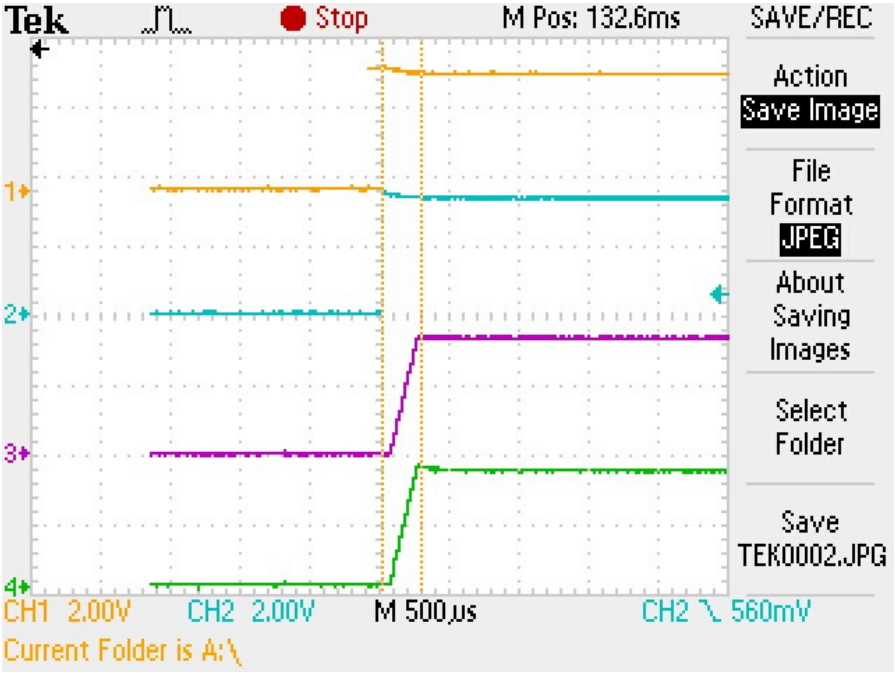
GPIO-9 retries

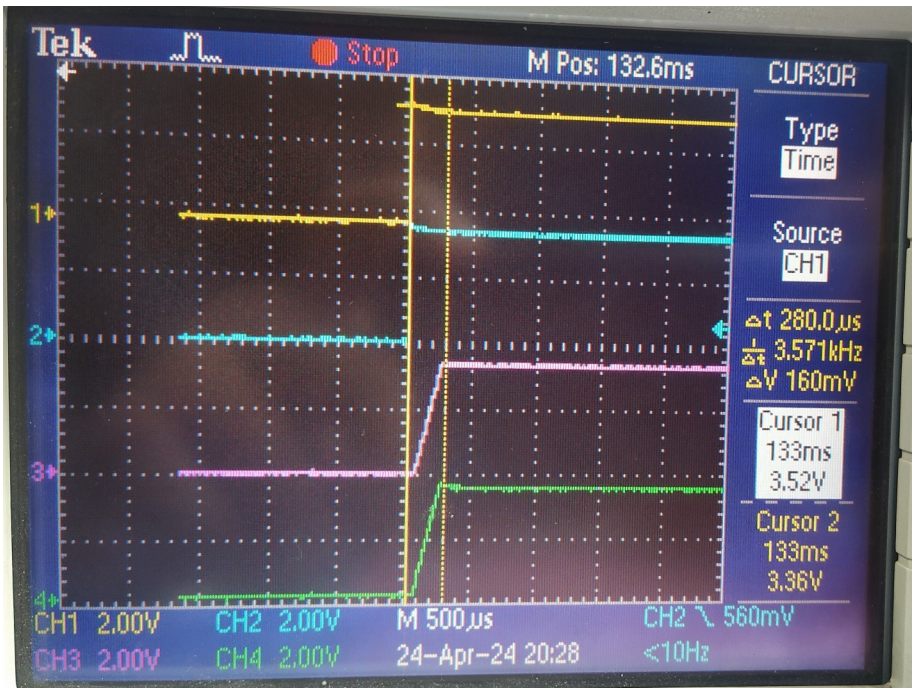


Signal 1 : GPIO-9 of TPS65941213-Q1

15 retires can be observed here

GPIO-9 and GPIO-11, LDO2, LDO1





Signal1: GPIO-9 of TPS65941213-Q1

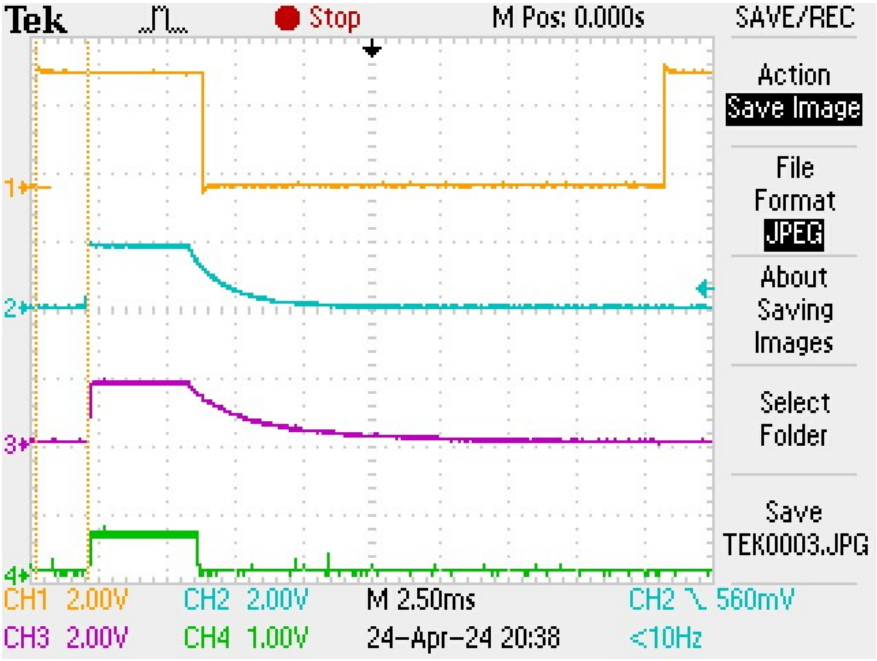
Signal 2: GPIO-11 of TPS65941111-Q1

Signal 3: LDO2 of TPS65941111-Q1

Signal 4: LDO1 of TPS65941111-Q1

All the signals start to rise at the same instant, in agreement with the TO_ACTIVE FSM

GPIO-9 and LDO1, LDO4, BUCK5



Signal1: GPIO-9 of TPS65941213-Q1

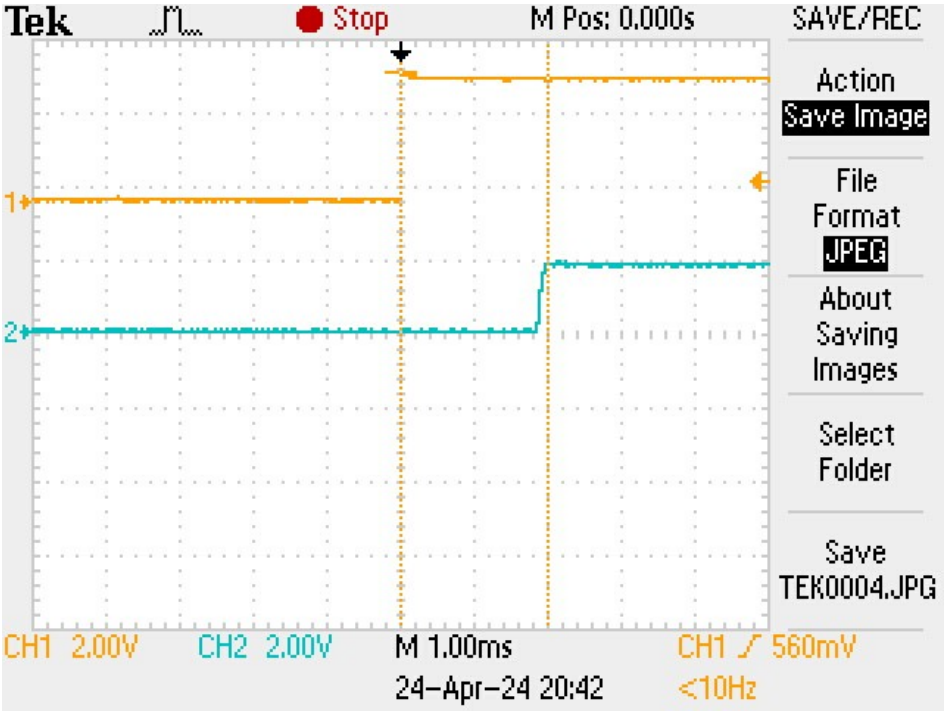
Signal 2: LDO1 of TPS65941213-Q1

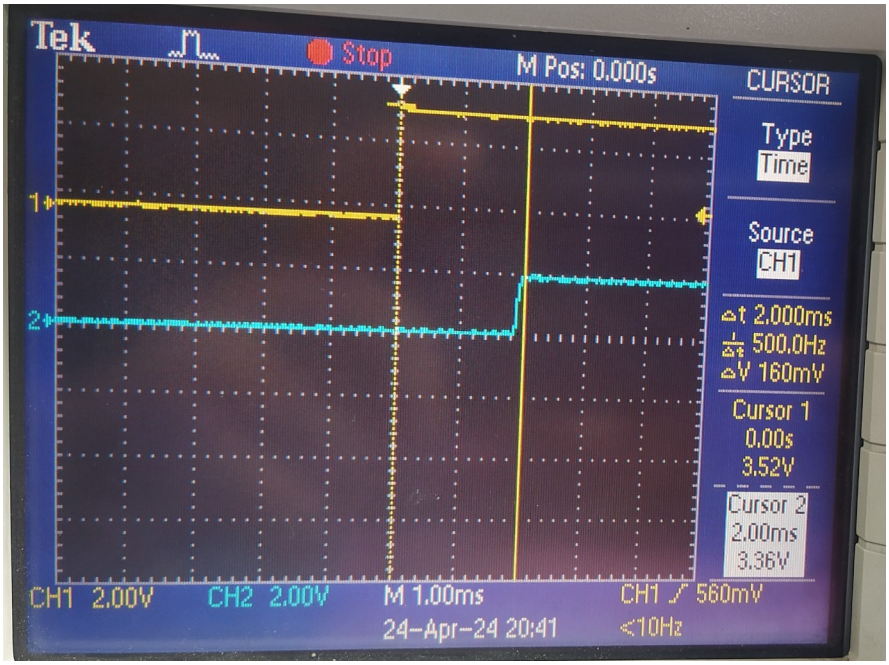
Signal 3: LDO4 of TPS65941213-Q1

Signal 4: BUCK5 of TPS65941213-Q1

Signals 2,3, and 4 begin to rise at approx 1900us after GPIO-9 in agreement with the TO_ACTIVE FSM (1700us is mentioned in the application notes)

GPIO-9 and LDO1





Signal1: GPIO-9 of TPS65941213-Q1

Signal 2: LDO4 of TPS65941111-Q1

Signal 2 begins to rise at approx 1900us after GPIO-9 in agreement with the TO_ACTIVE FSM (1700us is mentioned in the application notes)

Note: Following LDO4, the other signals in the sequence are at 0V