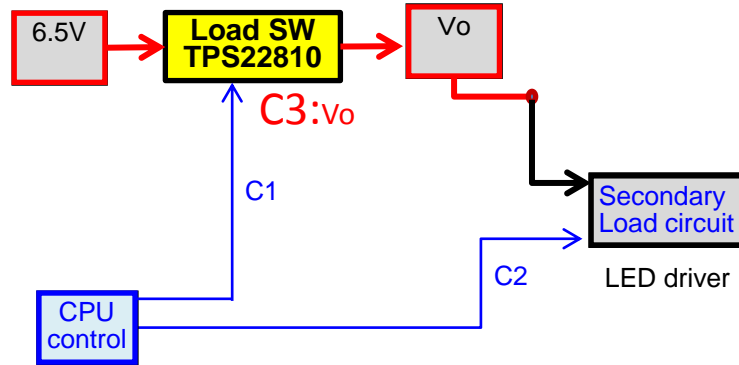


C4 input current



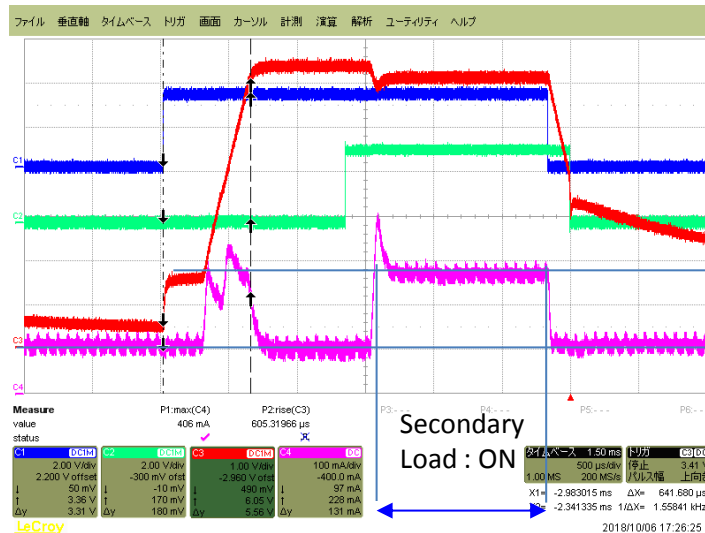
Load SW operating condition  
 $V_{in} : 6.5V$   
 $C_T = 3300pF$  (Output delay capacitor)  
 QOD pin  $\Rightarrow$  connected to VIN  
 Load capacitance  $= 44\mu F$

C1: Load SW ON

C2: Load ON

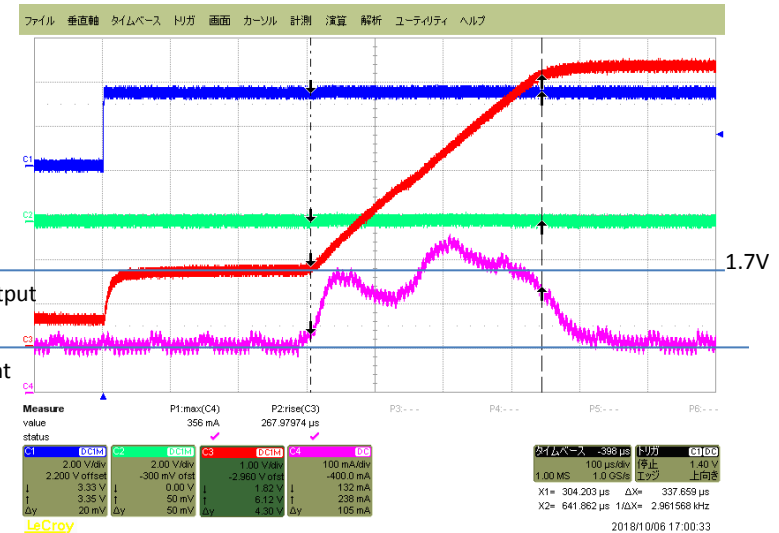
C3: Vo (Load SW output voltage)

C4: input current



C3: Load SW output voltage

C4: input current



QUESTION : Could you teach about the rising wave of C3(Vo)?

When we enabled Load SW ON with residual charge (about 0.6V) at Vo, this device started with 1.7V output, and started to rise up after 300us. Why does this 1.7V wave occur?