

TI 2546
BC1.2
YD/T 1591-2009

TI2543
BC1.2
YD/T 1591-2009

iPOD iPHONE (1A)
iPad iPHONE (2A)

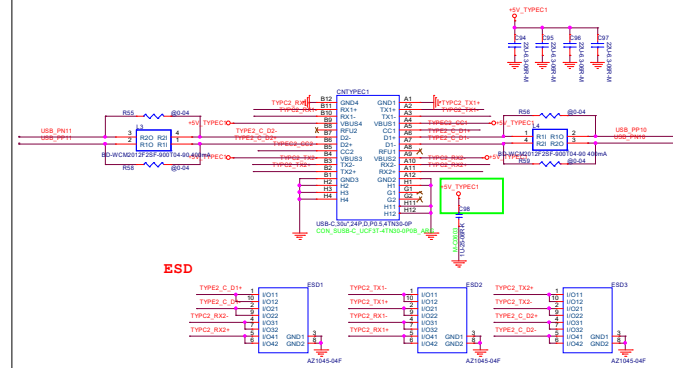
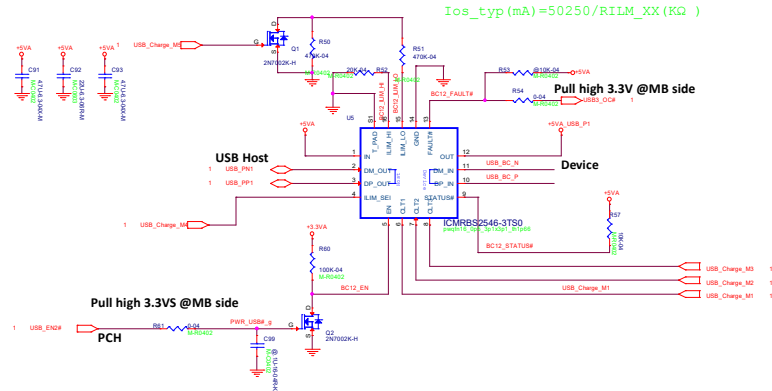
QFN 3*3

Device Control Pins				
Flow Line Condition	CTL1	CTL2	CTL3	ILIM_SEL
DCH (Discharge)	0	0	0	X
CDP	1	1	1	1
SDP2 (No Discharge from/to CDP)	1	1	1	0
SDP1 (Discharge from/to any charging state including CDP)	1	1	0	X
	0	1	0	X
DCP_SHORT	1	0	0	X
DCP_DIVIDER	1	0	1	X
	0	1	1	X
DCP_Auto	0	0	1	X

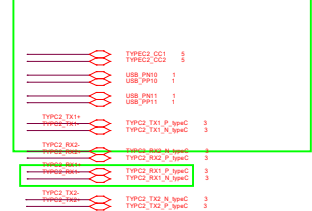
DESIGN PARAMETER	EXAMPLE VALUE
Input voltage, V_{IN}	5 V
Output voltage, V_{OUT}	5 V
Maximum continuous output current, I_{OUT}	2.5 A
Current limit, I_{LIM} at $R_{ILIM} = 1.05 \text{ k}\Omega$	0.025 A
Current limit, I_{LIM} at $R_{ILIM} = 2.1 \text{ k}\Omega$	2.01 A

Max Current

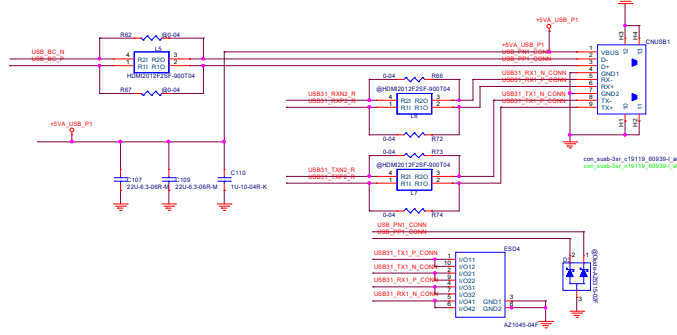
SDP USB2.0	0.5A
SDP USB3.0	0.9A
CDP	1.5A
DCP	1.5A



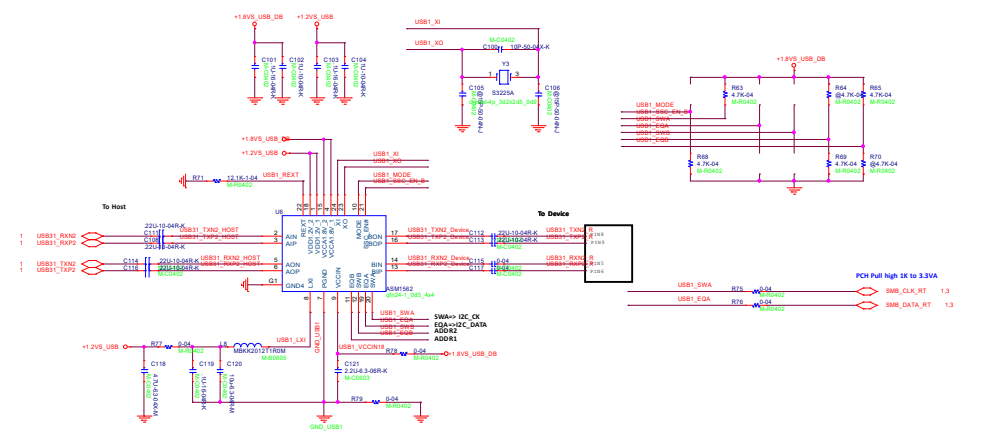
Type C Port



USB3.0 CONN.



Retimer for USB-A Gen2



Rev	USB + TypeC + Audio Jack	Rev	Rev
1	1	1	1
2	2	2	2
3	3	3	3
4	4	4	4
5	5	5	5