Liquid detection scheme

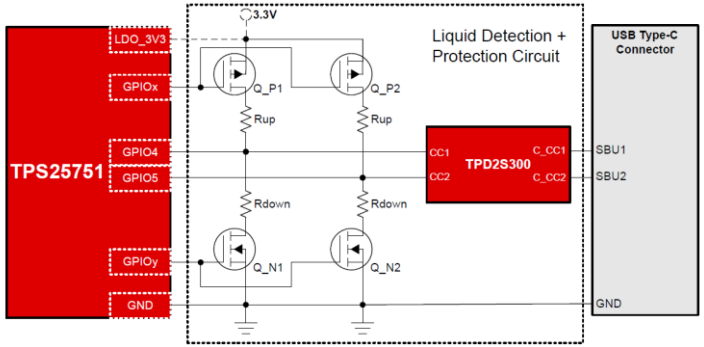

Table 3-29. Liquid Detection Config Register Field Descriptions

Bit	Field	Type	Reset	Description
82	Enable Liquid Detection	R/W	0h	Enables liquid detection on the SBU pins connected to the GPIO on the PD Controller. In order for this to function correctly the proper external liquid detection circuitry must be in place.
81	Enable Corrosion Mitigation	R/W	0h	Enable corrosion mitigation. Corrosion mitigation will disconnect the port, disabled the port, and pull down CC pins.
80	Liquid Detection State	R/W	0h	Liquid Detection State
79-76	Sample Time in 10ms Liquid	R/W	0h	Sample Time in multiples of 10ms (10ms per LSB as ms)
75-72	Sample Time in 10ms No Liquid	R/W	0h	Sample Time in multiples of 10ms (10ms per LSB as ms)
71-64	High Threshold ADC Liquid	R/W	0h	High Threshold ADC Liquid (14mV per LSB as mV)
63-56	Low Threshold ADC Liquid	R/W	0h	Low Threshold ADC Liquid (14mV per LSB as mV)
55-48	High Threshold ADC No Liquid	R/W	0h	High Threshold ADC No Liquid, provides hysteresis for exit out of Liquid Detected. (14mV per LSB as mV)
47-40	Low Threshold ADC No Liquid	R/W	0h	Low Threshold ADC No Liquid, provides hysteresis for exit out of Liquid Detected. (14mV per LSB as mV)
39-32	Number of Samples	R/W	0h	Number of samples (must be 2^N) to take average
31-16	Sleep Time In Sec Liquid	R/W	0h	Sleep in multiples of 1s when liquid is detected (1000ms per LSB as ms)
15-0	Sleep Time In Sec No Liquid	R/W	0h	Sleep in multiples of 1s when liquid is not detected. (1000ms per LSB as ms)